

# F20 ADAPTER BOARD

## TYPE1

Hybrid X8 PCIe on connector A, X4 KR on connector B

PCB REV: B

PCBA REV: B2B

FAB: 3

SCH Rev: 02

LAST UPDATE: 2015/10/20

Quanta	Department CCBU	Designer <Designer>	Project F20 Adapter Board type1	Doc Number <Doc>	Rev Av6
	Reviewer	<Reviewer>		Page Title Cover Page	

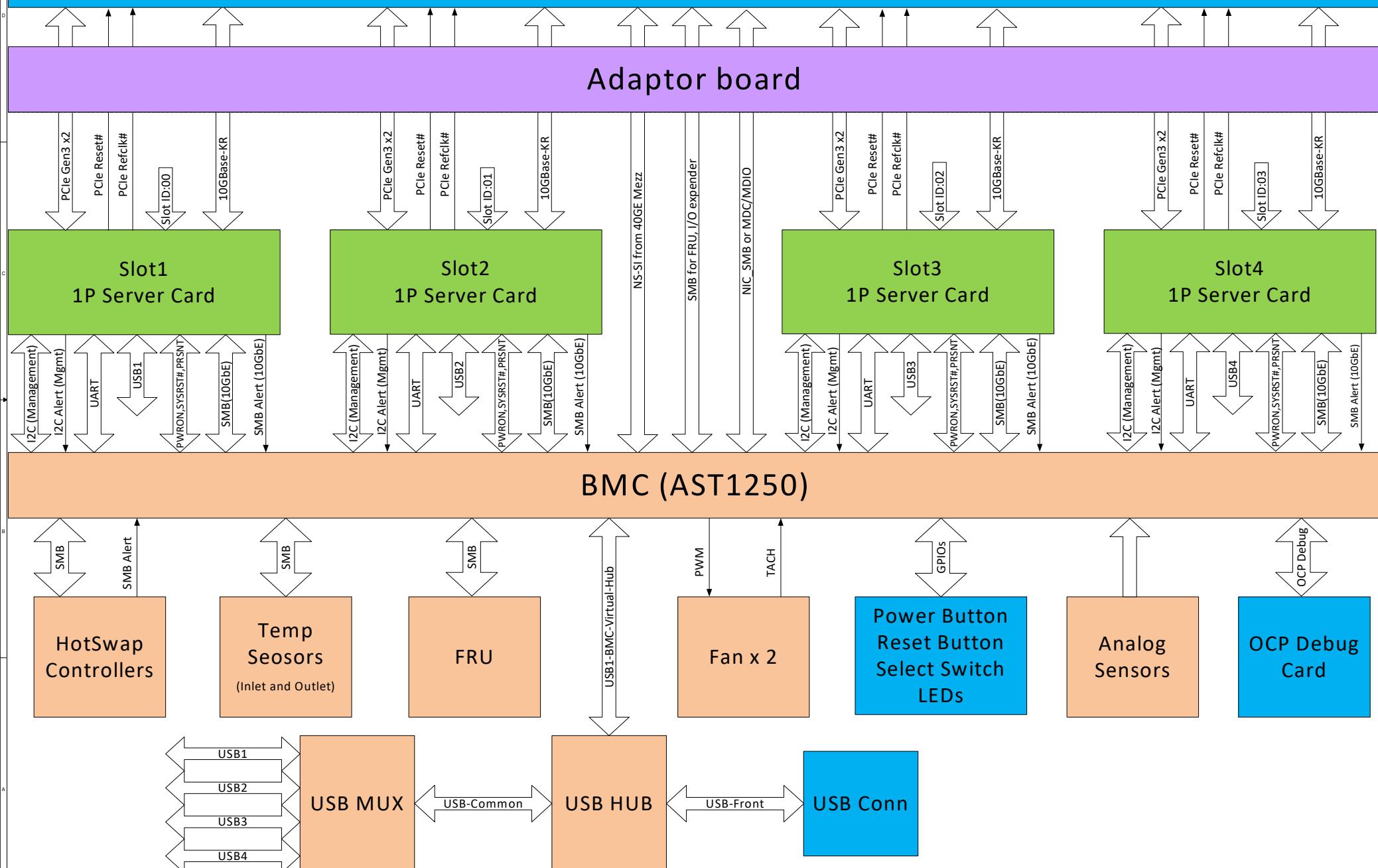
# INDEX

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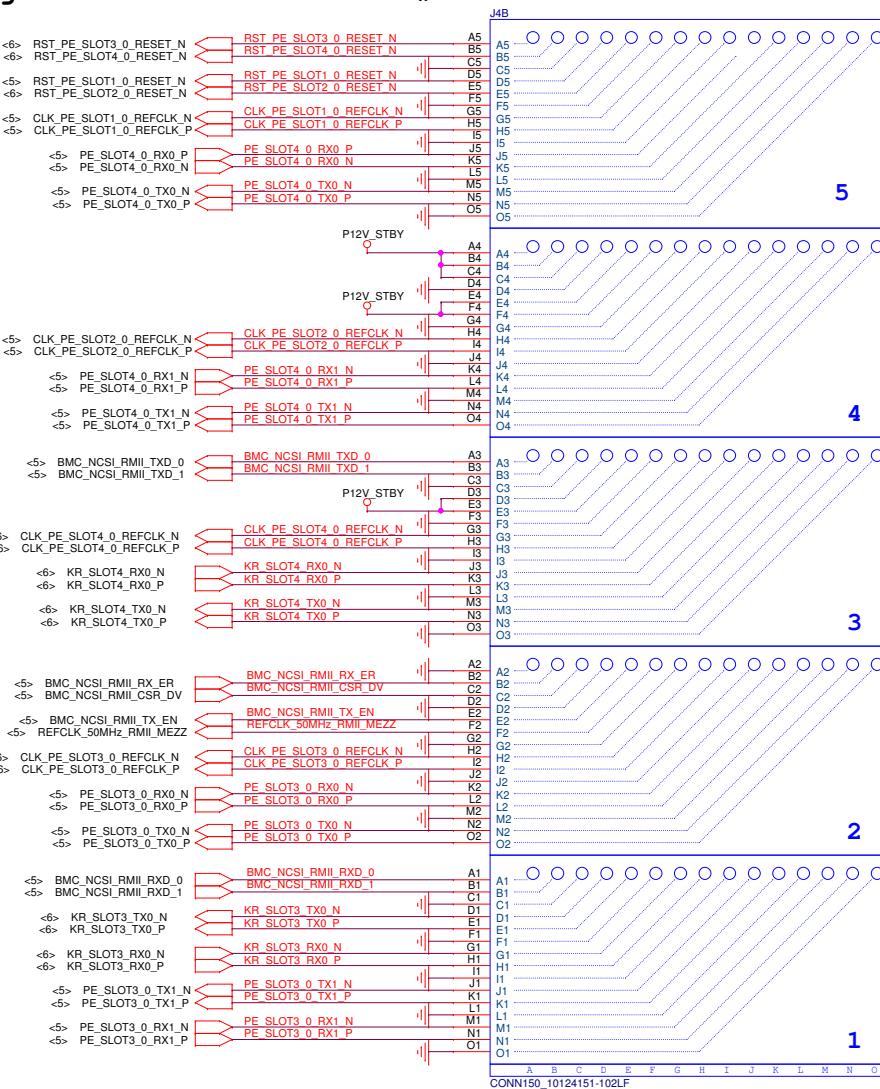
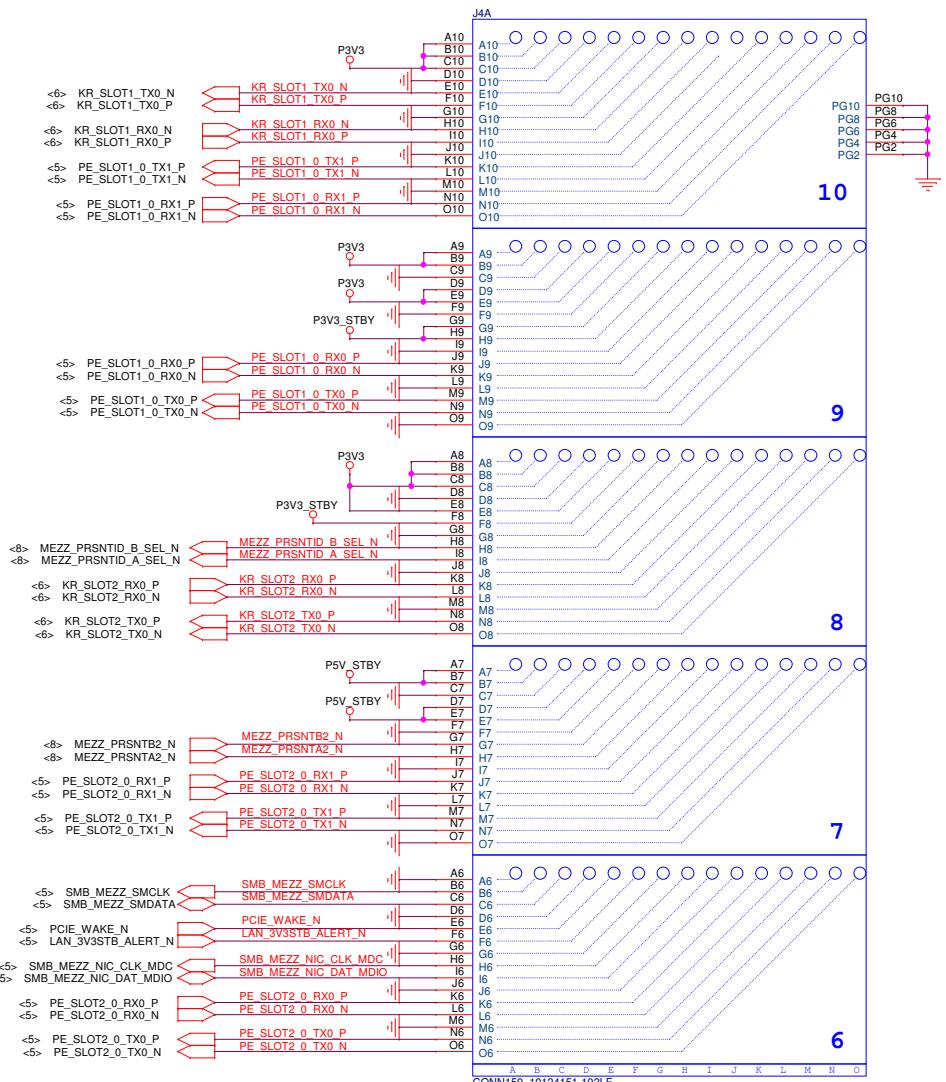
- 1 Cover Page
- 2 Page Index
- 3 Board Block Diagram
- 4 Adapter Board CONN
- 5 OCP v2.0 Mezz Conn A
- 6 OCP v2.0 Mezz Conn B
- 7 Mechanical
- 8 Baseboard ID

# Yosemite Block Diagram V0.06

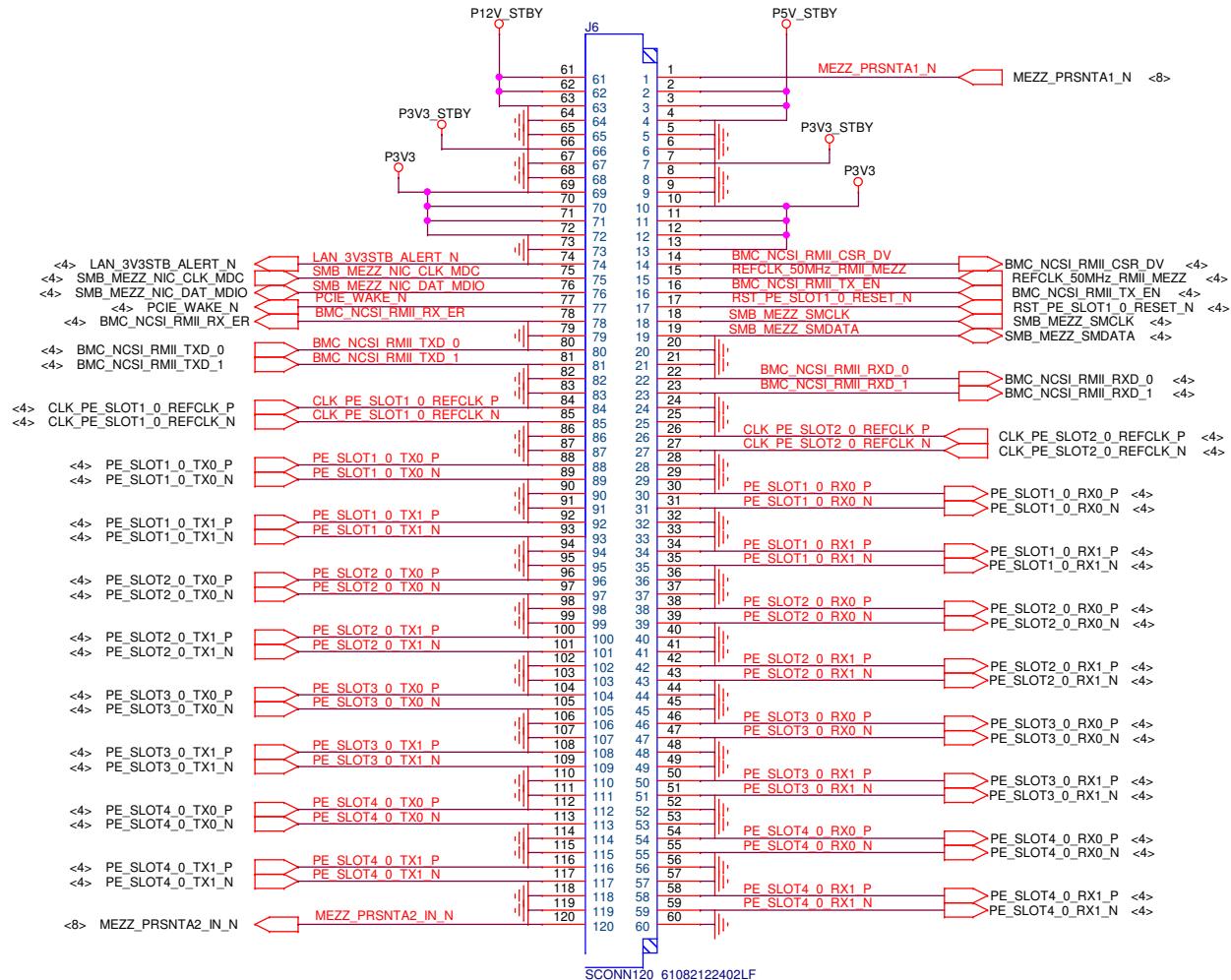
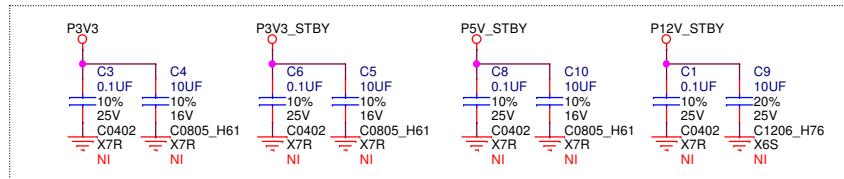
## OCP V2 40GE Mezz or 10GBASE-KR Mezz with QSFP+ Conn

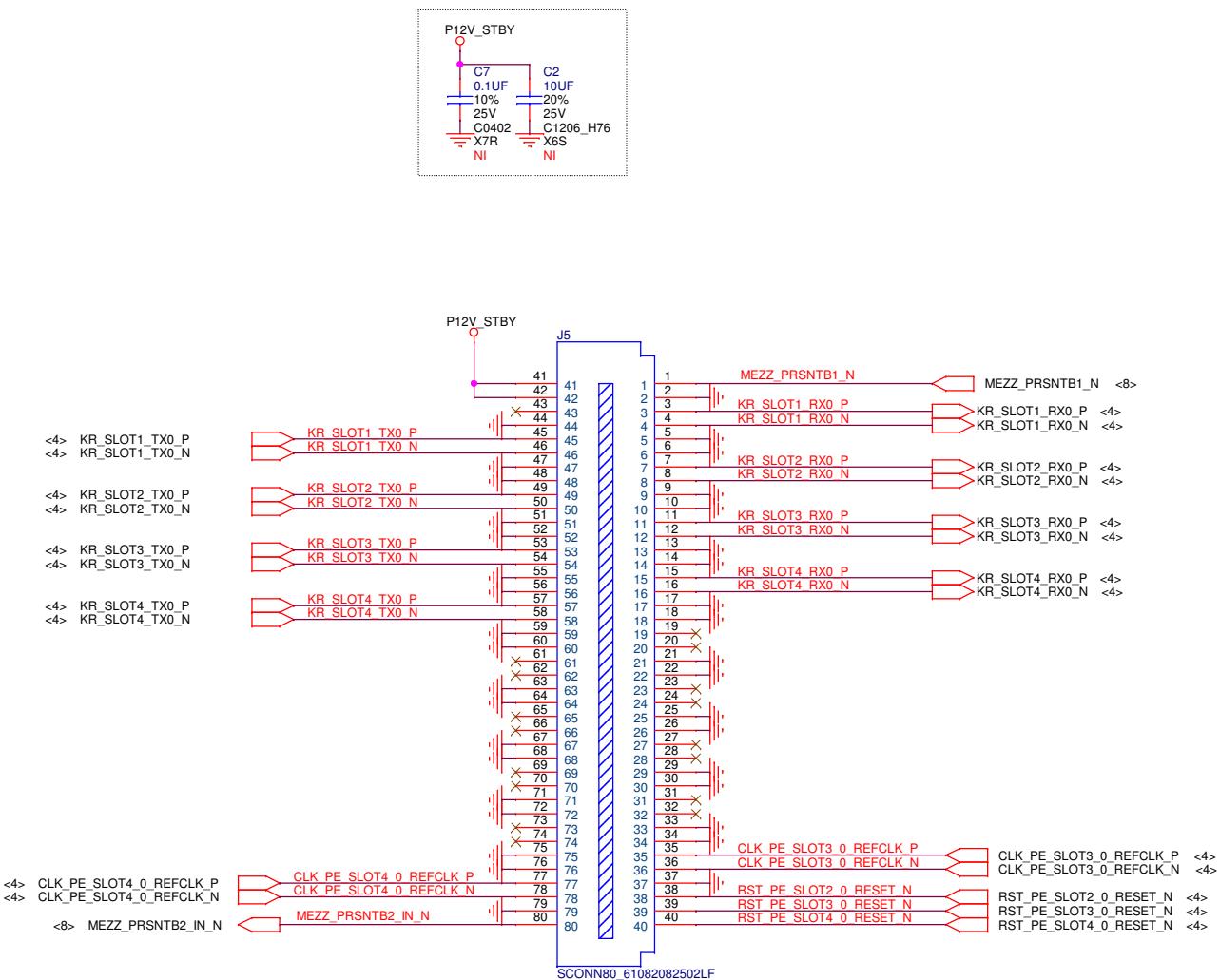


# Right angle Air Max conn: FCI# 10124151-102LF

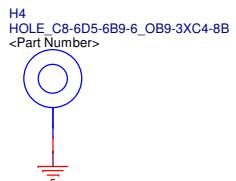


## Adapter Board CONN

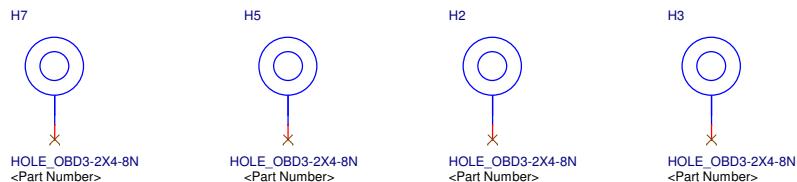




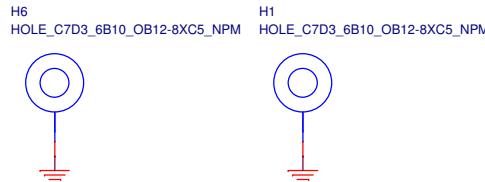
# SMT THUMB SCREW \* 1pcs



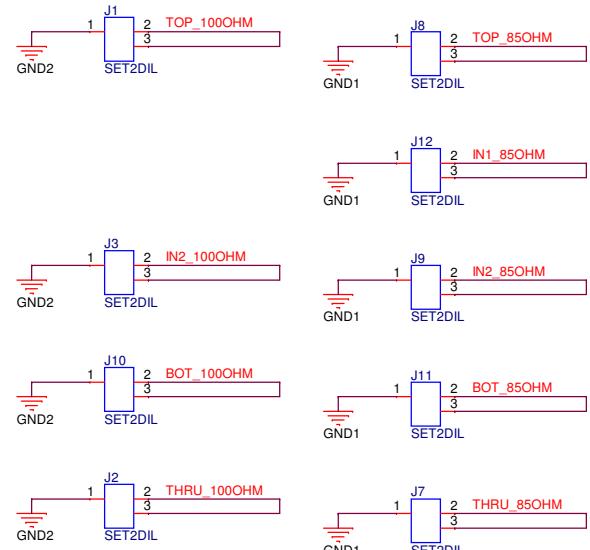
# Mezzanine holes \* 4pcs



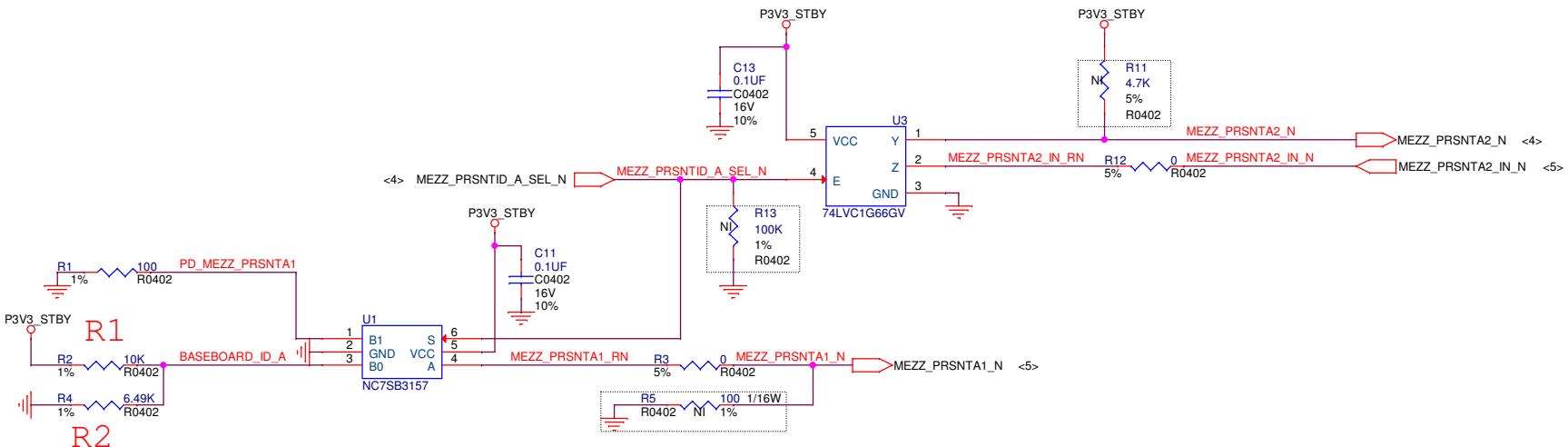
# Key Holes \* 2pcs



# SET2DIL



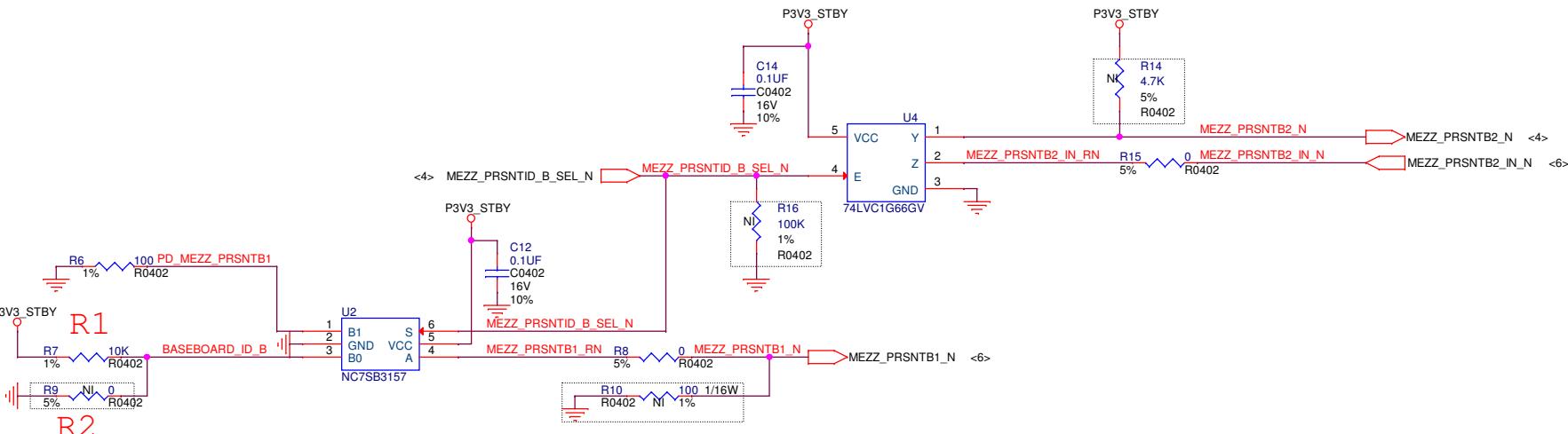
# Mechanical



PRSNTID_A_SEL_N	Function	MEZZ_PRSNTA2_N	MEZZ_PRSNTA1_N
H	B1 Connected to A	Pullup	Pulldown
L	B0 Connected to A	high-impedance	Baseboard ID

PRSNTID_B_SEL_N	Function	MEZZ_PRSNTB2_N	MEZZ_PRSNTB1_N
H	B1 Connected to A	Pullup	Pulldown
L	B0 Connected to A	high-impedance	Baseboard ID

	ConnA R1	ConnA R2	ConnB R1	ConnB R2	Base Board type
<b>Adapter type1*</b>	10kΩ	6.49kΩ	10kΩ	NC	Four x2 PCIe Root Ports on baseboard Connector A Up to 8x KR on baseboard Connector B
<b>Adapter type2</b>	10kΩ	3.83kΩ	10kΩ	3.83kΩ	Two x4 PCIe Root Ports on baseboard Connector A Two x4 PCIe Root Ports on baseboard Connector B



## Baseboard ID