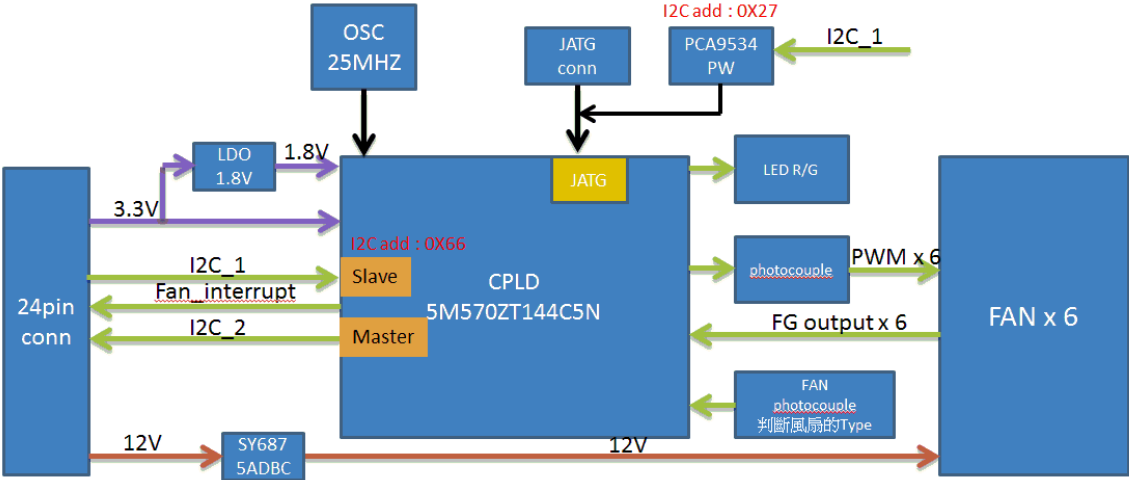


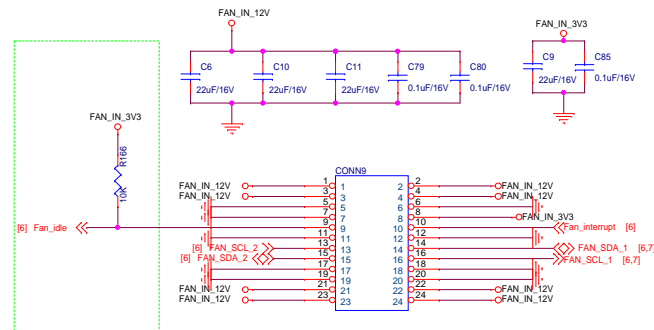
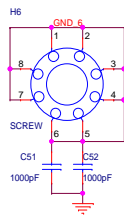
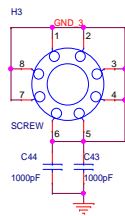
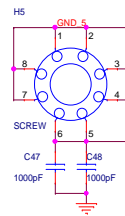
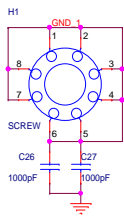
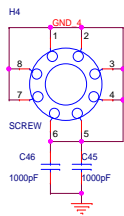
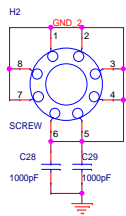
ES7632XC2-1114-168ZZ Cover Page

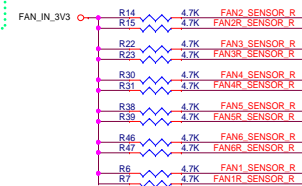
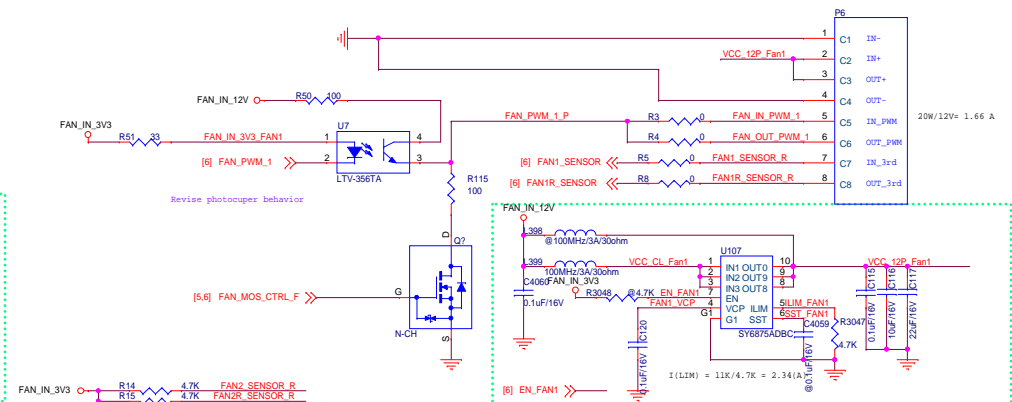
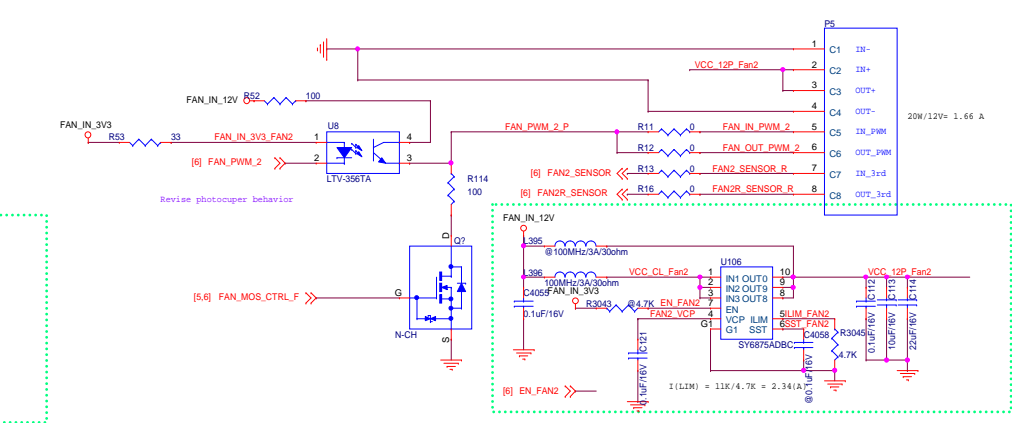
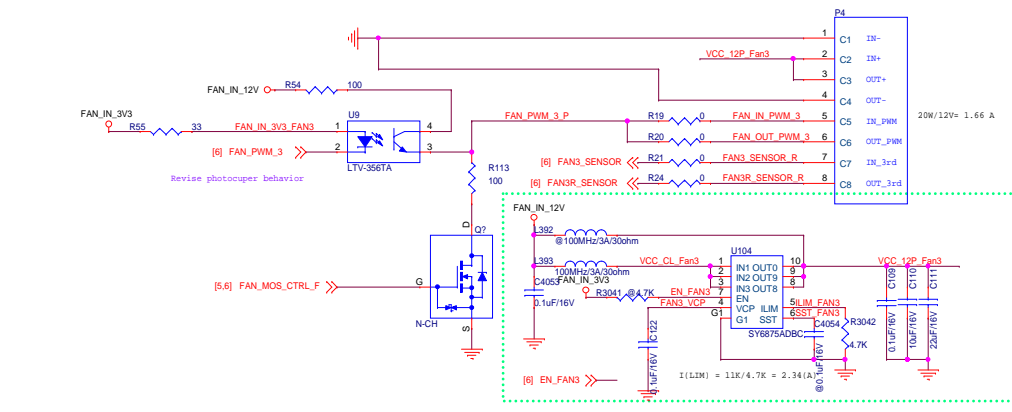
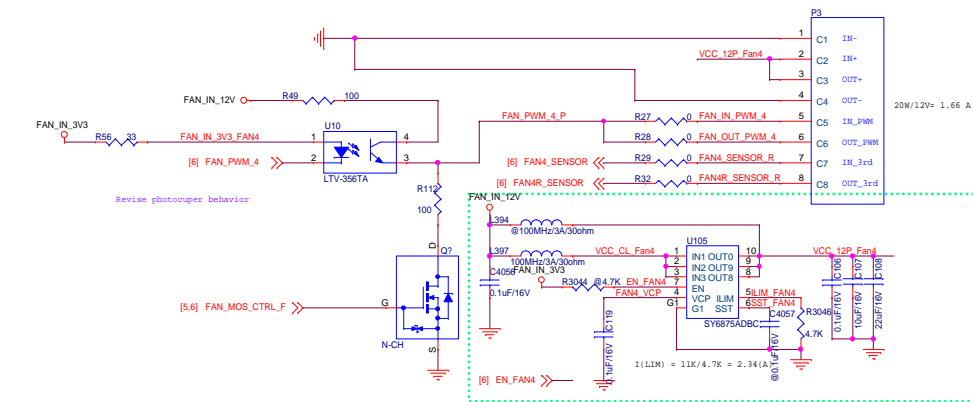
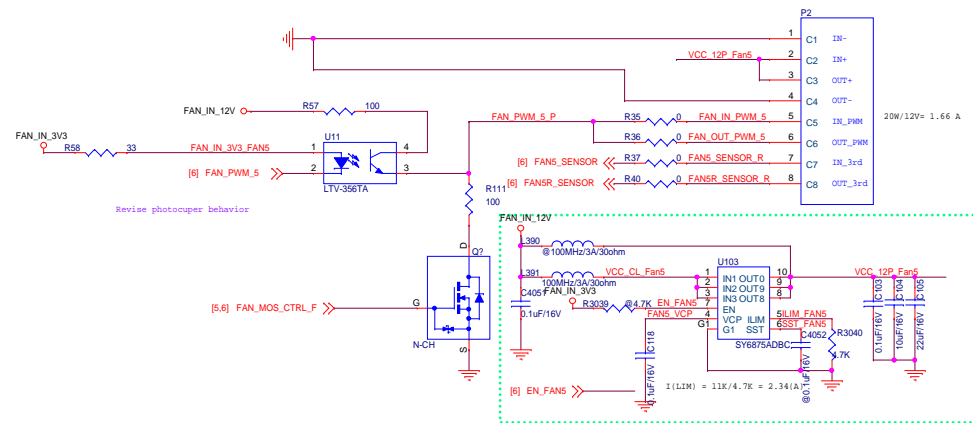
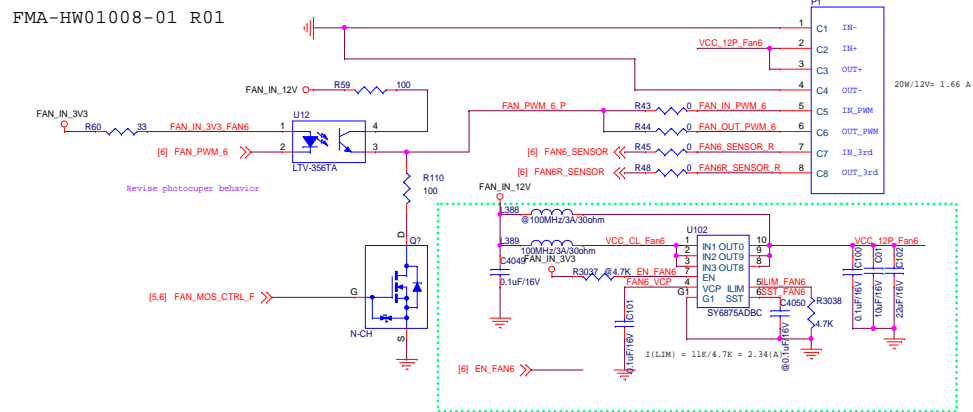
Number		Name
1	-	Cover Page
2	-	History
3	-	Block_design
4	-	FAN_CONN
5	-	FAN_CONN2
6	-	CPLD_1
7	-	CPLD_2

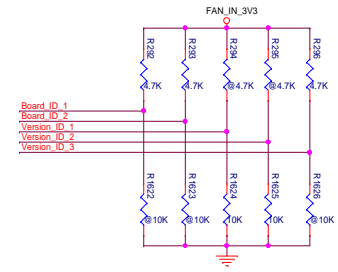
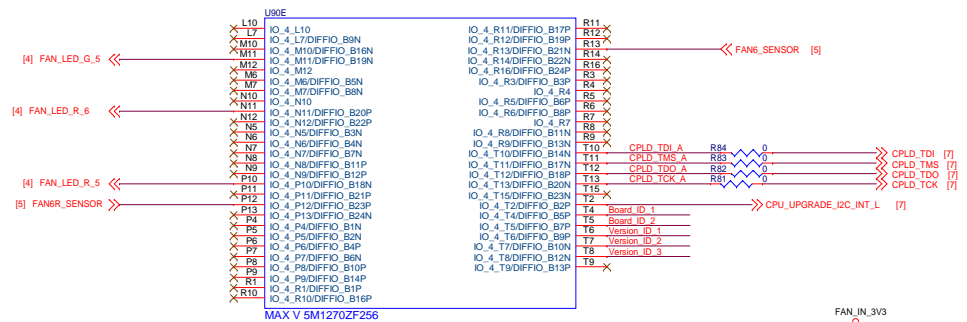
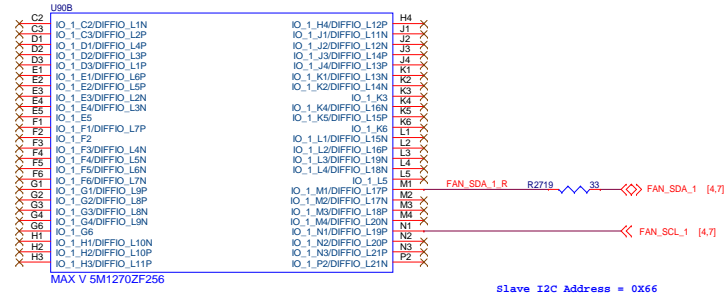
REVISION HISTORY

Version	Date	Engineer	Change Description
R0A	2015/01/01	Daniel_yang	Copy schematic from 165000001148A
R0A	2015/01/13	Daniel_yang	1.Modify photocouper behaviour: R59、R57、R49、R54、R52、R50 change to 100ohm R60、R58、R56、R55、R53、R51 change to 33ohm Add R78、R79、R80、R77、R89、R90 and use 100ohm 2.Change photocouper to A-Type. 3.Modify photocouper resistance
R0A	2015/01/14	Daniel_yang	1.Add current limit IC for fan enable/disable function (P5)
R0A	2015/01/16	Daniel_yang	1.Update board ID define (P6) 2.Update pin4 to 0.1uF of Current limit IC (P5) 3.Depopulate L1~L6 (P5) 4.Add pull high 10k on fan idle signal of conn8
R0A	2015/02/04	Daniel_yang	1.Modify Board ID 01 : 165000001148A NP3ZZ7632001A =>for ZZ 11 : 165000001253A NP3ZZ7632008A =>for AO
R0B	2015/04/16	Daniel_yang	1.Revise VCC3V3 to Fan_IN_3V3 2.Revise pin1 net name to 3.3V_IN of photocouper pin 1 (P5) 3.Change pin1 of potocouper resistance to 220ohm from 110ohm (P7) 4.Move to R0B version
R01	2015/08/31	Elvis_Yeh	1.Move board ID to R01 2.Add mos on pin3 of photocouper and assign CPLD A7 pin to control this for fan speed on field upgrade.

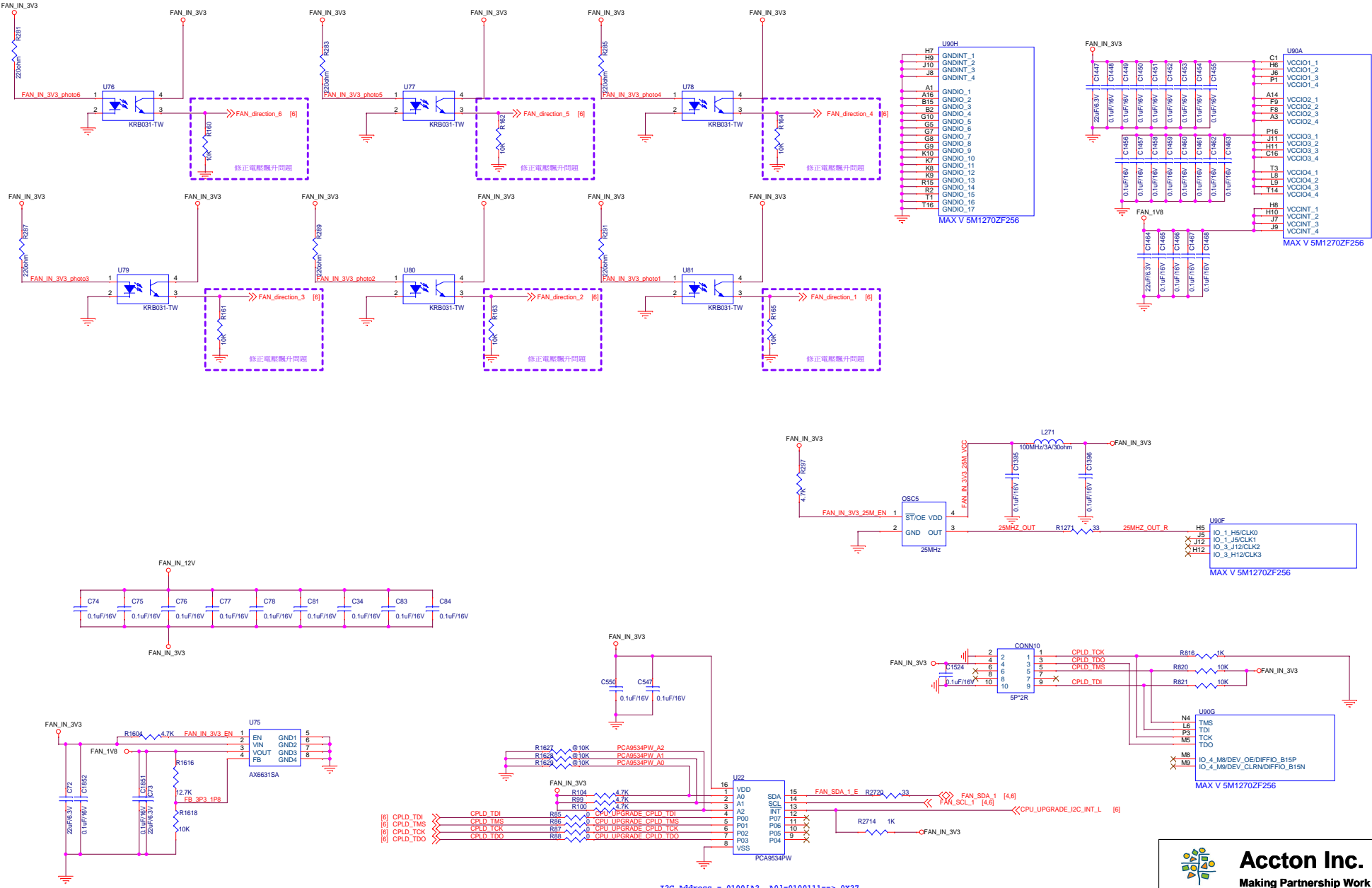









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Board ID[2:1]
  01 : ZZ product
  11 : AO product
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I2C Address = 0100[A2..A0]=0100111==> 0X27



Accton Inc.

Making Partnership Work

Model Name		ES7632XC2-1114-168ZZ	Rev	01
Size	Page Name	CPLD_2	Engineer	Elvis_Yeh
Date:		Wednesday, September 16, 2015	Sheet	7 of 7