

Table of Contents

1	Cover Page	18	Clocks
2	History	19	DDR3 SODIMM_CH0
3	Block Diagram	20	DDR3 SODIMM_CH1
4	Clock Tree	21	XDP_PORT80 Debug
5	Power Tree	22	SPI_SCREW_HS_RPCAP
6	SMBus Tree	23	FPGA_Management
7	Reset Tree	24	FPGA_POWER
8	Power Sequence	25	B2B & SATA Connectors
9	GPIO Table	26	VCCP_VNN_PWM
10	JTAG Tree	27	VCCP_VNN_MOS
11	Rangeley_DDRIII_CH0	28	VDDQ_VTT
12	Rangeley_DDRIII_CH1	29	V1P0
13	Rangeley_PCIE, GBE	30	V3P3A_V1P1
14	Rangeley SATA,USB,MISC	31	V5A_V3P3
15	Rangeley Power	32	LDO_V1P8_V1P5_V1P35_V1P5A
16	Rangeley Power	33	Debug LED
17	Rangeley Power_GND		



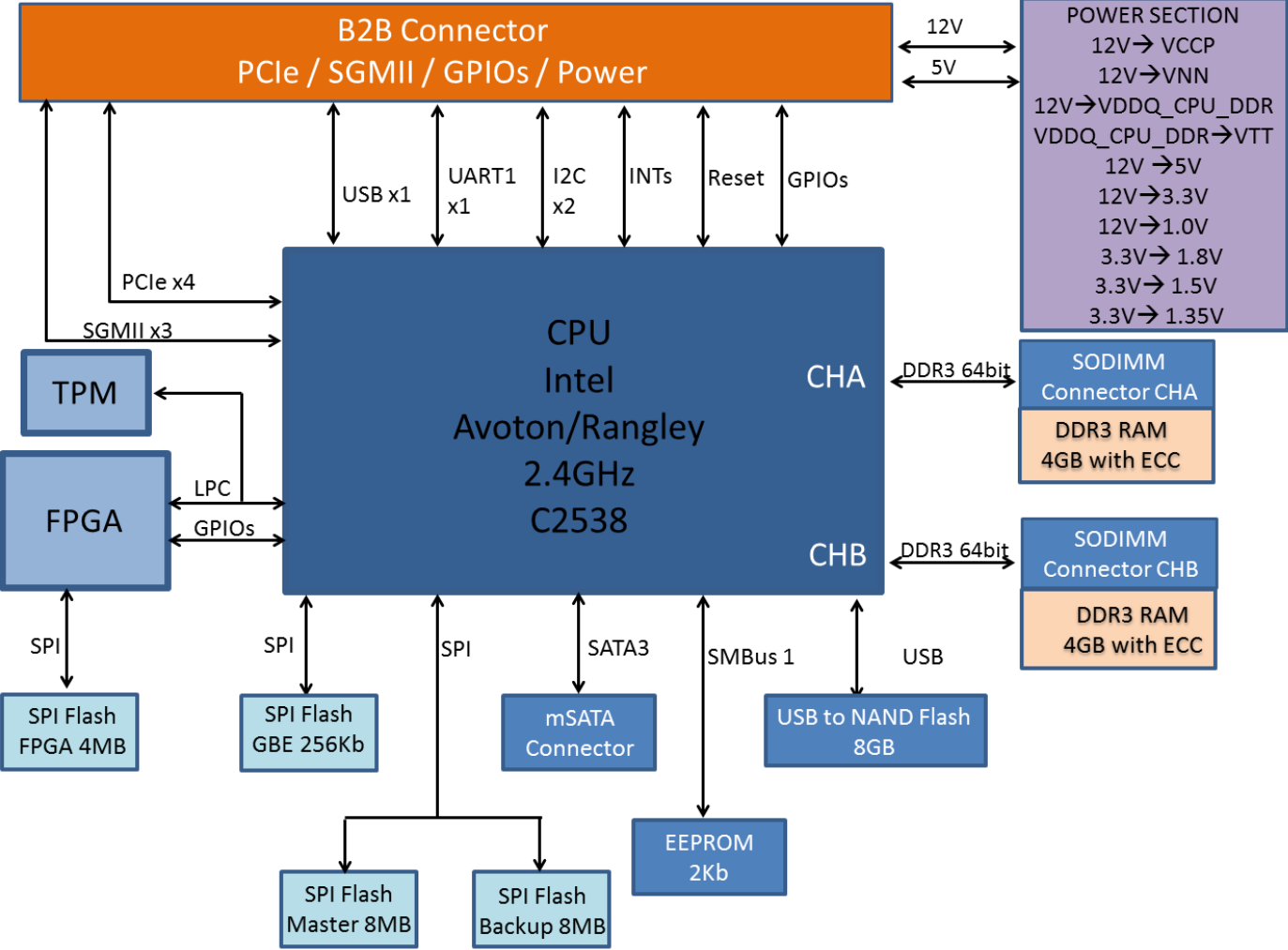
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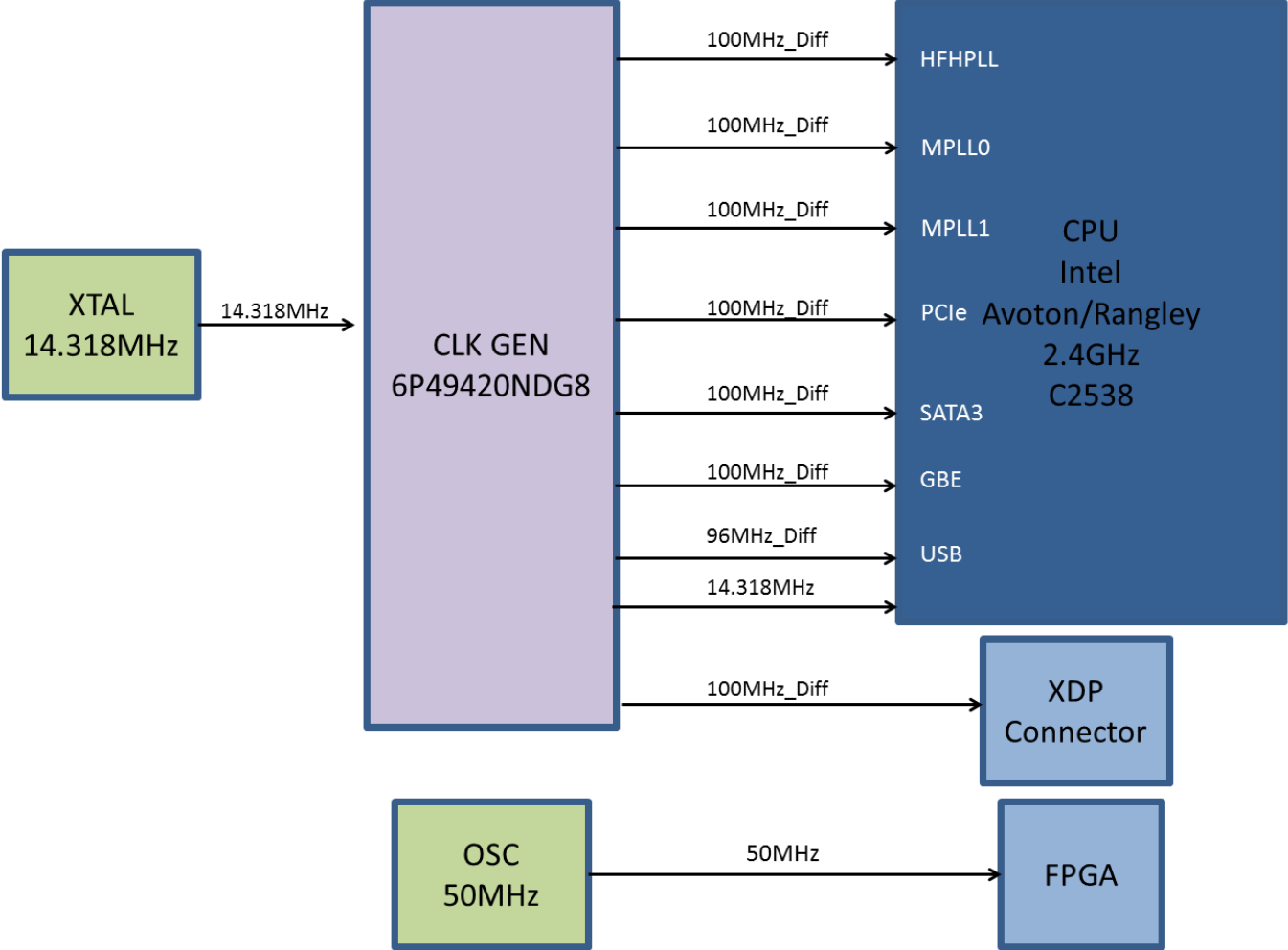
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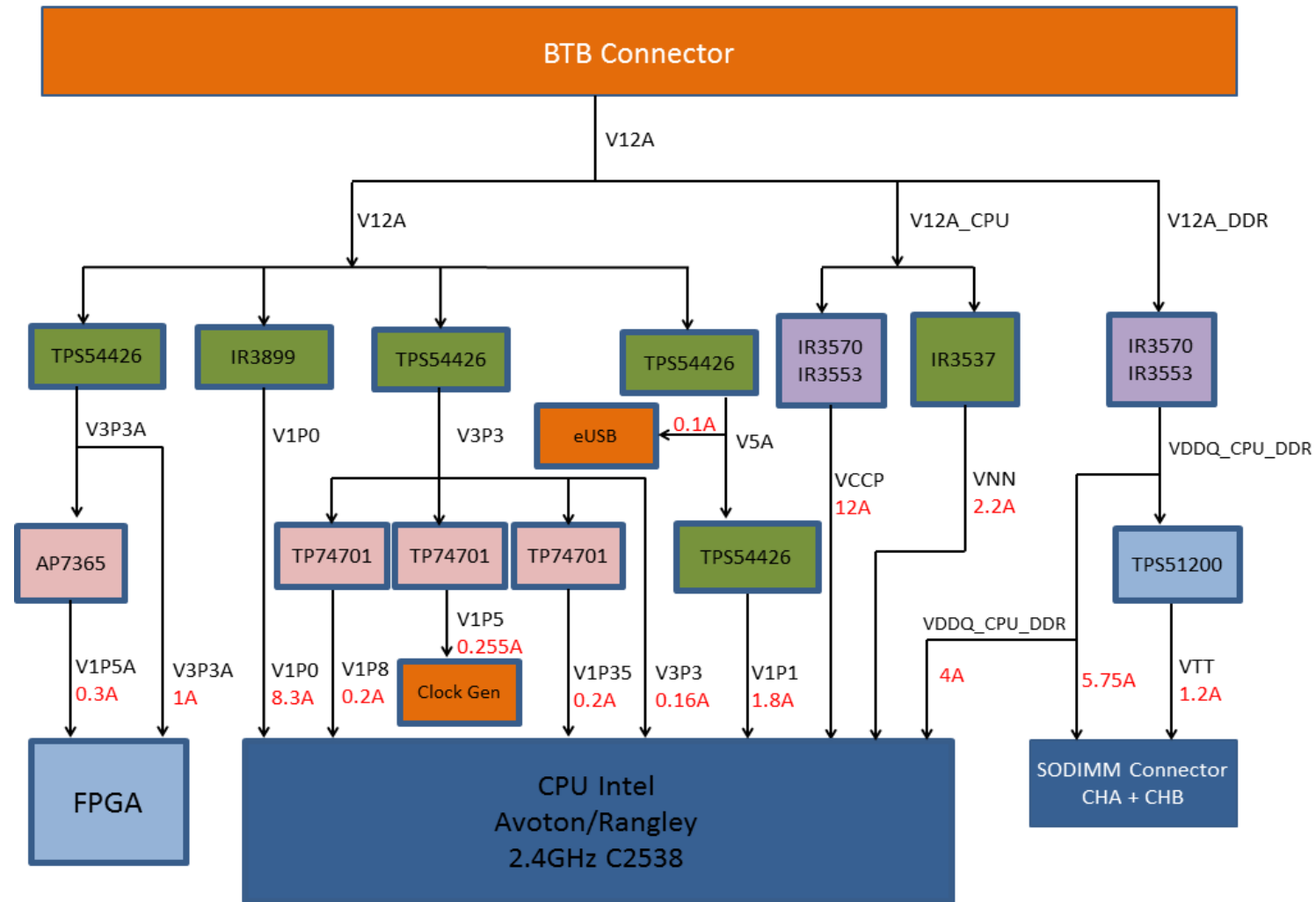
Model Name		ES7632BT2-1114-168ZZ CPU Board	Rev	R01
Size	Page Name	Engineer		
C	P01_Cover	Jimmy_Huang		
Date:	Tuesday, December 15, 2015	Sheet	1	of 33

History

SCH Version	Date	Description
R0A	2014/03/05	Initial Version
R01	2014/06/30	Release to MP



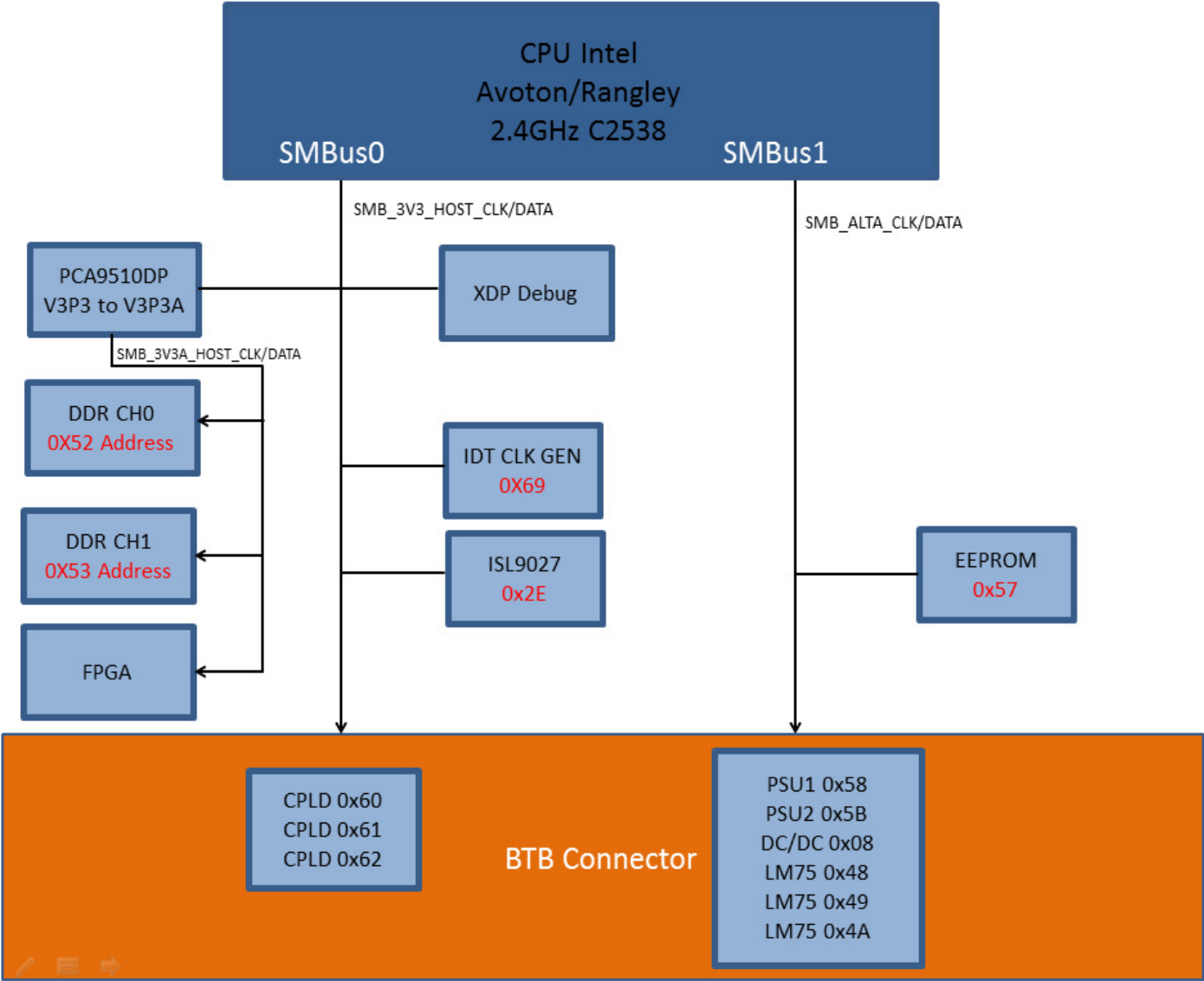


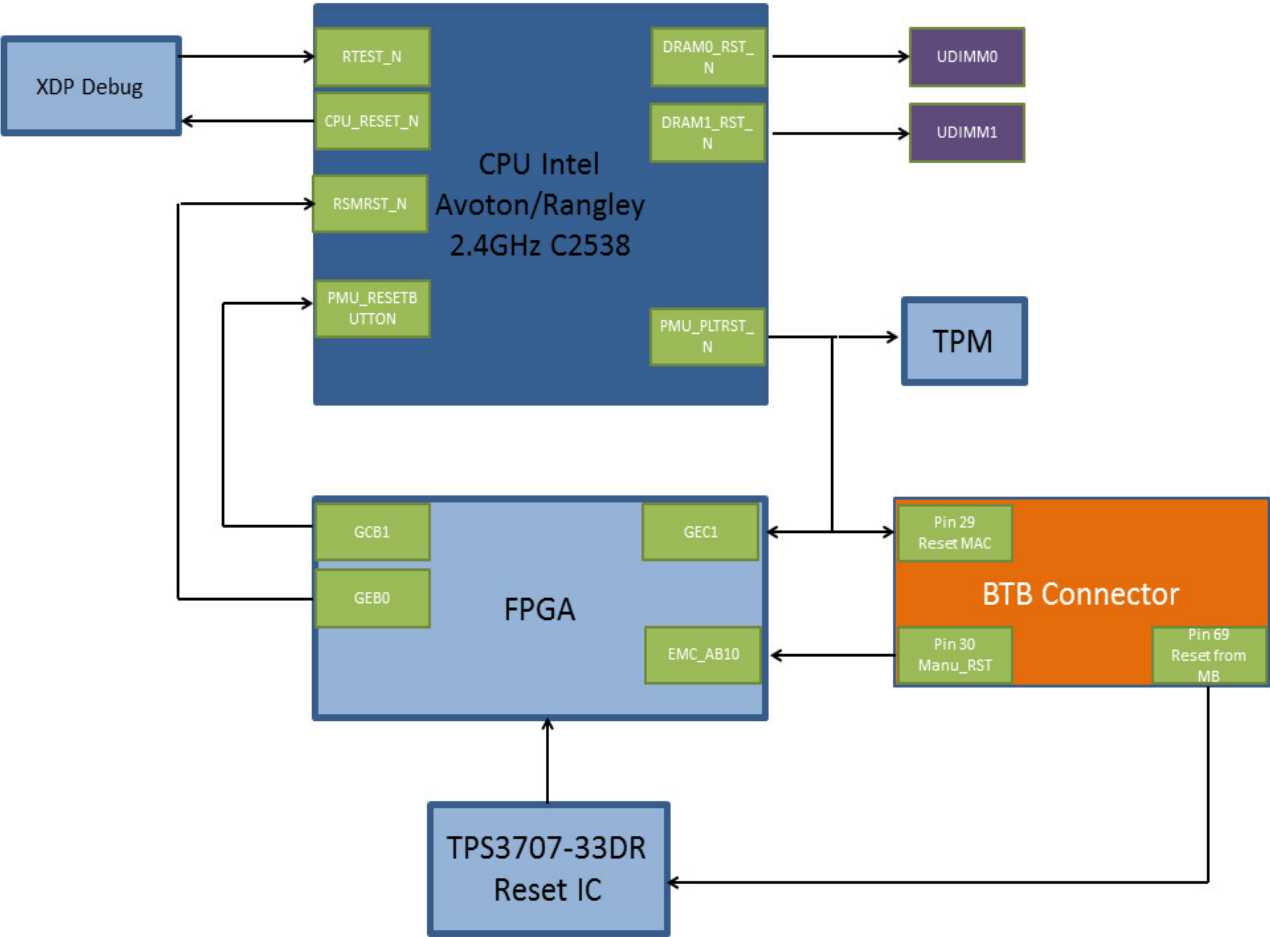


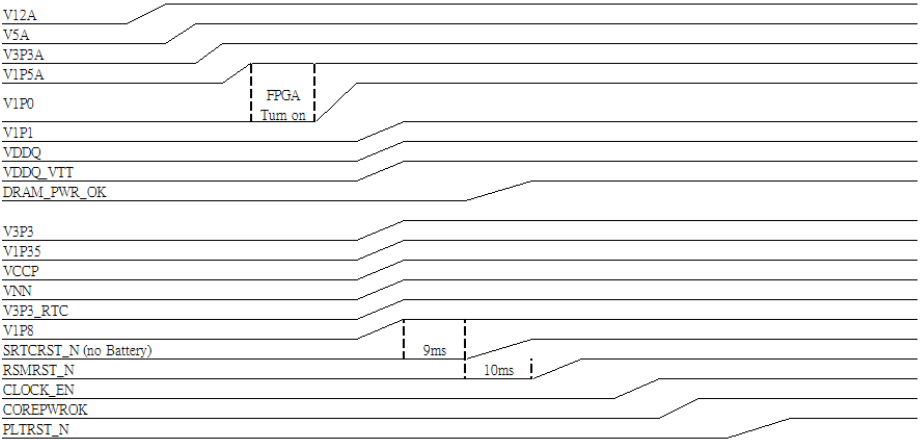
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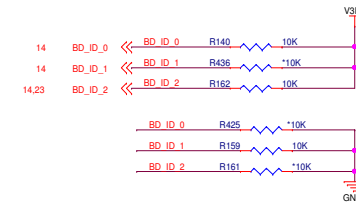
Model Name	ES7632BT2-1114-168ZZ CPU Board	Rev	R01
Size	C	Page Name	P05_Power Tree
Date	Tuesday, December 15, 2015	Engineer	Jummy_Huang
		Sheet	5 of 33







GPIOs	Pin#	Alternate Function	Rangeley Configuration	GPIOs	Pin#	Alternate Function	Rangeley Configuration
GPIO_0	AL56	NMI	Alternate Function	GPIO_SUS0	V66	N/A	ALTA_CPLD_TCK
GPIO_1	AL63	ERROR2_B	Alternate Function	GPIO_SUS1	W54	N/A	ALTA_CPLD_TDI
GPIO_2	AL62	ERROR1_B	Alternate Function	GPIO_SUS2	T53	N/A	BD_ID_2
GPIO_3	AL65	ERROR0_B	Alternate Function	GPIO_SUS3	Y63	CPU_RESET_B	Alternate Function
GPIO_4	AMS2	IRERR_B	Alternate Function	GPIO_SUS4	Y57	SUSFWRDNACK	N/A ; pull up
GPIO_5	AL52	MCERR_B	Alternate Function	GPIO_SUS5	AD58	PMU_SUSCLK	N/A
GPIO_6	AG50	UART1_RXD	Alternate Function	GPIO_SUS6	AC52	PMU_SLP_DDRVTT_B	Goes to FPGA, Output
GPIO_7	AH50	UART1_TXD	Alternate Function	GPIO_SUS7	Y50	PMU_SLP_LAN_B	Goes to FPGA, Output
GPIO_8	AN62	SMB_CLK0	Alternate Function	GPIO_SUS8	AD66	PMU_WAKE_B	Alternate Function
GPIO_9	AF62	SMB_DATA0	Alternate Function	GPIO_SUS9	AC49	PMU_PWRBTN_B	Alternate Function
GPIO_10	AL58	SMB_ALRT_N0	Alternate Function	GPIO_SUS10	AB65	SUS_STAT_B	N/A, pull down
GPIO_11	AN63	SMB_DATA1	Goes to Mainboard	GPIO_SUS11	AD63	USB_OC0_B	Alternate Function
GPIO_12	AF63	SMB_CLK1	Goes to Mainboard	GPIO_SUS12	AC58	SPL_CS1_B	Alternate Function
GPIO_13	AN65	SMB_DATA2	UART0_TXD	GPIO_SUS13	W51	GBE_EE_DI	Alternate Function
GPIO_14	AR65	SMB_CLK2	UART0_RXD	GPIO_SUS14	W60	GBE_EE_DO	Alternate Function
GPIO_15	AT63	SATA_GPO	BD_ID_0	GPIO_SUS15	T50	GBE_EE_SK	Alternate Function
GPIO_16	AL49	SATA_LEDN	N/A	GPIO_SUS16	R59	GBE_EE_CS	Alternate Function
GPIO_17	AH51	SATA3_GPO	BD_ID_1	GPIO_SUS17	T58	GBE_SDP0_0	CPLD_INT_L
GPIO_18	AH54	SATA3_LEDN	Alternate Function	GPIO_SUS18	T48	GBE_SDP0_1	CPLD2_INT_L
GPIO_19	AH59	FLEX_CLK_SE0	Alternate Function	GPIO_SUS19	P46	GBE_LED0	N/A, pull down
GPIO_20	AG56	FLEX_CLK_SE1	Alternate Function	GPIO_SUS20	W50	GBE_LED1	N/A, pull down
GPIO_21	AG54	LPC_LAD0	Alternate Function	GPIO_SUS21	P48	GBE_LED2	N/A, pull down
GPIO_22	AM53	LPC_LAD1	Alternate Function	GPIO_SUS22	R58	GBE_LED3	N/A, pull down
GPIO_23	AL53	LPC_LAD2	Alternate Function	GPIO_SUS23	V63	GBE_WOL	N/A, pull down
GPIO_24	AG59	LPC_LAD3	Alternate Function	GPIO_SUS24	W56	GBE_MDIO0_LXC_CLK	MDIO0_CLK
GPIO_25	AH56	LPC_FRAMEB	Alternate Function	GPIO_SUS25	W59	GBE_MDIO0_LXC_DATA	MDIO0_DATA
GPIO_26	AG51	LPC_CLKOUT0	Alternate Function	GPIO_SUS26	Y54	GBE_MDIO1_LXC_CLK	ALTA_CPLD_TDO
GPIO_27	AM49	LPC_CLKOUT1	Alternate Function	GPIO_SUS27	Y53	GBE_MDIO1_LXC_DATA	ALTA_CPLD_TMS
GPIO_28	AH48	LPC_CLKRUNB	Alternate Function				
GPIO_29	AT50	ILB_SERIRQ	Alternate Function				
GPIO_30	AM58	PMU_RESETBUTTON_B	Alternate Function				



Board assignments			
BD_ID_2	BD_ID_1	BD_ID_0	Board Type
0	0	0	Lyon Peak SV
0	0	1	Lyon Peak BDV
0	1	0	Lyon Peak PPV
1	0	0	Mohon Peak
1	0	1	Intel Project 2
1	1	0	Tiger Cove

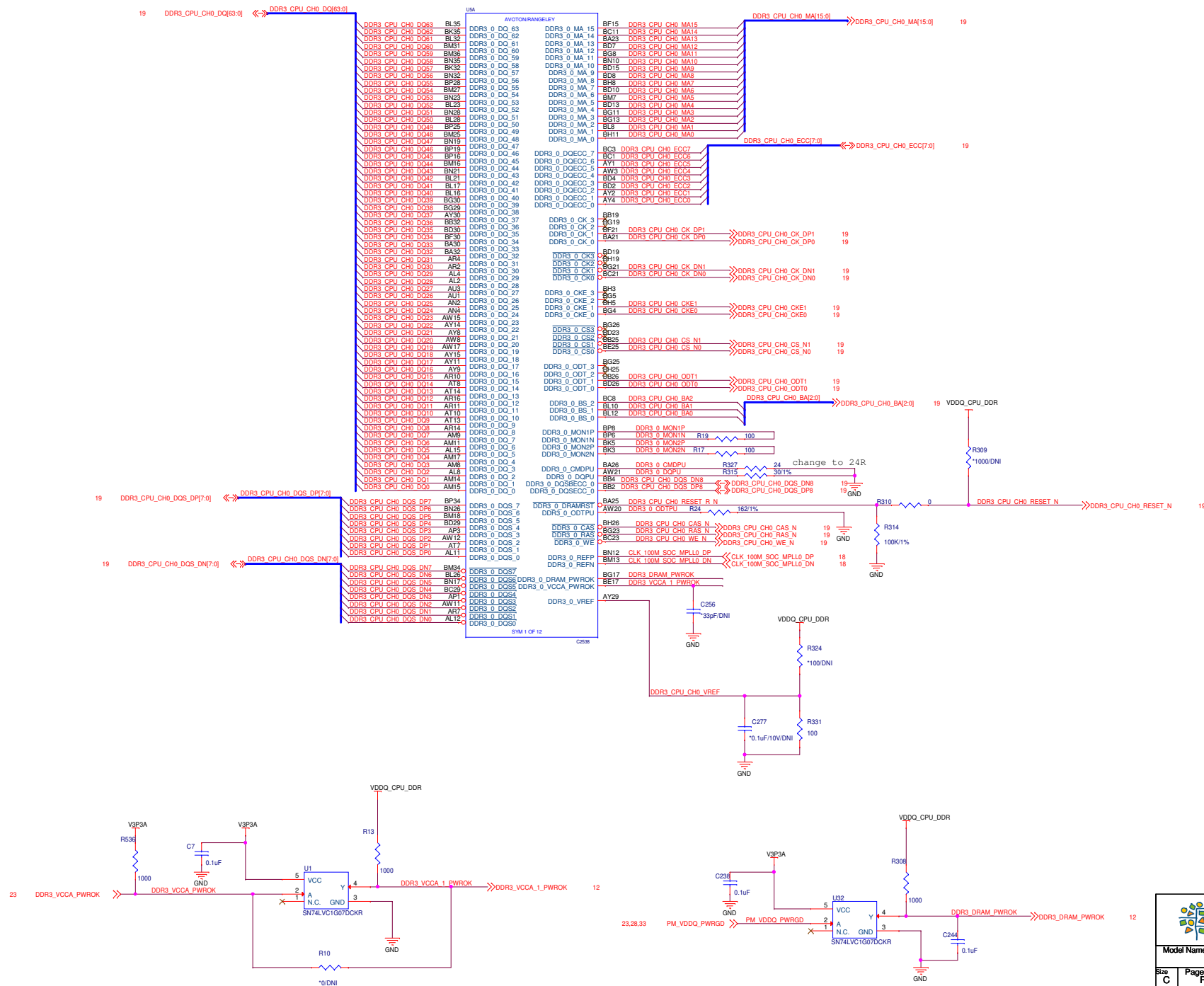


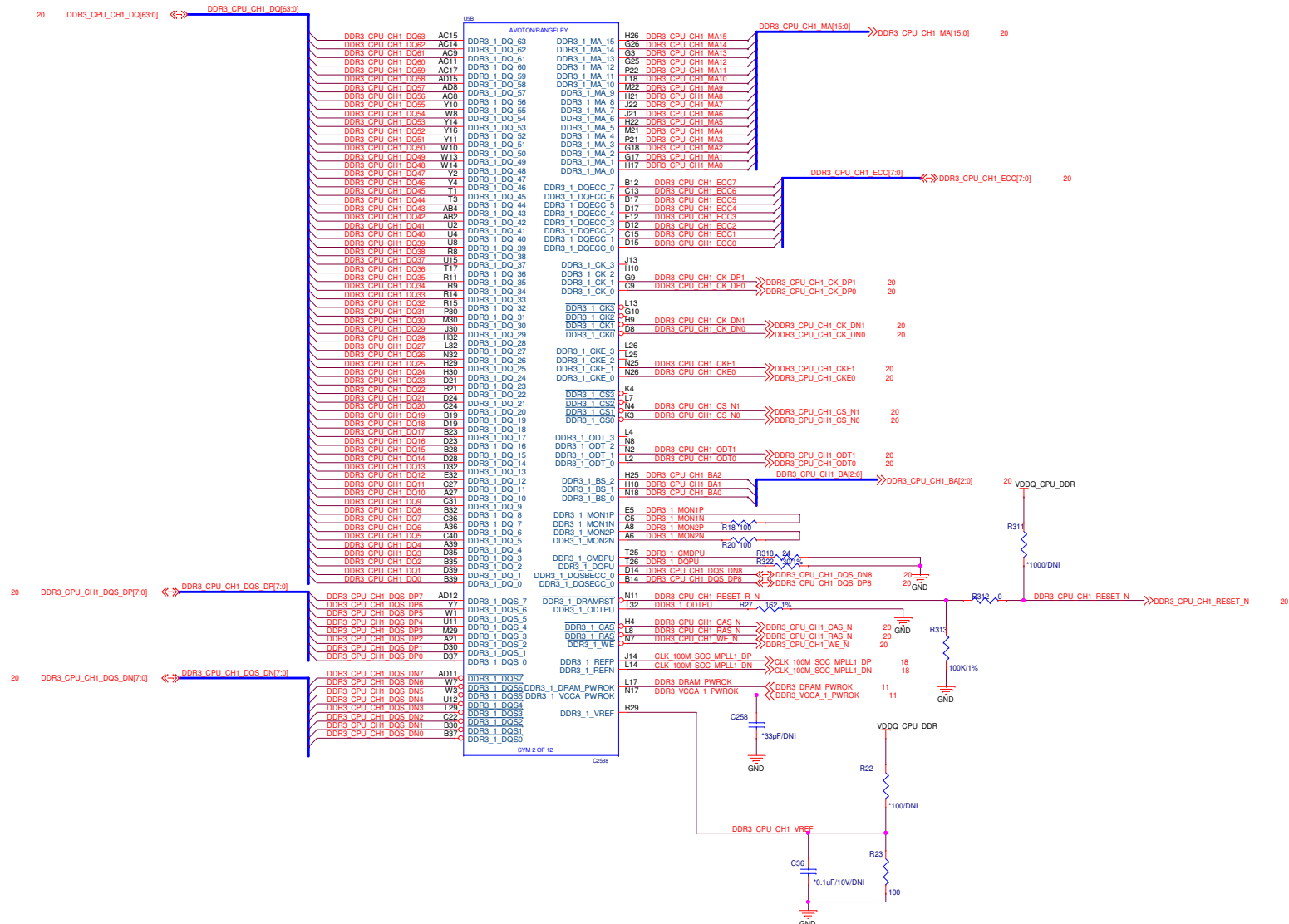
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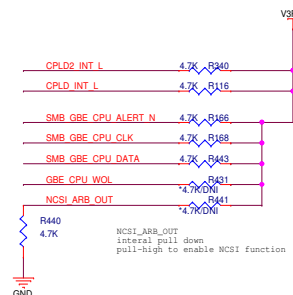
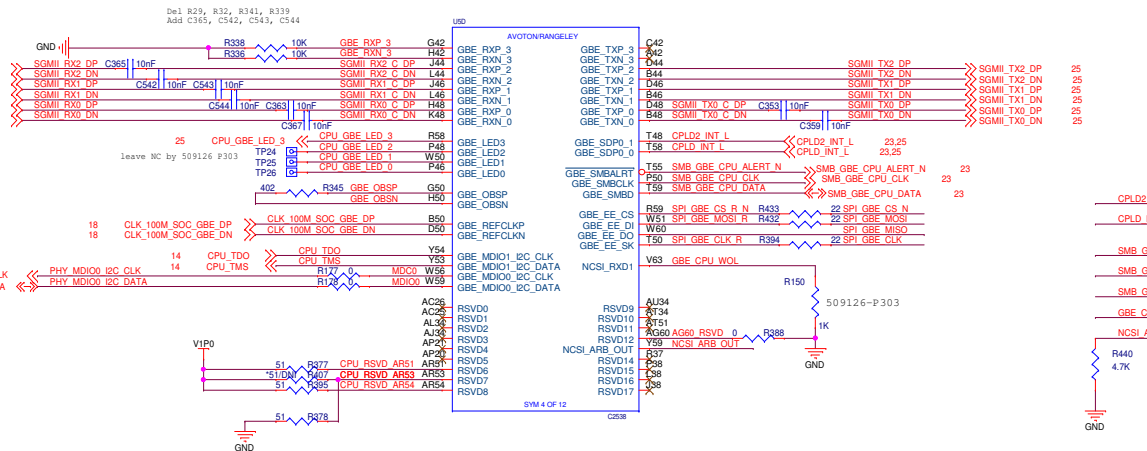
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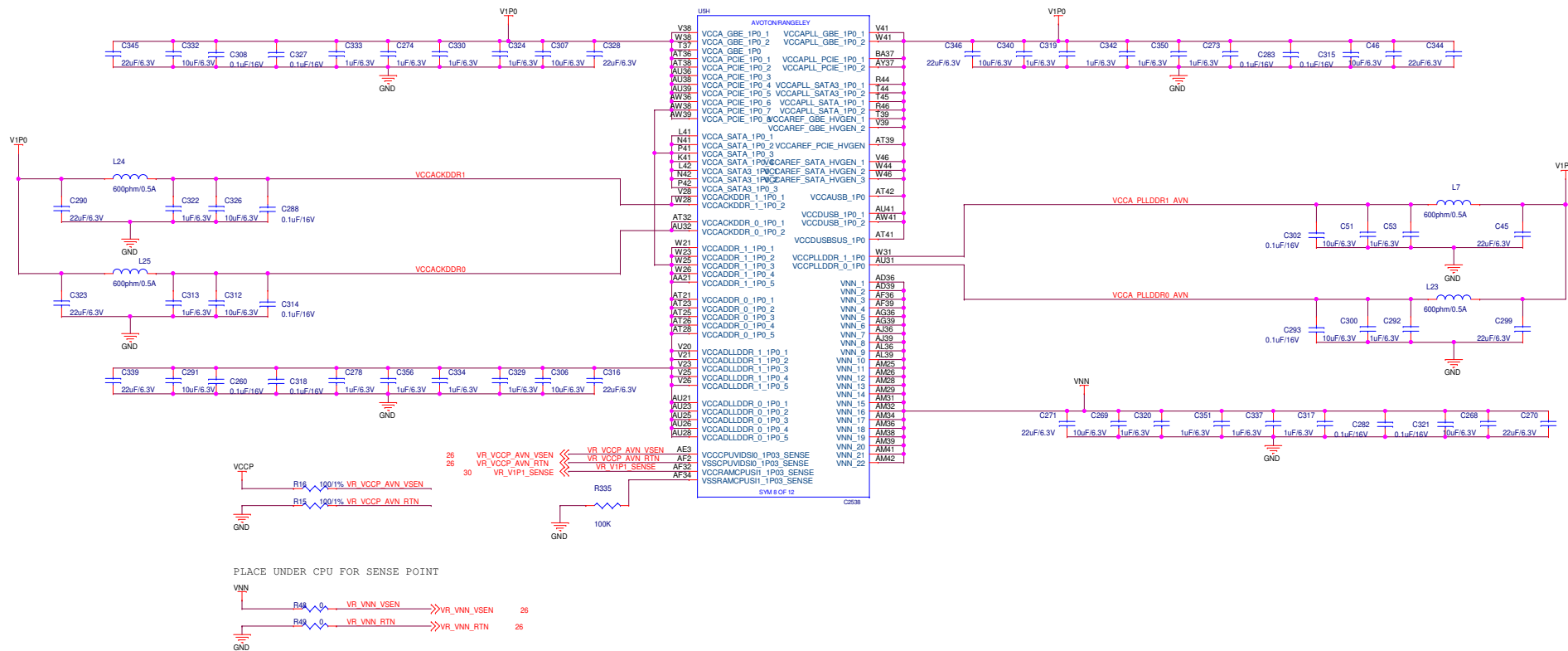
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Size C	Page Name	P09_GPIO TABLE	Engineer Stanley_Lu
Date: Tuesday, December 15, 2015		Sheet	9 of 33

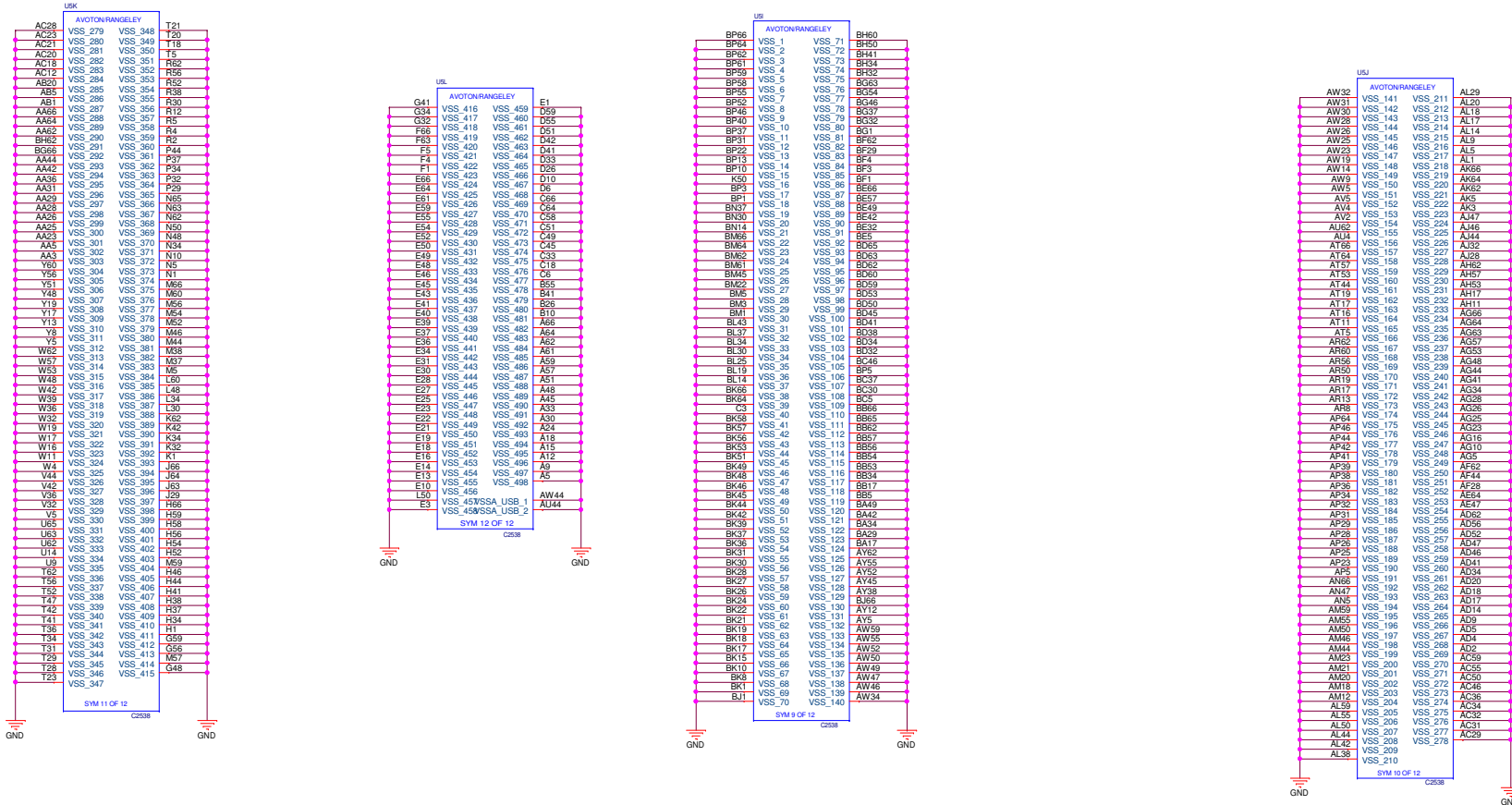
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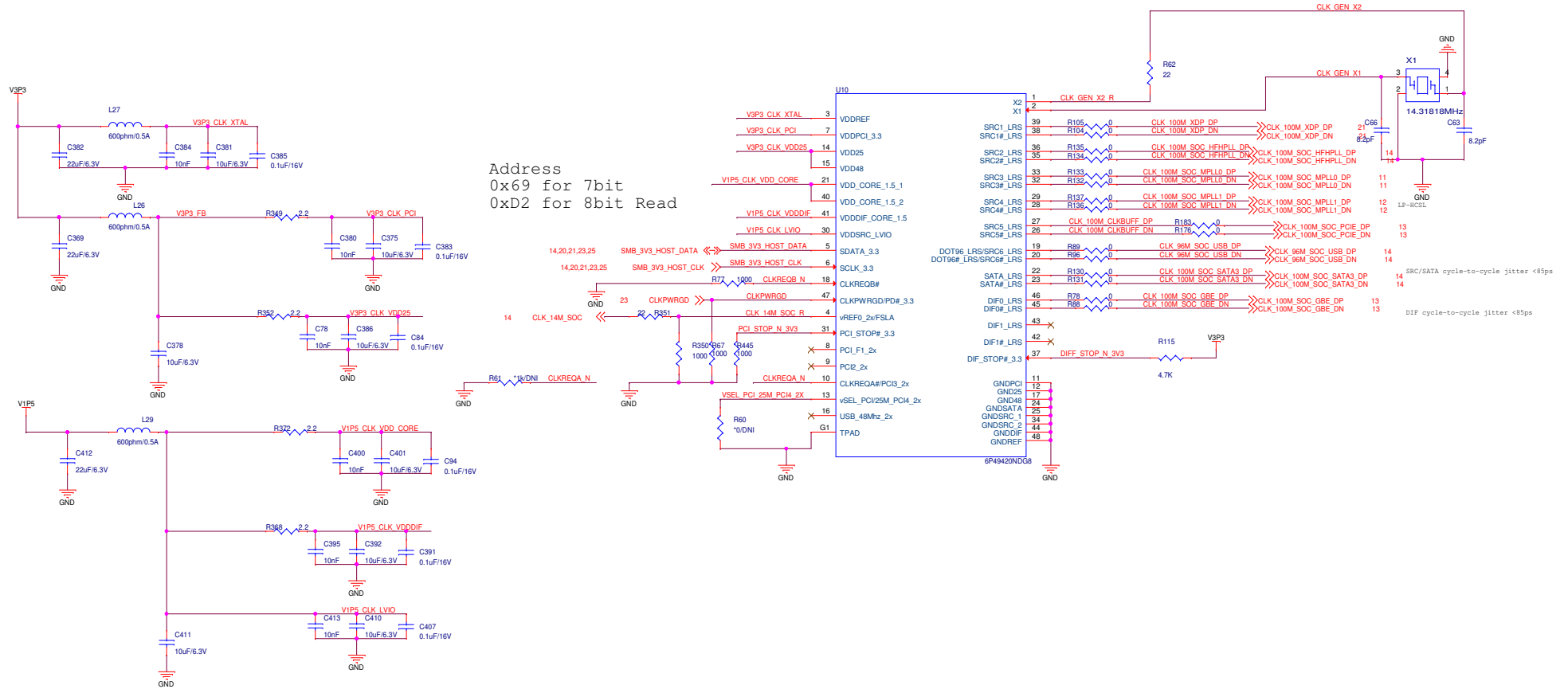




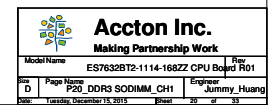
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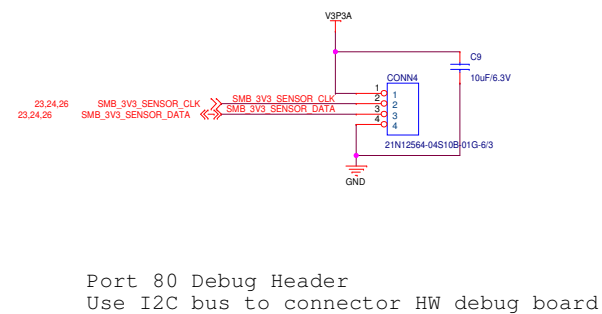
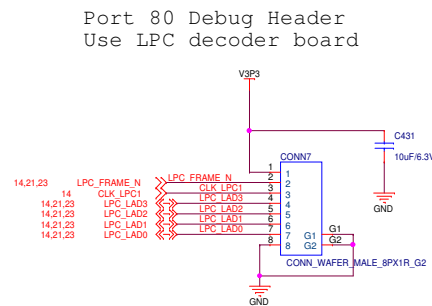
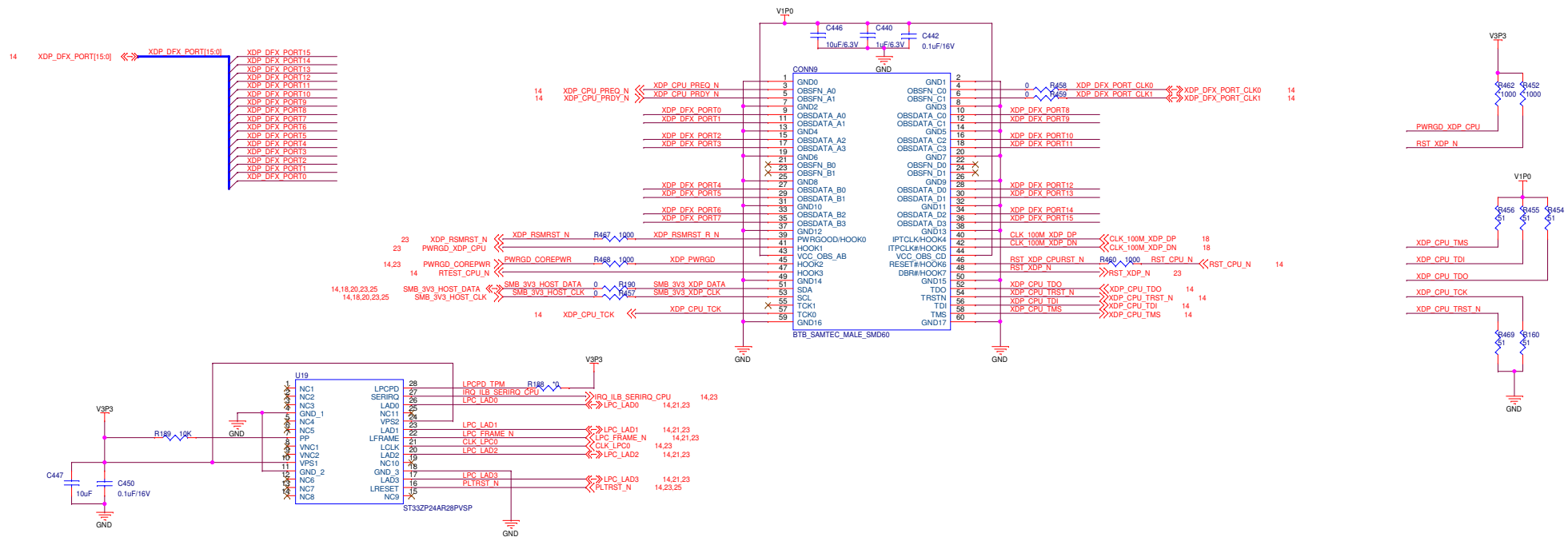
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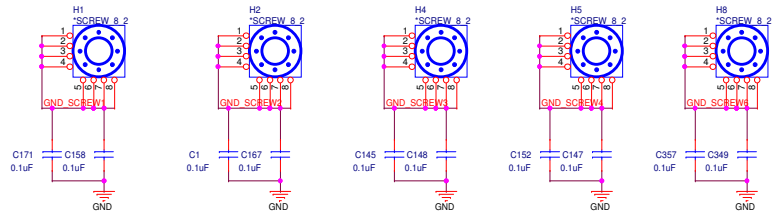
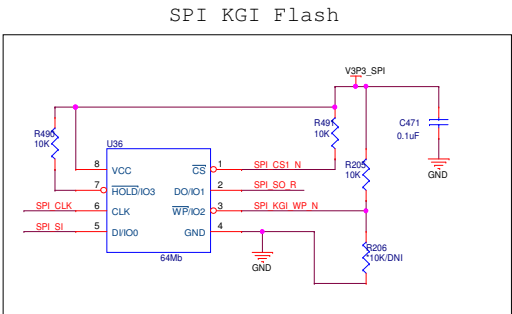
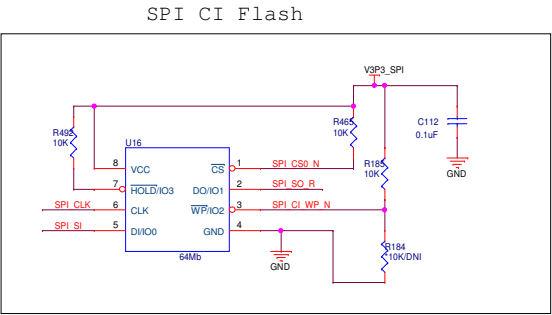
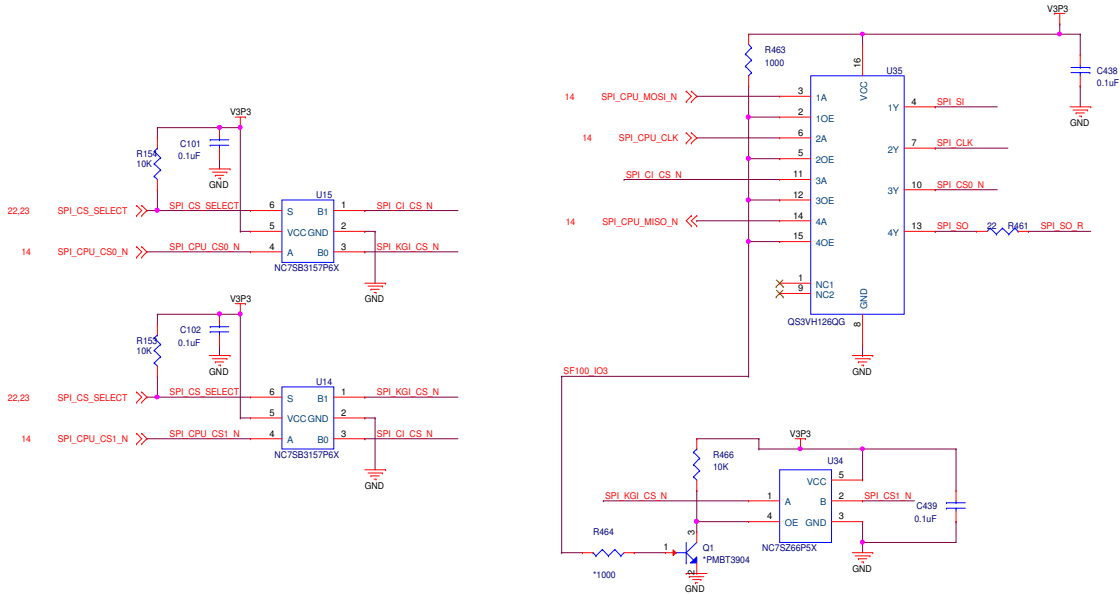
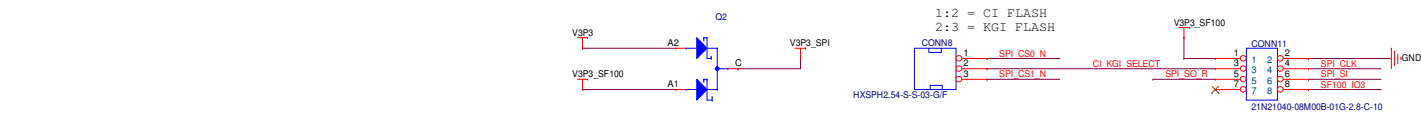
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Size		P17_Rangeley Power_GND	Jummy_Huang
Date:		Tuesday, December 15, 2015	Sheet 17 of 33



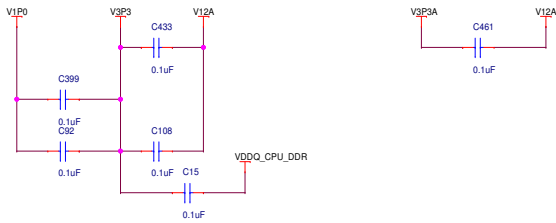
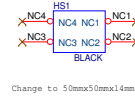


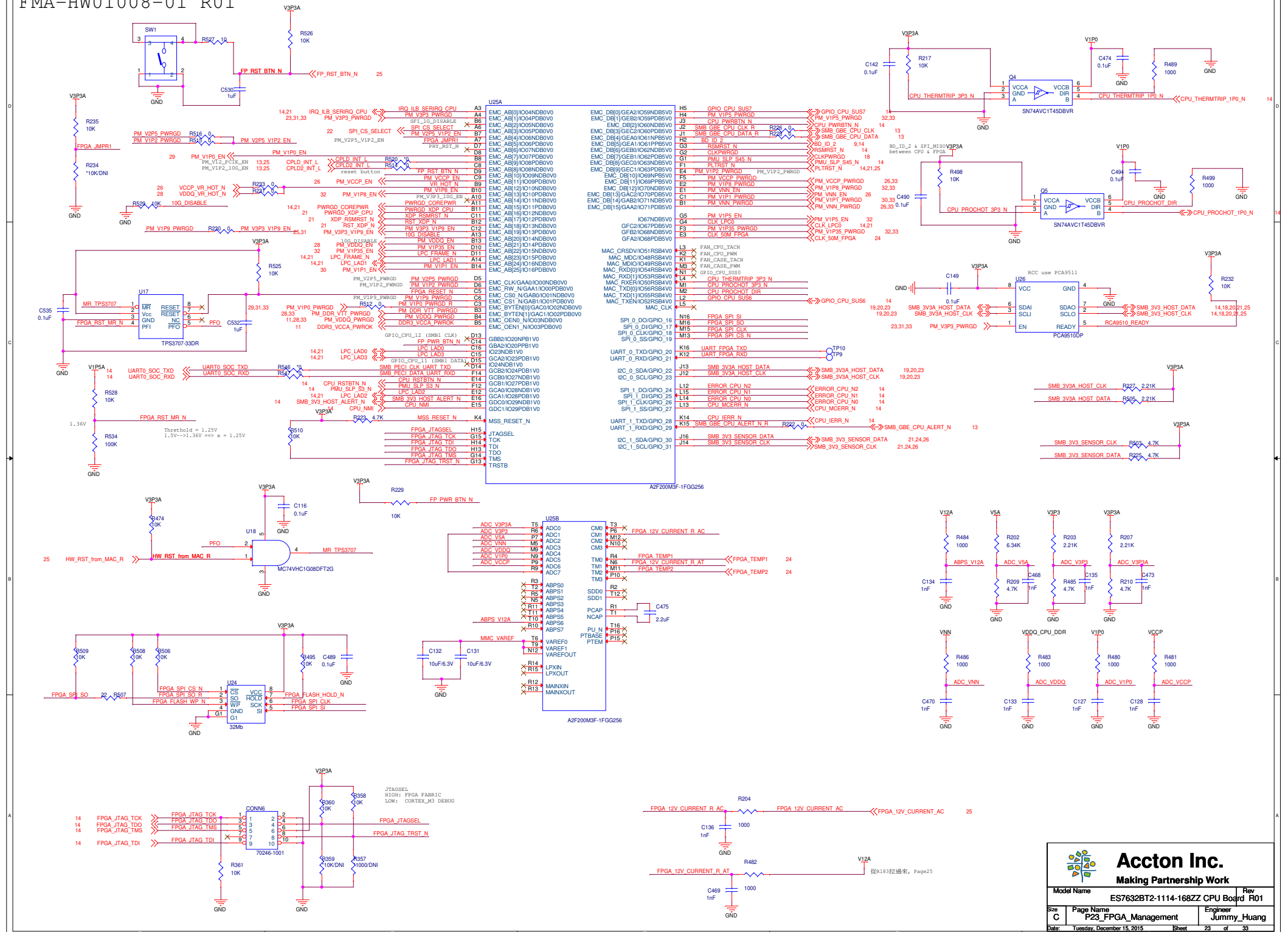
$$H = 5.2\text{mm}$$


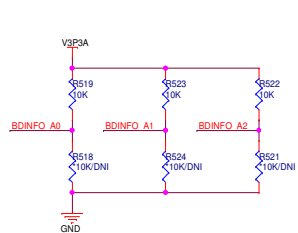
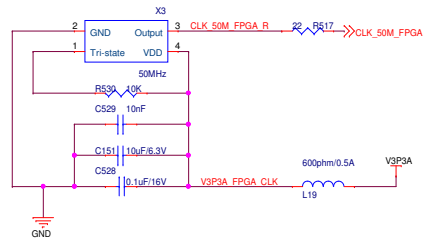
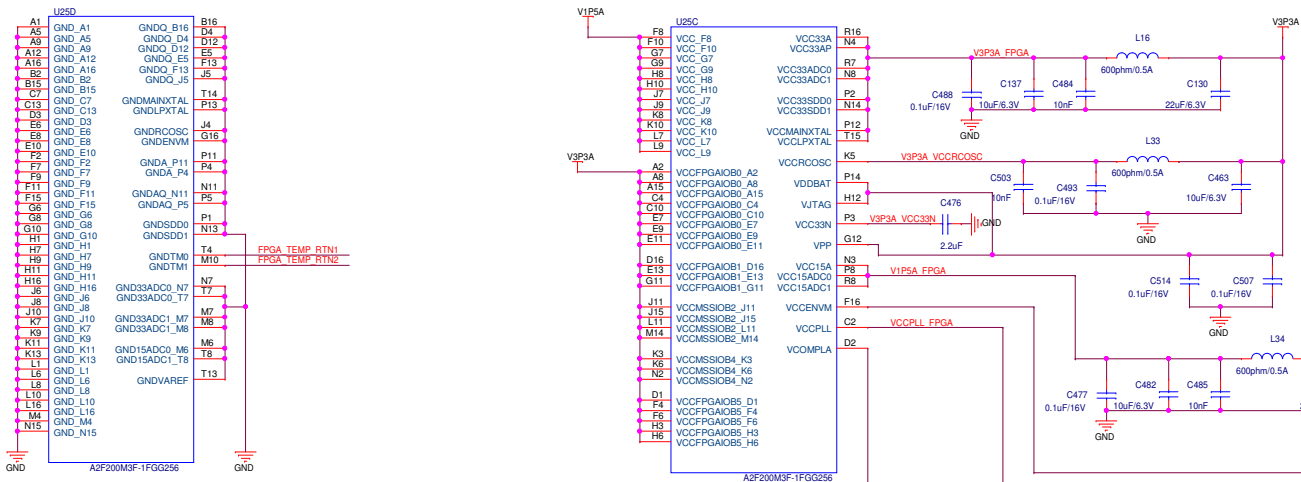




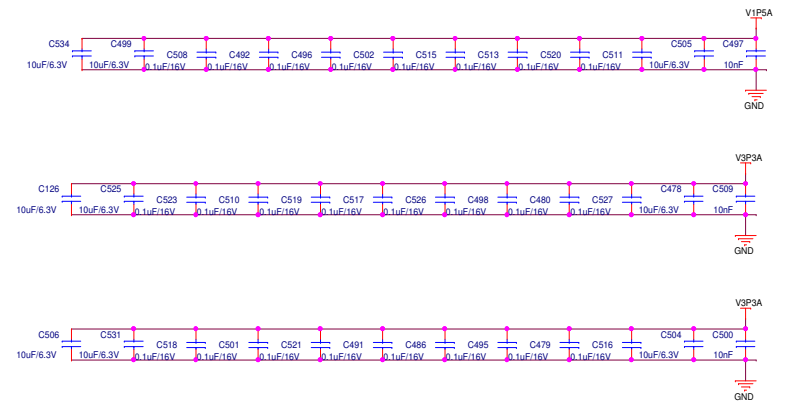
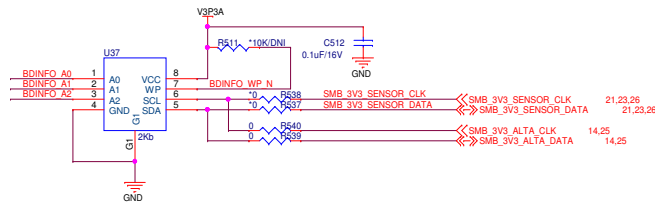
CPU Heatsink







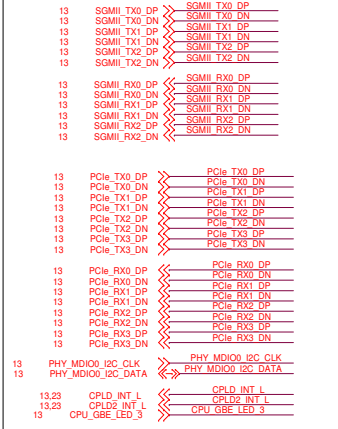
ADDRESS = AEh (8bit)
ADDRESS = 57h (7bit)
EEPROM (OPTIONAL)
BOARD TRACKING INFO



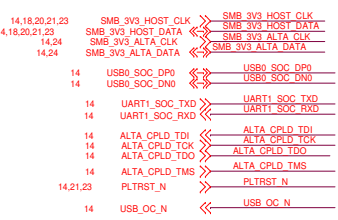
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Model Name		ES7632BT2-1114-168ZZ CPU Board R01	Rev
Size	Page Name	P24_FPGA_POWER	Engineer
C		Jummy_Huang	
Date:	Tuesday, December 15, 2015	Sheet	24 of 33

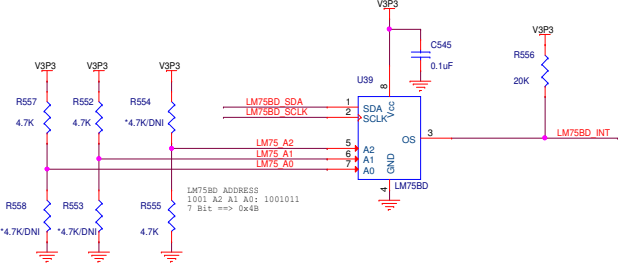
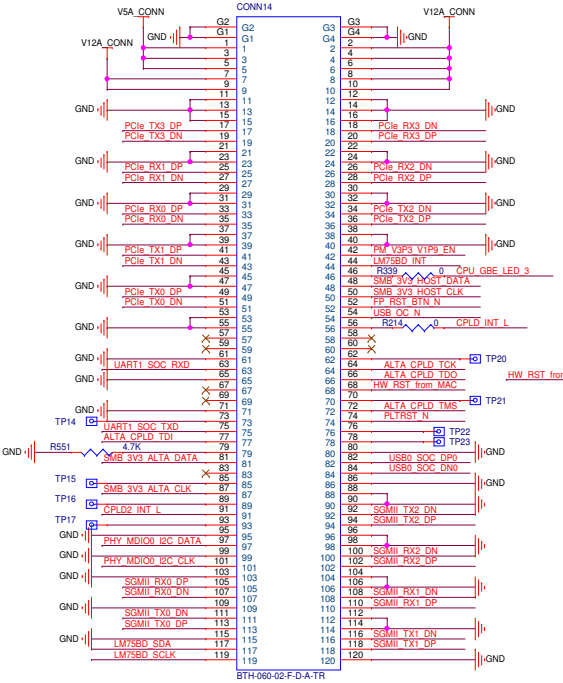
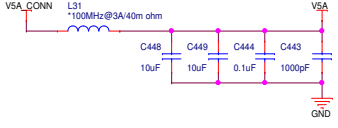
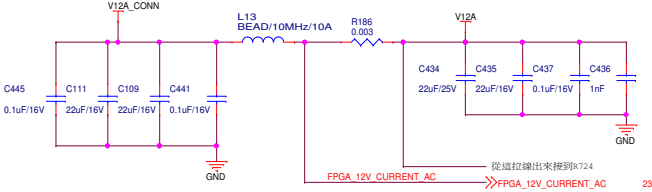
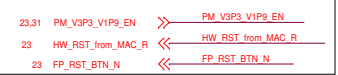
P13_Rangeley_PCIE, GBE

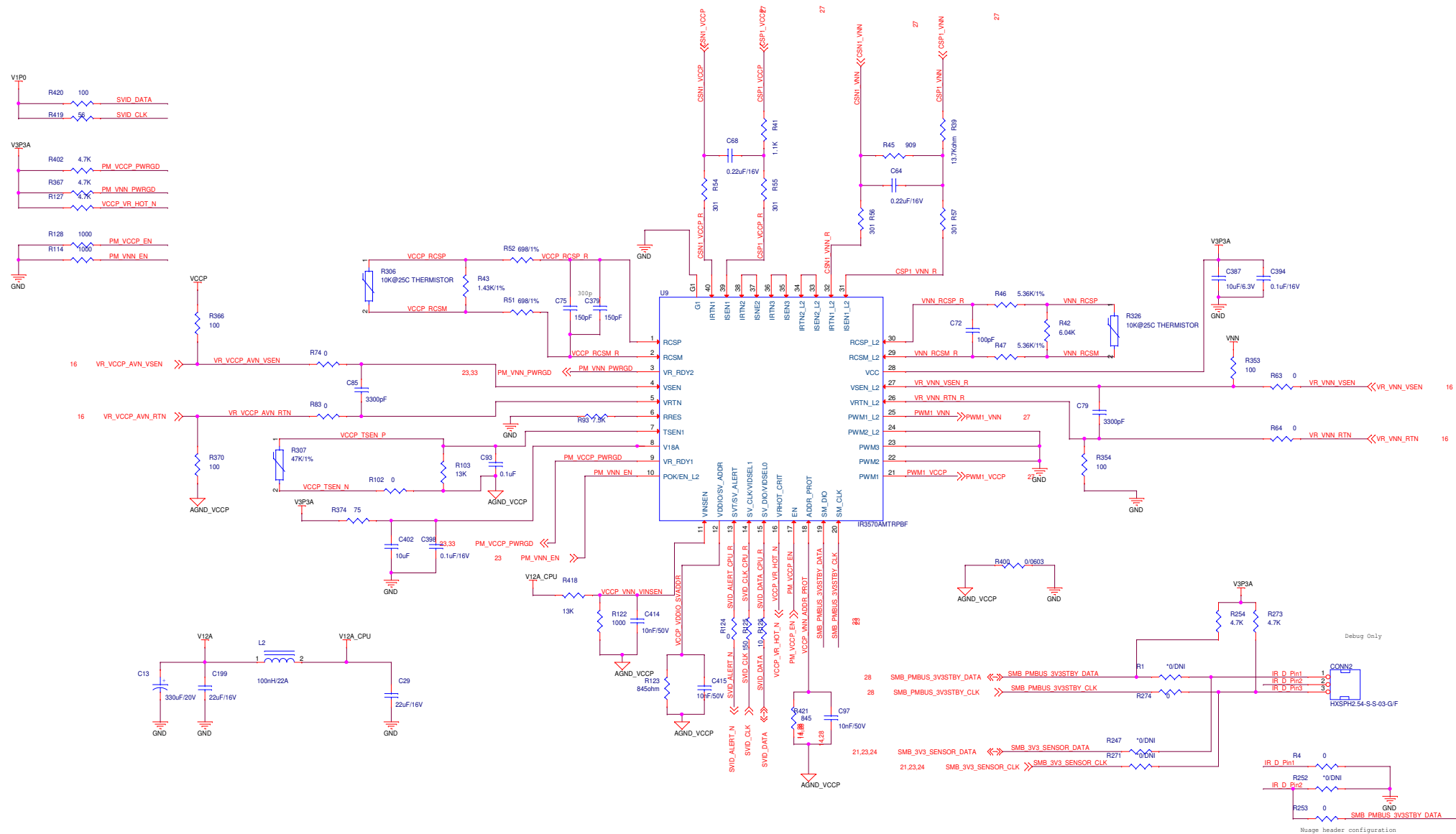


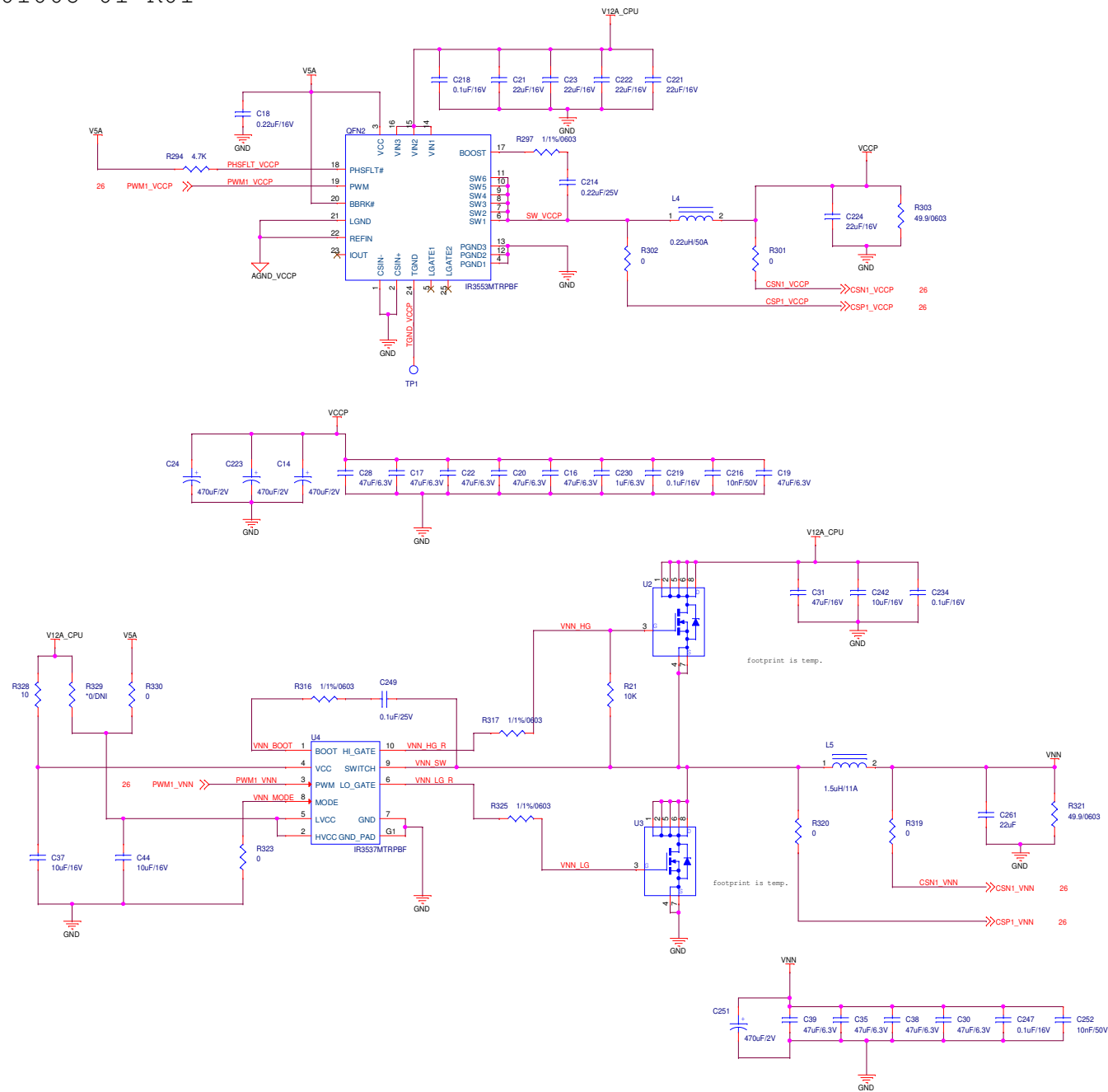
P14_Rangeley SATA,USB,MISC



P24_FPGA_Management

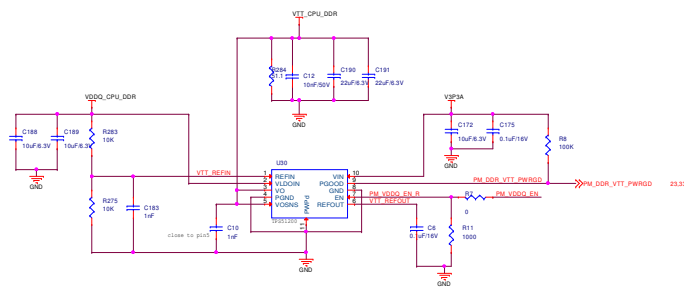
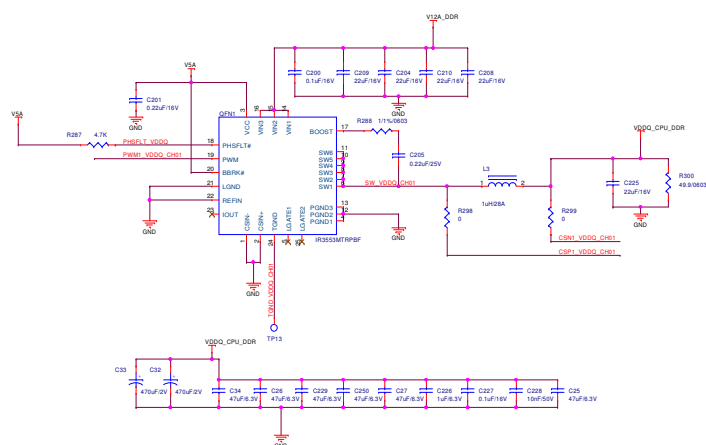
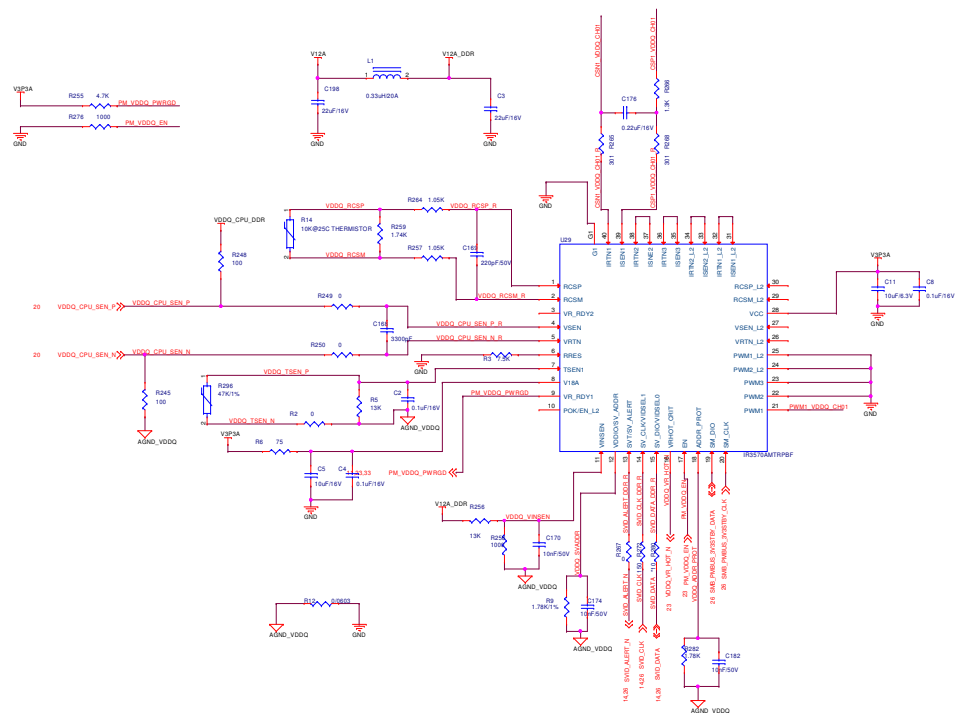




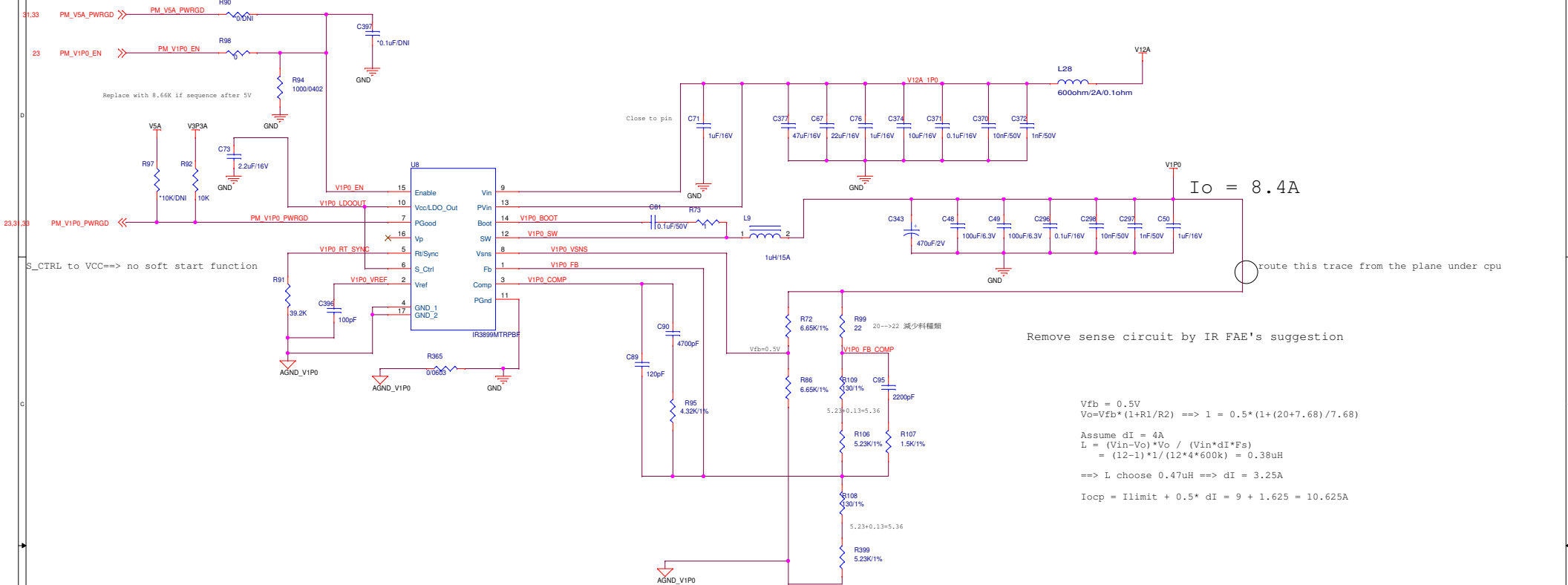


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Model Name		ES7632BT2-1114-168ZZ CPU Board R01	Rev
Size	Page Name	P27_VCCP_VNN_MOS	Engineer
C			Jummy_Huang
Date: Tuesday, December 15, 2015		Sheet	27 of 33



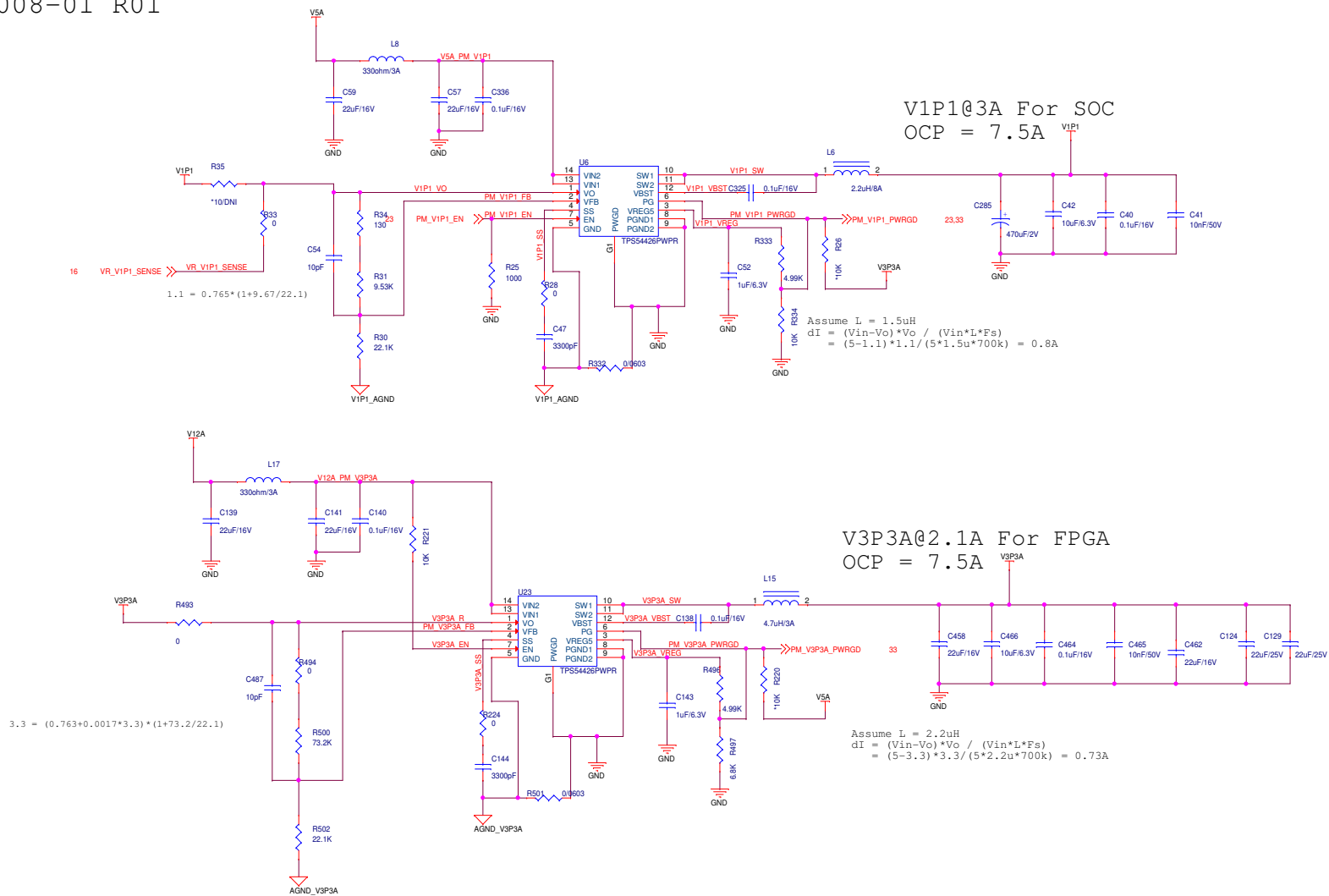
FMA-HW01008-01 R01



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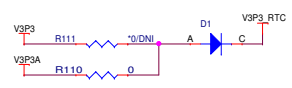
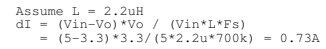
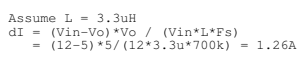
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Size	C	Page Name		P29_V1P0	Engineer	Jummy_Huang
Date:	Tuesday, December 15, 2015		Sheet	29	of	33

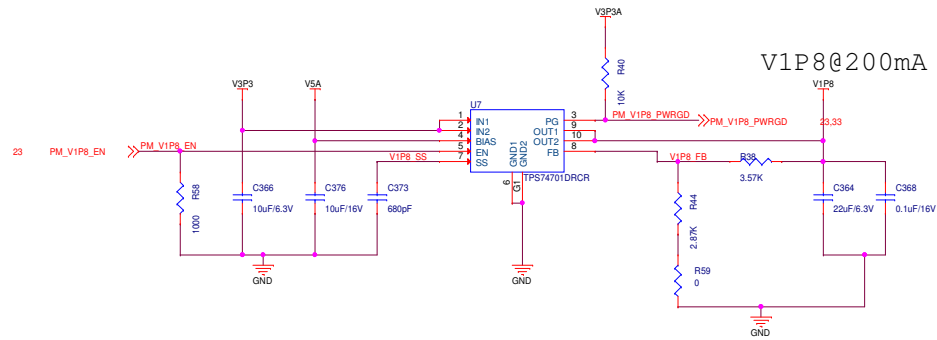


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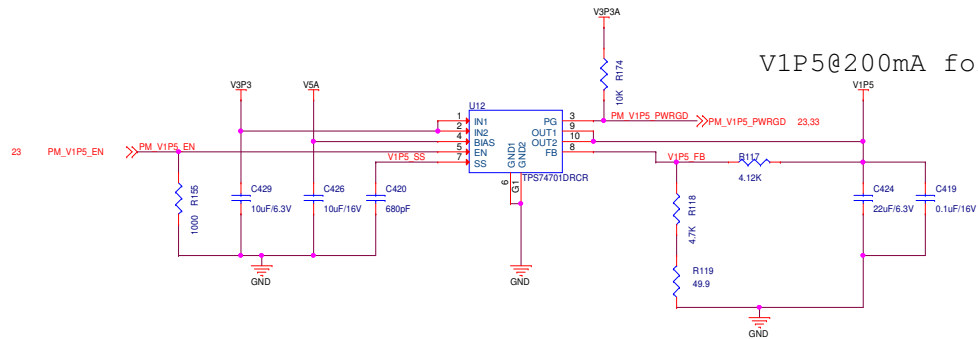
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Model Name	ES7632BT2-1114-168ZZ CPU Board R01	
Size	Page Name	Engineer
C	P30_V3P3A_V1P1	Jummy_Huang
Date:	Tuesday, December 15, 2015	Sheet 30 of 33



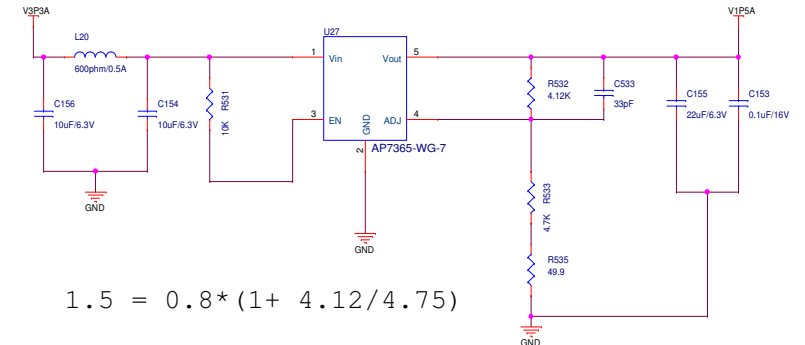


$$1.8 = 0.8 * (1 + 3.57/2.87)$$

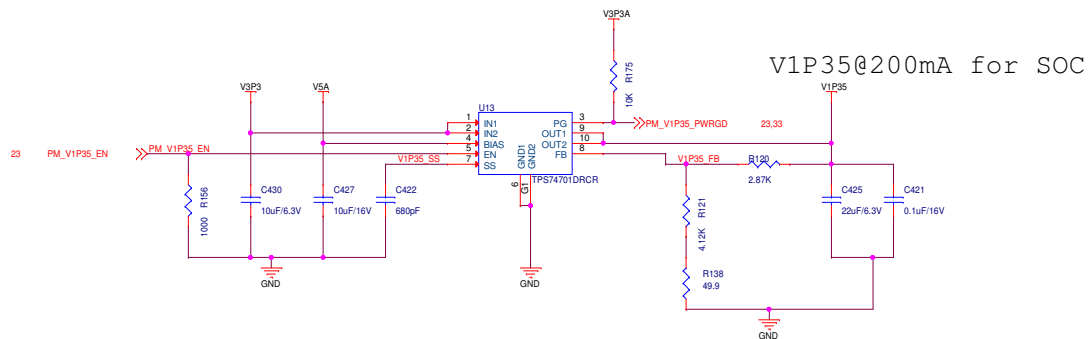


$$1.5 = 0.8 * (1 + 4.12/4.75)$$

V1P5A@300mA for FPGA



$$1.5 = 0.8 * (1 + 4.12/4.75)$$



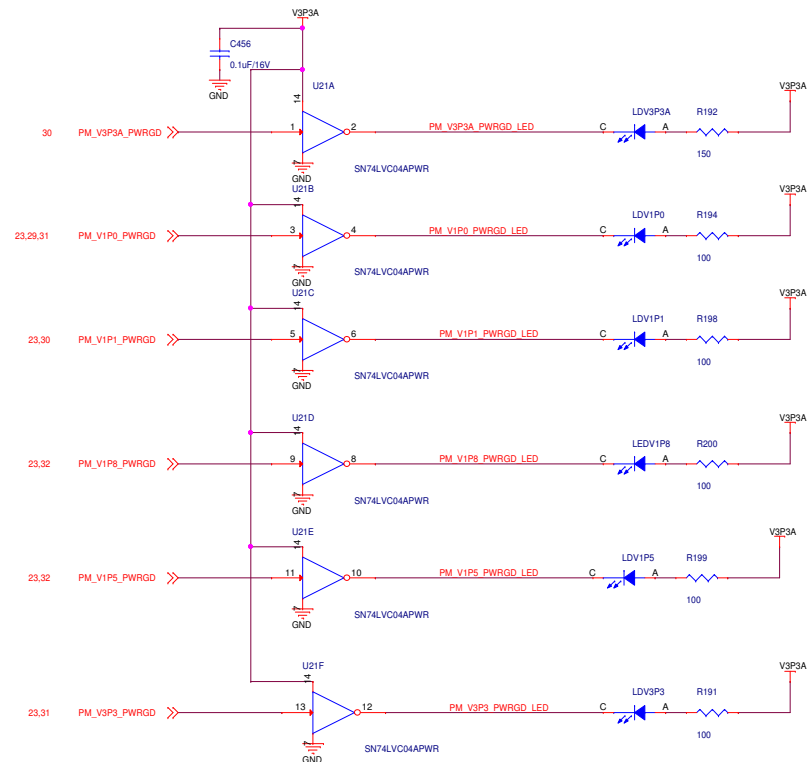
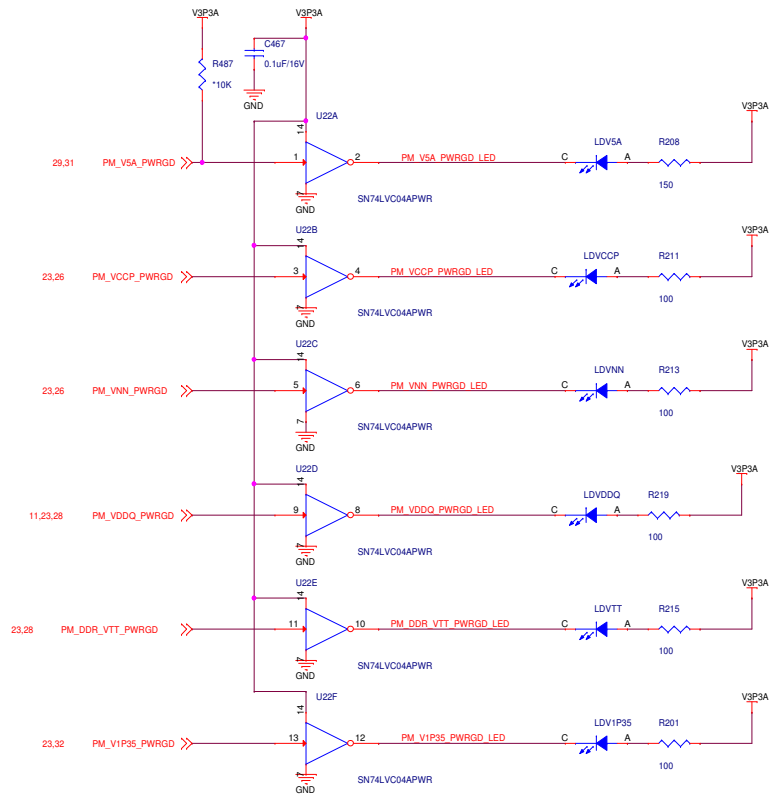
$$1.35 = 0.8 * (1 + 2.87/4.12)$$



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Model Name		ES7632BT2-1114-168ZZ CPU Board R01	Rev
Size	C	Page Name	Engineer
		P32_LDO	Jummy_Huang
Date:	Tuesday, December 15, 2015	Sheet	32 of 33

Sequence
1. V5A / V3P3A
2. V1P5A
3. V1P0
4. V1P1 / VDDQ/VTT...else power



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Model Name		ES7632BT2-1114-168ZZ CPU Board	Rev	R01
Size	C	Page Name	P33_Debug LED	Engineer
Date:		Tuesday, December 15, 2015	Sheet	33 of 33