OCP Server Committee 7/20/2016

Attendees (24 or more)

- John Stuewe – Dell
- Mark Shaw – Microsoft
- Bob Ogrey - AMD
- Adam Abramski - Intel
- Ahmad Ali
- Bergi Bagdasarian
- Bobby Rogers, Manish Patel - HPE
- Scott Zimmerman, Tim Smith BofA
- Boris, Yossi Mimran – Silicom
- Conor Malone – Hyve
- Howard Cohen – Stack Velocity
- Jerry Luo - Ericcson
- Mark P.
- Paul Hartke – Xilinx
- Roy Lee
- Zhimn He – A-Star Data Storage Institute
- Menno Kortekaas – Circle A
- Five or more phone numbers…
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Agenda

- Microsoft Open CloudServer vNext Architecture
    - o Overview of the new system, what we've learned with OCP, changes made for OCP and how the OCP community can participate.
    - o This is intended to be Open Source Hardware, so Microsoft will release before it's done and will release snapshots along the way. Working out a plan to begin releasing specs this fall with collateral this winter. Will release for everything except the motherboard. For the motherboard, there will be a generic specification that calls out interfaces (I/O, Power, Management) for now and intent to eventually release that as well.
    - o Questions
        - ▪ Which CPU? Microsoft is developing the next Intel CPU. We'll provide the generic specification for anybody else who wants it.
        - ▪ How much storage capacity on the server? 8xM.2's with 2xLFF HDD slots. We're working on a 2U version with additional 8xLFF HDDs.
        - ▪ What power is supported? I accidently pulled out of the deck our power strategy. The PMDU has generic adapters on the AC power. We are developing power "whips" for three datacenter classes (208V50A, 415V30A, 400V32A) that allow us to have one rack SKU that ships anywhere in the world. Each data

center will purchase whips that meet the above requirements. If there are additional voltage/currents that need to be supported, please provide feedback.

- http://files.opencompute.org/oc/public.php?service=files&t=1be51ac1ba3f99222742dcef00b9ee1e

- Hyve/Intel decathlete spec update
  - Submitting a Decathelete spec, was looking for some minor changes. The spec has been revved and posted, mostly making the changes as optional. Spec on the wiki.
  - Intel is doing internal review of the Decathlete spec before submitting.
- Silicom OCP Mezzanine
  - Boris & Yossi – networking board design company. Series of OCP mezzanines, this is the first. Quad port. End of layout, trying to get prototype in two months. Based on Broadwell/Skylake server board using Intel chip.
- OCP Project Voting still open. Make your vote count!
  - Question on LED placement. This is the first quad out there and the LEDs have limits where they can be located, but it would be nice if it were aligned. Silicom will center the LEDs on the connectors and request that the spec be modified accordingly.
  - Spec puts place for 3x MAC, but this is quad. With NCSI, will need 5 MAC addresses. No room for this, but wil need to figure it out. Will skipping MAC addresses cause issues?
    - Silicom to submit a recommendation.

One snapshot (you always see people come and go)