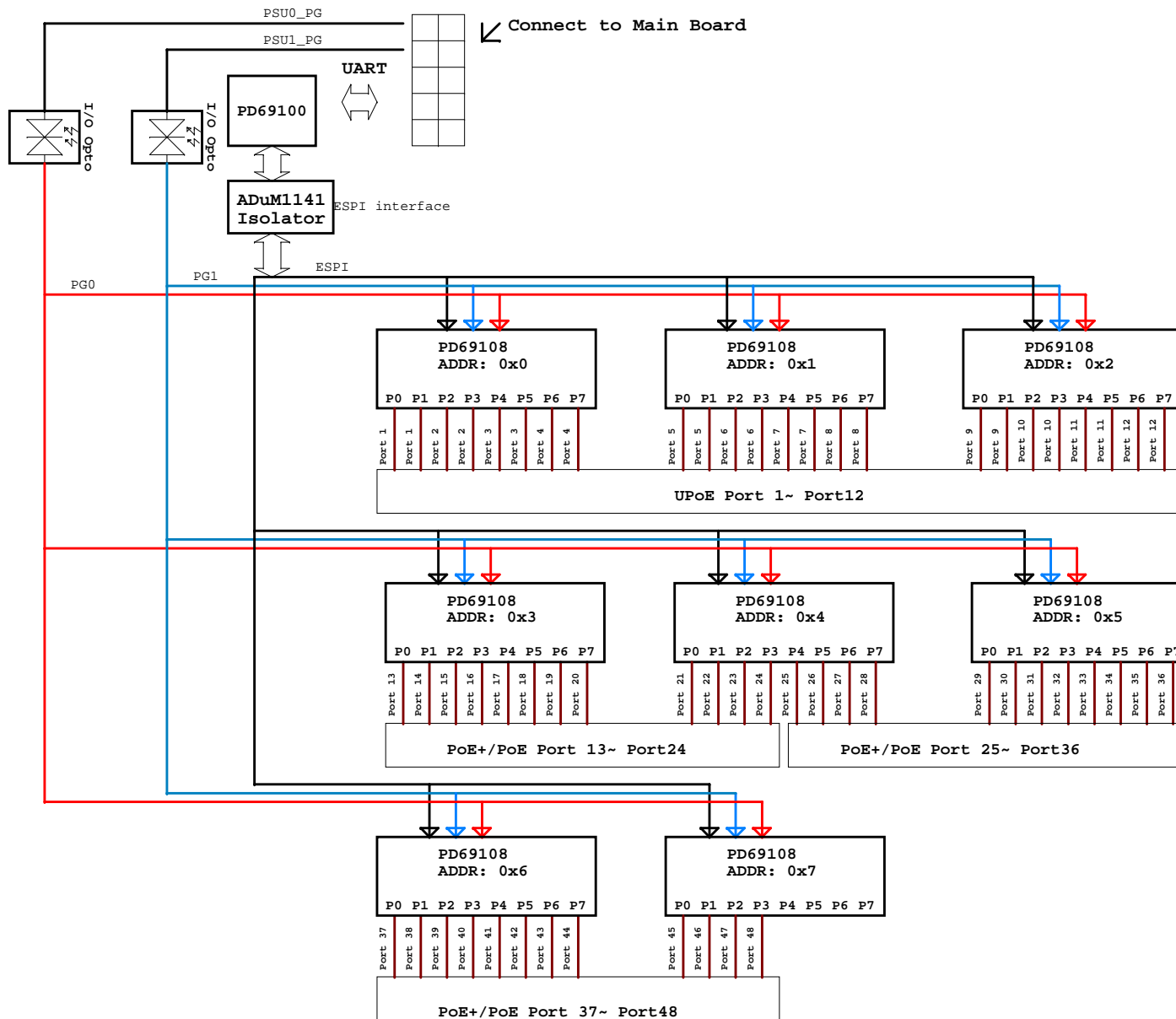
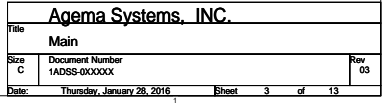


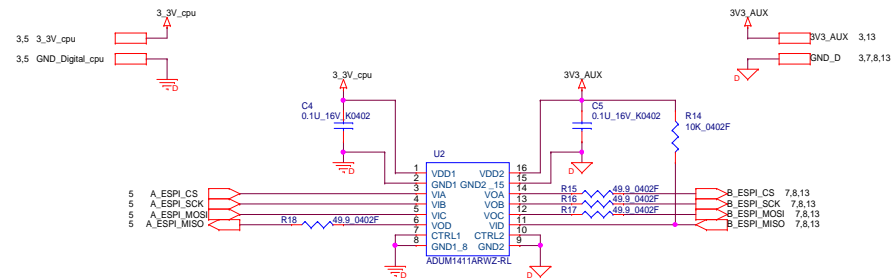
Revision History			DATE
X00_REV01	Created <a href="#">PCB Part Number:</a>		
X00_REV02	PD69104 is replaced with PD69108	Page 3	2012/10/29
	Add notes for 2KV solution	Page 6 to page 13	2012/11/06
X00_REV03	Add line filter to each port	Page 6 to page 13	2012/11/17
	Add J16 for debug	Page 3	2012/11/29
X01_REV01	Add C214, C215 on H1	Page 3	2013/07/19
	C204, C205 change footprint from CS1808 to RS1206 and populate 20M Ohm		
	Change T1 footprint with bigger PTH	Page 3	2013/09/23

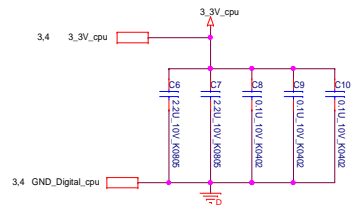
Page Descriptions	
01	Title
02	Block Diagram
03	Main
04	ESPI Isolation
05	PoE controller
06	PoE Manager Slave 1
07	PoE Manager Slave 2
08	PoE Manager Slave 3
09	PoE Manager Slave 4
10	PoE Manager Slave 5
11	PoE Manager Slave 6
12	PoE Manager Slave 7
13	PoE Manager Socket



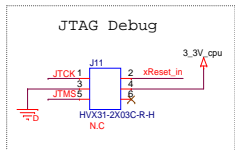
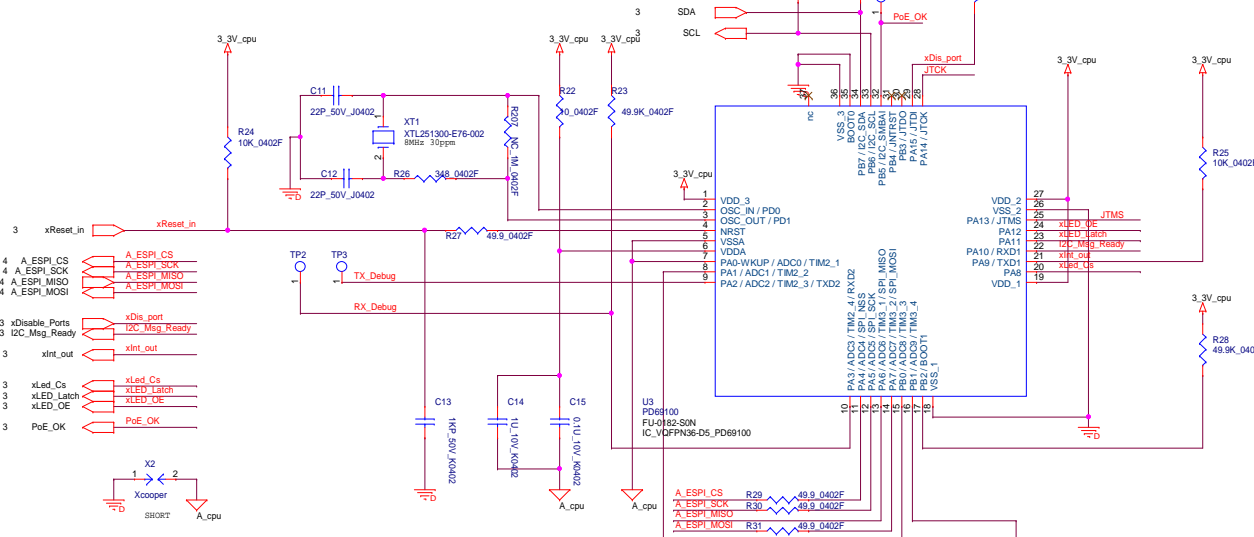
## Isolation



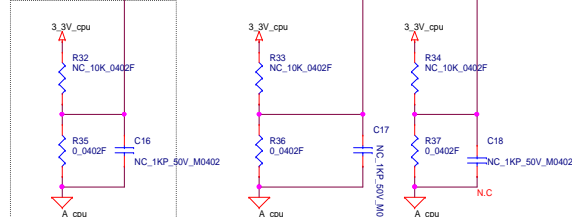




P33: UART\_TX (SCL)  
P34: UART\_RX (SDA)



Rmode



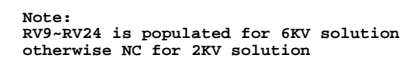
Note:  
R37 need to pull low for UART  
R34 pull up and R37 NC for I2C addr: 0X3C







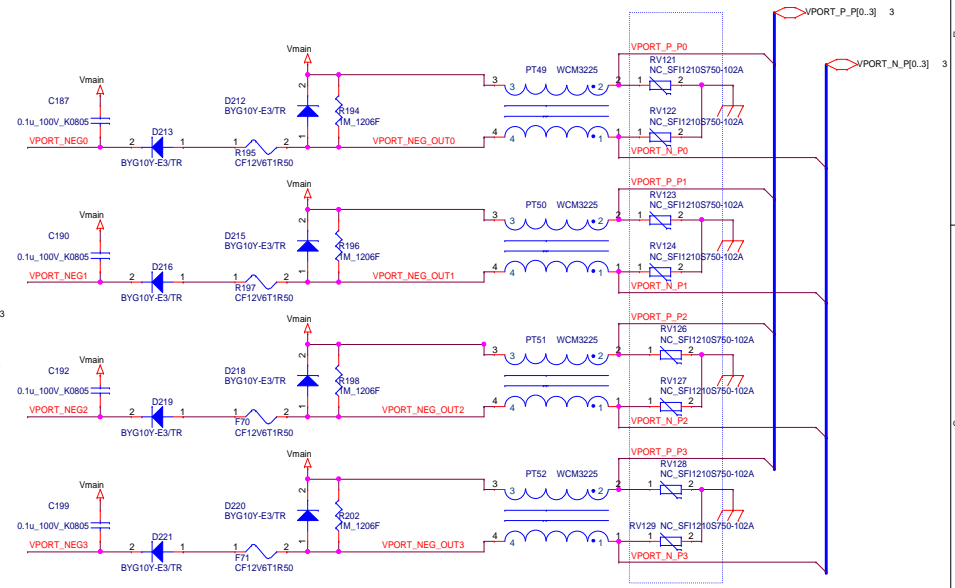
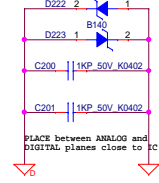
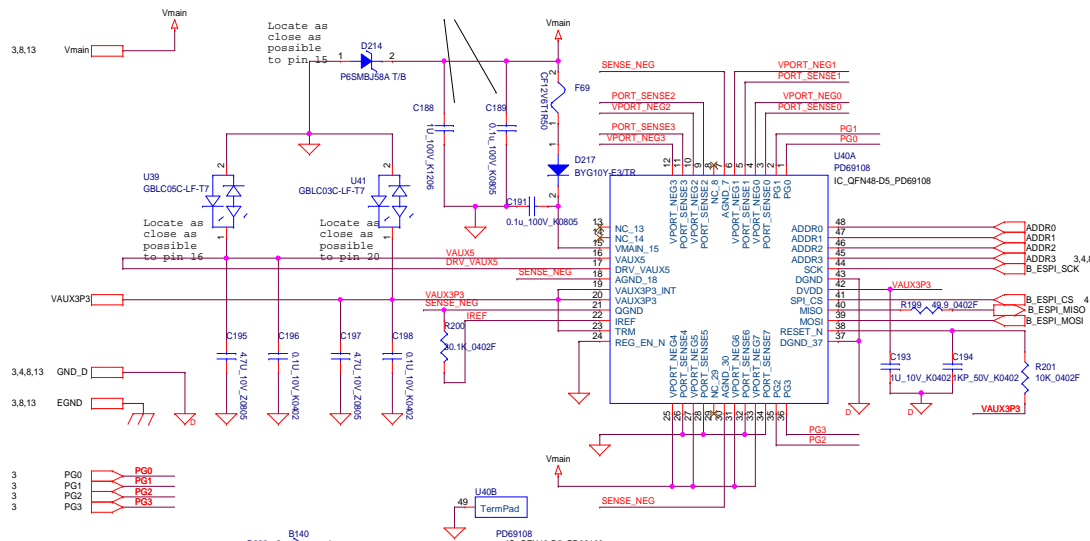








Place Capacitors  
Next to IC pins.  
Connect IC  
pins to thermal pad  
on top layer



Note:  
RV121~RV124,RV126~RV129 is populated for 6KV solution  
otherwise NC for 2KV solution

PD69108 un-used port 4~7:  
pin 26,28,32,34 connect to analog GND  
pin 25,27,31,33 connect to Vmain  
pin 30 connect to SENSE\_NEG  
pin 29 is NC

