
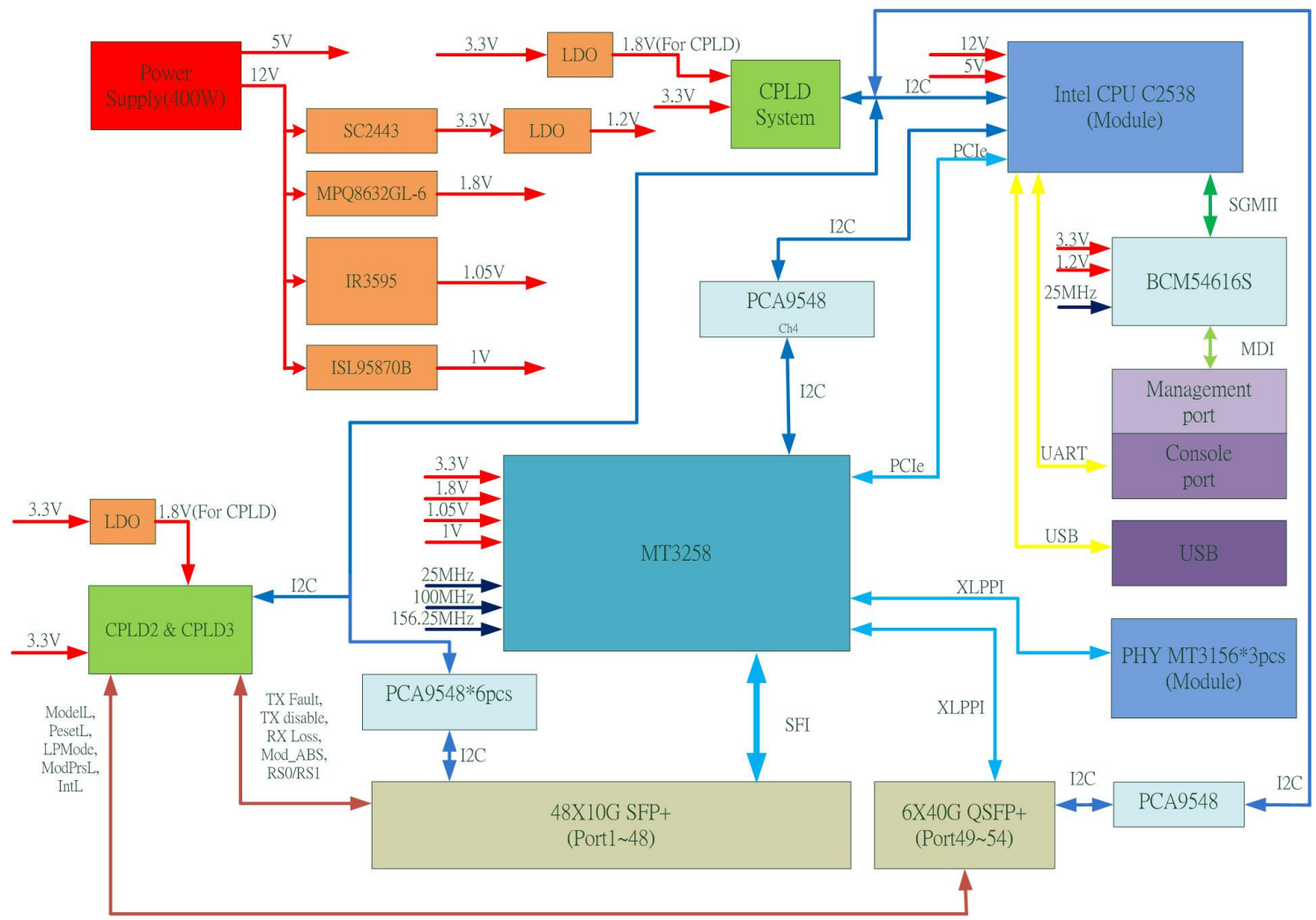


REVISION HISTORY

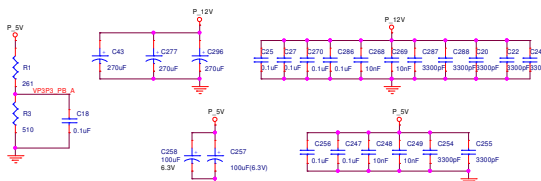
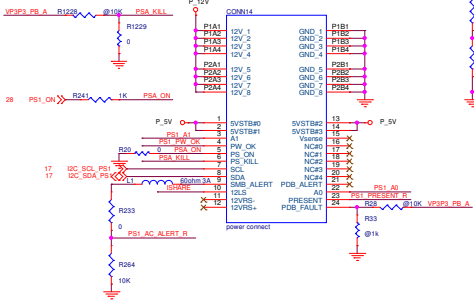
Version	Date	Change Description
ROA	2014/12/10	Initial Design
	2014/12/29	1. Reserve option : The signals (MT3258_ROV0, MT3258_ROV1, MT3258_ROV2) connected the MT3258-GPIO_5, GPIO_6, GPIO_7 2. Reserve 3.3V to IR3584 for power sequence on page 4 3. Modify 156.25MHz buffer solution from 8SLVP1208ANEIG18 to ZL40205LDF1 for cost consideration on page 16
	2015/1/6	1. Reference to the changelist file AS5510-54X Schematic Review Comment_v0.3_20141230a.xlsx
	2015/3/16	1. Reference to the changelist file AS5510-54X Schematic Review Comment_v0.7_20141230a.xlsx 2. Re-design 1.05V with IR3595 solution on page 4 and 6. 3. Change CPLD System to 5M5702T144C5N and add DC-DC (3.3V --> 1.8V) for CPLD System on page 28 4. Change CPLD 2 to 5M12702F256C5N and add DC-DC (3.3V --> 1.8V) for CPLD 2 on page 29 5. Change CPLD 3 to 5M12702F256C5N and add DC-DC (3.3V --> 1.8V) for CPLD 2 on page30 6. Add PCA9548 (U63, U64, U65, U66, U67, U70) for SFP+ I2C interface and add PCA9548 U69 for QSFP+ on page 29, 30 7. Add buffer SN74LVLC1G125DBVR (U71) for 54616_MDC 8. Add R2178, R2179 to co-layout for USB interface 9. Add heatsink HS1 and C2403, C2404, C2405, C2406 10. Add CONN37 for IR3595 download code 11. Change CPLD2(U14), CPLD3(U10) pin assignment for PCB layout
	2015/6/18	1. Meet power sequence 3.3V-->1.8V-->1.05V&1V a. C1812 modified from 2042244KC101H(220nF) to 2042734JH110A(27nF) b. C132 modified from 2041043KC100E(0.1uF) to 2042233KC110E(22nF) 2. Setup default voltage 1.05V a. Mount R1973, R1978 (4.7K) b. Mount R1970, R1986 (1K)
	2015/8/14	1. Add LPC interface to CPLDs on page 28, 29, 30 (Refer to watchet blue rectangle). 2. MT3258 configuration for 720G/960G on page 7, 8 (refer to orange rectangle).

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History			
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C	NEPHOS NPH4806 MAINBOARD ROB		
Date:	Wednesday, 26/11/2015	Sheet	1 of 24



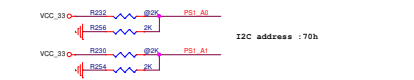
		No. 1, Gusheng Rd., Binchen Science Park Hangzhou City, Zhejiang 310012, P.R.C. TEL: +86-571-86707868 FAX: +86-571-86707810	
Block Diagram			
Title NEPHOS NPH4806 MAINBOARD ROB	Document Number NPH4806-ROB-001	Rev 1.0	Date 2013.11.20

PSU Connector (LEFT SIDE)



Slave address will be 0x70 (default), 0x72, 0x74, 0x76
For non-redundant power supply, the slave address for I2C function should be 0x70.

System addressing Address1 (A1) Address0 (A0)	00	0/1	1/0	1/1
Power supply I2C slave address	70h	72h	74h	76h



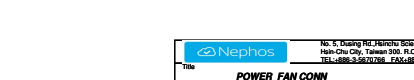
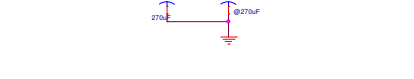
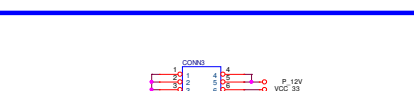
PS1 Present, Active Low	PS1_PRESENT_R	15	R239	PS1_PRESENT	28
PSU Present	PS1_PRESENT	0	PS1_PRESENT	28	PSU Present
PSU 12V Power Good, Active Low	PS1_12V_PG	15	R239	PS1_12V_PG	28
PSU 12V Power Good	PS1_12V_PG	0	PS1_12V_PG	28	PSU 12V Power Good
PSU 12V Power Fail	PS1_12V_PG	1	PS1_12V_PG	28	PSU 12V Power Fail
Active Low	PS1_AC_ALERT_R	15	R239	PS1_AC_ALERT_H	28
PSU POWER Fail	PS1_AC_ALERT_H	0	PS1_AC_ALERT_H	28	PSU POWER Fail



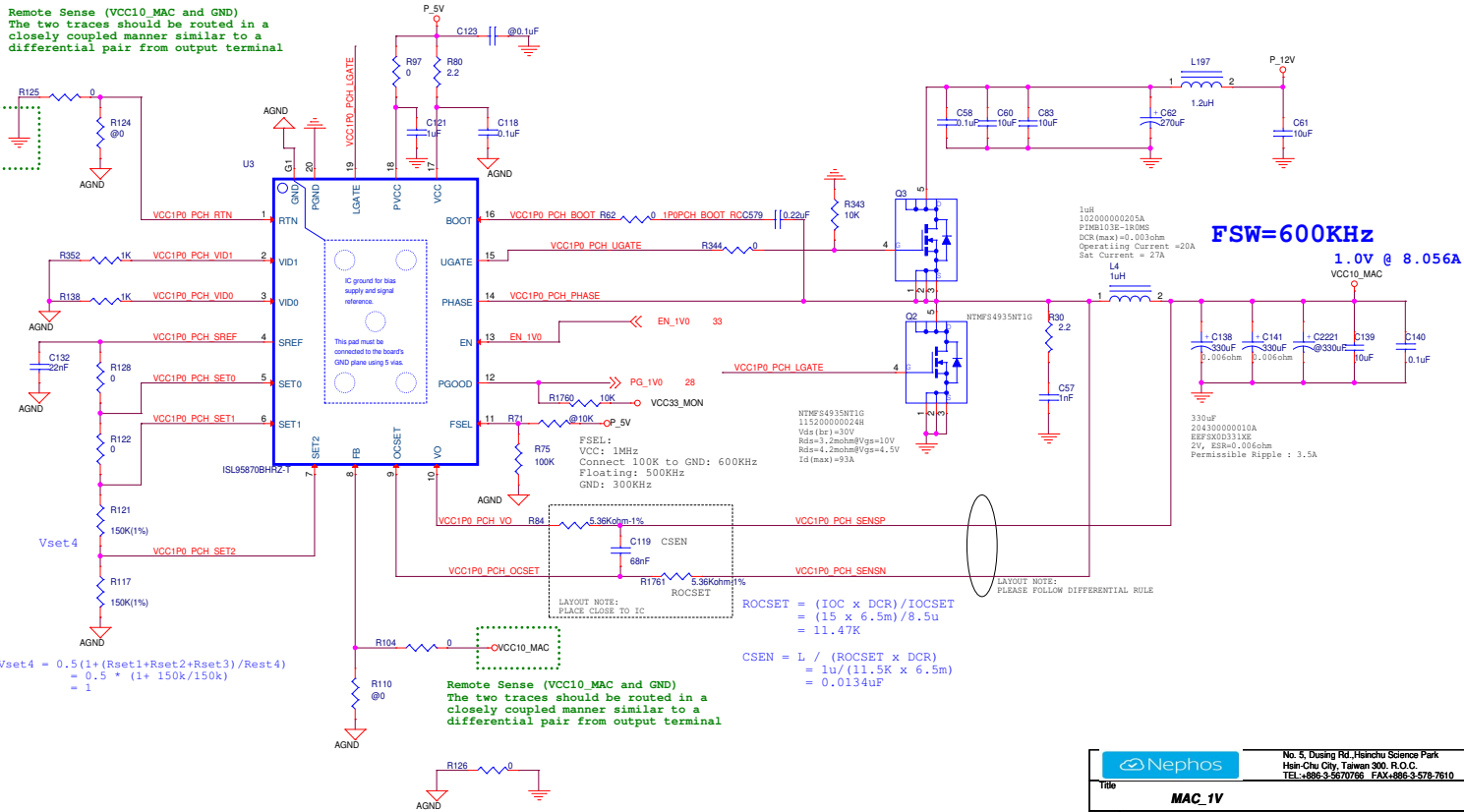
PS2 Present, Active Low	PS2_PRESENT_R	15	R239	PS2_PRESENT	28
PSU Present	PS2_PRESENT	0	PS2_PRESENT	28	PSU Present
PSU 12V Power Good, Active Low	PS2_12V_PG	15	R239	PS2_12V_PG	28
PSU 12V Power Good	PS2_12V_PG	0	PS2_12V_PG	28	PSU 12V Power Good
PSU 12V Power Fail	PS2_12V_PG	1	PS2_12V_PG	28	PSU 12V Power Fail
Active Low	PS2_AC_ALERT_R	15	R239	PS2_AC_ALERT_H	28
PSU POWER Fail	PS2_AC_ALERT_H	0	PS2_AC_ALERT_H	28	PSU POWER Fail

Slave address will be 0x70 (default), 0x72, 0x74, 0x76
For non-redundant power supply, the slave address for I2C function should be 0x70.

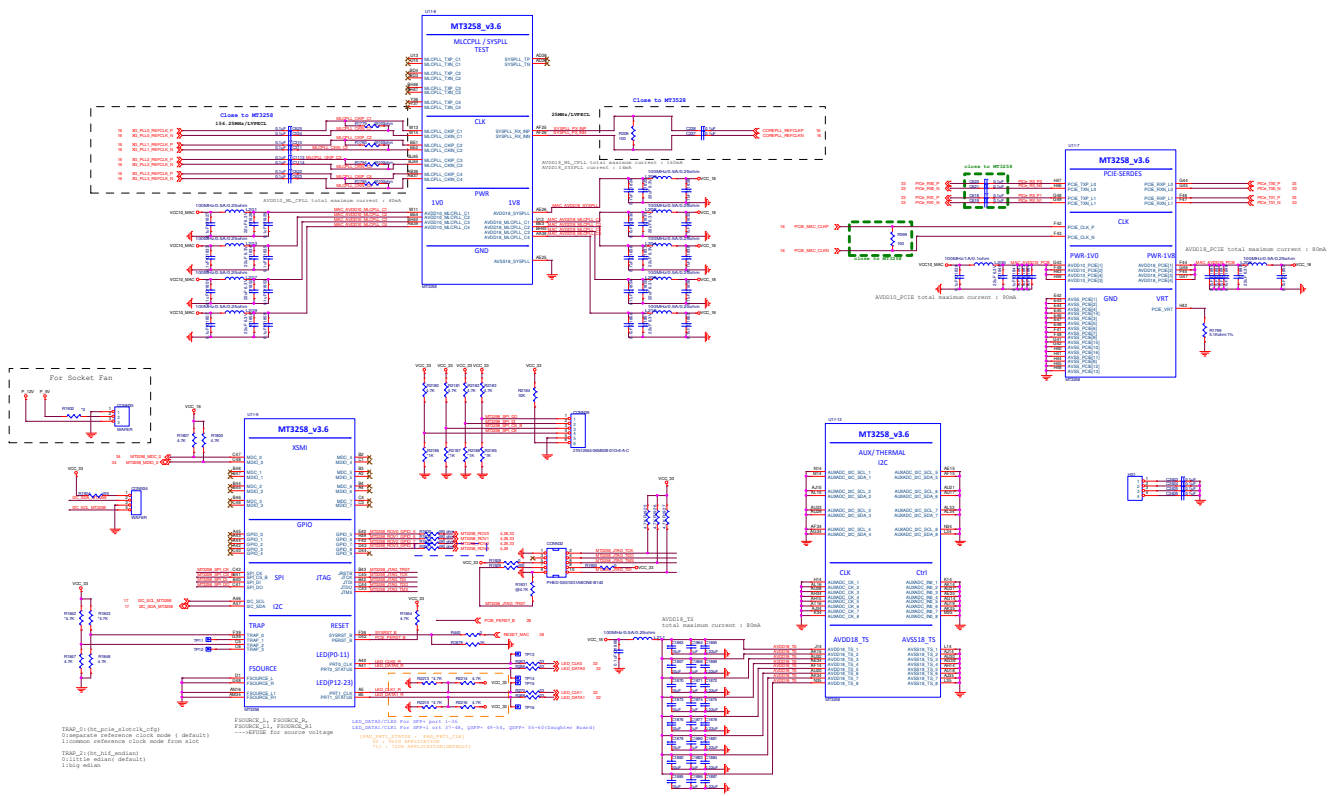
System addressing Address1 (A1) Address0 (A0)	00	0/1	1/0	1/1
Power supply I2C slave address	70h	72h	74h	76h



Remote Sense (VCC10_MAC and GND)
The two traces should be routed in a closely coupled manner similar to a differential pair from output terminal



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MAC 1V			
Title MAC 1V	Document Number NEPHOS NPH4806 MAINBOARD ROB	Rev ROB	Date: Wednesday, July 13, 2016
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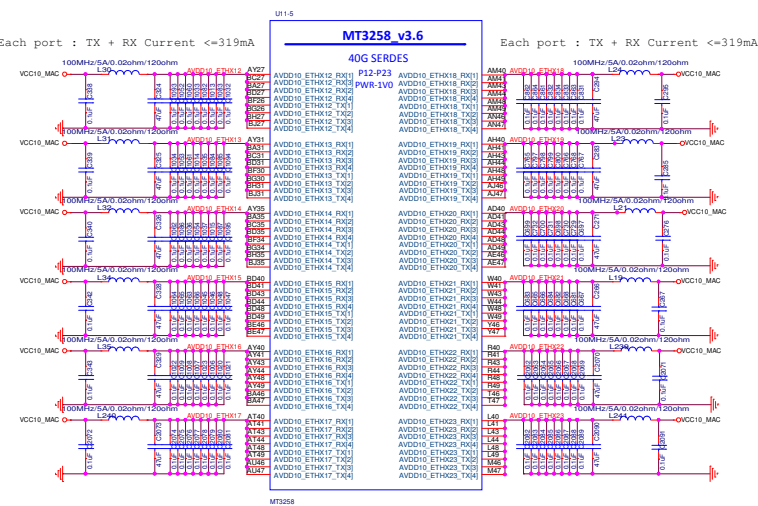
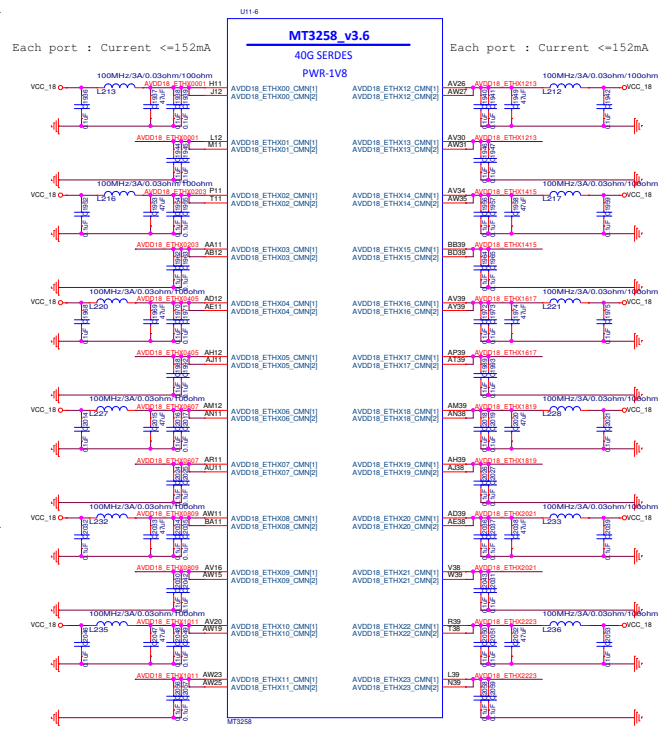
U11-1

MT3258_v3.6

40G SERDES

PO-P7

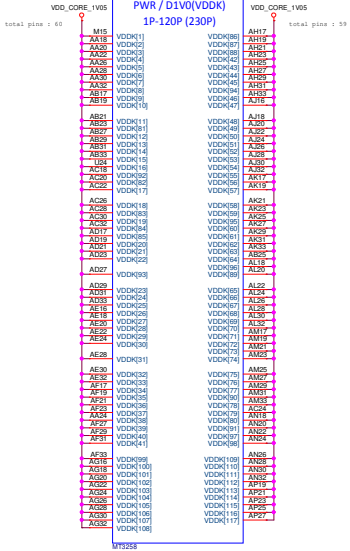
34	ETH000_RXPD	0.5UF	C1889	ETH000_RXPD_C	J8	ETH000_RXPD0	31
34	ETH000_RXND	0.5UF	C1890	ETH000_RXND_C	J9	ETH000_RXND0	32
34	ETH000_RXM1	0.5UF	C1891	ETH000_RXM1_C	J10	ETH000_RXM10	33
34	ETH000_RXM2	0.5UF	C1892	ETH000_RXM2_C	J11	ETH000_RXM20	34
34	ETH000_RXM3	0.5UF	C1893	ETH000_RXM3_C	J12	ETH000_RXM30	35
34	ETH000_RXM4	0.5UF	C1894	ETH000_RXM4_C	J13	ETH000_RXM40	36
34	ETH000_RXM5	0.5UF	C1895	ETH000_RXM5_C	J14	ETH000_RXM50	37
34	ETH000_RXM6	0.5UF	C1896	ETH000_RXM6_C	J15	ETH000_RXM60	38
34	ETH000_RXM7	0.5UF	C1897	ETH000_RXM7_C	J16	ETH000_RXM70	39
34	ETH000_RXM8	0.5UF	C1898	ETH000_RXM8_C	J17	ETH000_RXM80	40
34	ETH000_RXM9	0.5UF	C1899	ETH000_RXM9_C	J18	ETH000_RXM90	41
34	ETH000_RXM10	0.5UF	C1900	ETH000_RXM10_C	J19	ETH000_RXM100	42
34	ETH000_RXM11	0.5UF	C1901	ETH000_RXM11_C	J20	ETH000_RXM110	43
34	ETH000_RXM12	0.5UF	C1902	ETH000_RXM12_C	J21	ETH000_RXM120	44
34	ETH000_RXM13	0.5UF	C1903	ETH000_RXM13_C	J22	ETH000_RXM130	45
34	ETH000_RXM14	0.5UF	C1904	ETH000_RXM14_C	J23	ETH000_RXM140	46
34	ETH000_RXM15	0.5UF	C1905	ETH000_RXM15_C	J24	ETH000_RXM150	47
34	ETH000_RXM16	0.5UF	C1906	ETH000_RXM16_C	J25	ETH000_RXM160	48
34	ETH000_RXM17	0.5UF	C1907	ETH000_RXM17_C	J26	ETH000_RXM170	49
34	ETH000_RXM18	0.5UF	C1908	ETH000_RXM18_C	J27	ETH000_RXM180	50
34	ETH000_RXM19	0.5UF	C1909	ETH000_RXM19_C	J28	ETH000_RXM190	51
34	ETH000_RXM20	0.5UF	C1910	ETH000_RXM20_C	J29	ETH000_RXM200	52
34	ETH000_RXM21	0.5UF	C1911	ETH000_RXM21_C	J30	ETH000_RXM210	53
34	ETH000_RXM22	0.5UF	C1912	ETH000_RXM22_C	J31	ETH000_RXM220	54
34	ETH000_RXM23	0.5UF	C1913	ETH000_RXM23_C	J32	ETH000_RXM230	55
34	ETH000_RXM24	0.5UF	C1914	ETH000_RXM24_C	J33	ETH000_RXM240	56
34	ETH000_RXM25	0.5UF	C1915	ETH000_RXM25_C	J34	ETH000_RXM250	57
34	ETH000_RXM26	0.5UF	C1916	ETH000_RXM26_C	J35	ETH000_RXM260	58
34	ETH000_RXM27	0.5UF	C1917	ETH000_RXM27_C	J36	ETH000_RXM270	59
34	ETH000_RXM28	0.5UF	C1918	ETH000_RXM28_C	J37	ETH000_RXM280	60
34	ETH000_RXM29	0.5UF	C1919	ETH000_RXM29_C	J38	ETH000_RXM290	61
34	ETH000_RXM30	0.5UF	C1920	ETH000_RXM30_C	J39	ETH000_RXM300	62
34	ETH000_RXM31	0.5UF	C1921	ETH000_RXM31_C	J40	ETH000_RXM310	63
34	ETH000_RXM32	0.5UF	C1922	ETH000_RXM32_C	J41	ETH000_RXM320	64
34	ETH000_RXM33	0.5UF	C1923	ETH000_RXM33_C	J42	ETH000_RXM330	65
34	ETH000_RXM34	0.5UF	C1924	ETH000_RXM34_C	J43	ETH000_RXM340	66
34	ETH000_RXM35	0.5UF	C1925	ETH000_RXM35_C	J44	ETH000_RXM350	67
34	ETH000_RXM36	0.5UF	C1926	ETH000_RXM36_C	J45	ETH000_RXM360	68
34	ETH000_RXM37	0.5UF	C1927	ETH000_RXM37_C	J46	ETH000_RXM370	69
34	ETH000_RXM38	0.5UF	C1928	ETH000_RXM38_C	J47	ETH000_RXM380	70
34	ETH000_RXM39	0.5UF	C1929	ETH000_RXM39_C	J48	ETH000_RXM390	71
34	ETH000_RXM40	0.5UF	C1930	ETH000_RXM40_C	J49	ETH000_RXM400	72
34	ETH000_RXM41	0.5UF	C1931	ETH000_RXM41_C	J50	ETH000_RXM410	73
34	ETH000_RXM42	0.5UF	C1932	ETH000_RXM42_C	J51	ETH000_RXM420	74
34	ETH000_RXM43	0.5UF	C1933	ETH000_RXM43_C	J52	ETH000_RXM430	75
34	ETH000_RXM44	0.5UF	C1934	ETH000_RXM44_C	J53	ETH000_RXM440	76
34	ETH000_RXM45	0.5UF	C1935	ETH000_RXM45_C	J54	ETH000_RXM450	77
34	ETH000_RXM46	0.5UF	C1936	ETH000_RXM46_C	J55	ETH000_RXM460	78
34	ETH000_RXM47	0.5UF	C1937	ETH000_RXM47_C	J56	ETH000_RXM470	79
34	ETH000_RXM48	0.5UF	C1938	ETH000_RXM48_C	J57	ETH000_RXM480	80
34	ETH000_RXM49	0.5UF	C1939	ETH000_RXM49_C	J58	ETH000_RXM490	81
34	ETH000_RXM50	0.5UF	C1940	ETH000_RXM50_C	J59	ETH000_RXM500	82
34	ETH000_RXM51	0.5UF	C1941	ETH000_RXM51_C	J60	ETH000_RXM510	83
34	ETH000_RXM52	0.5UF	C1942	ETH000_RXM52_C	J61	ETH000_RXM520	84
34	ETH000_RXM53	0.5UF	C1943	ETH000_RXM53_C	J62	ETH000_RXM530	85
34	ETH000_RXM54	0.5UF	C1944	ETH000_RXM54_C	J63	ETH000_RXM540	86
34	ETH000_RXM55	0.5UF	C1945	ETH000_RXM55_C	J64	ETH000_RXM550	87
34	ETH000_RXM56	0.5UF	C1946	ETH000_RXM56_C	J65	ETH000_RXM560	88
34	ETH000_RXM57	0.5UF	C1947	ETH000_RXM57_C	J66	ETH000_RXM570	89
34	ETH000_RXM58	0.5UF	C1948	ETH000_RXM58_C	J67	ETH000_RXM580	90
34	ETH000_RXM59	0.5UF	C1949	ETH000_RXM59_C	J68	ETH000_RXM590	91
34	ETH000_RXM60	0.5UF	C1950	ETH000_RXM60_C	J69	ETH000_RXM600	92
34	ETH000_RXM61	0.5UF	C1951	ETH000_RXM61_C	J70	ETH000_RXM610	93
34	ETH000_RXM62	0.5UF	C1952	ETH000_RXM62_C	J71	ETH000_RXM620	94
34	ETH000_RXM63	0.5UF	C1953	ETH000_RXM63_C	J72	ETH000_RXM630	95
34	ETH000_RXM64	0.5UF	C1954	ETH000_RXM64_C	J73	ETH000_RXM640	96
34	ETH000_RXM65	0.5UF	C1955	ETH000_RXM65_C	J74	ETH000_RXM650	97
34	ETH000_RXM66	0.5UF	C1956	ETH000_RXM66_C	J75	ETH000_RXM660	98
34	ETH000_RXM67	0.5UF	C1957	ETH000_RXM67_C	J76	ETH000_RXM670	99
34	ETH000_RXM68	0.5UF	C1958	ETH000_RXM68_C	J77	ETH000_RXM680	100
34	ETH000_RXM69	0.5UF	C1959	ETH000_RXM69_C	J78	ETH000_RXM690	101
34	ETH000_RXM70	0.5UF	C1960	ETH000_RXM70_C	J79	ETH000_RXM700	102
34	ETH000_RXM71	0.5UF	C1961	ETH000_RXM71_C	J80	ETH000_RXM710	103
34	ETH000_RXM72	0.5UF	C1962	ETH000_RXM72_C	J81	ETH000_RXM720	104
34	ETH000_RXM73	0.5UF	C1963	ETH000_RXM73_C	J82	ETH000_RXM730	105
34	ETH000_RXM74	0.5UF	C1964	ETH000_RXM74_C	J83	ETH000_RXM740	106
34	ETH000_RXM75	0.5UF	C1965	ETH000_RXM75_C	J84	ETH000_RXM750	107
34	ETH000_RXM76	0.5UF	C1966	ETH000_RXM76_C	J85	ETH000_RXM760	108
34	ETH000_RXM77	0.5UF	C1967	ETH000_RXM77_C	J86	ETH000_RXM770	109
34	ETH000_RXM78	0.5UF	C1968	ETH000_RXM78_C	J87	ETH000_RXM780	110
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34	ETH000_RXM80	0.5UF	C1970	ETH000_RXM80_C	J89	ETH000_RXM800	112
34	ETH000_RXM81	0.5UF	C1971	ETH000_RXM81_C	J90	ETH000_RXM810	113
34	ETH000_RXM82	0.5UF	C1972	ETH000_RXM82_C	J91	ETH000_RXM820	114
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34	ETH000_RXM84	0.5UF	C1974	ETH000_RXM84_C	J93	ETH000_RXM840	116
34	ETH000_RXM85	0.5UF	C1975	ETH000_RXM85_C	J94	ETH000_RXM850	117
34	ETH000_RXM86	0.5UF	C1976	ETH000_RXM86_C	J95	ETH000_RXM860	118
34	ETH000_RXM87	0.5UF	C1977	ETH000_RXM87_C	J96	ETH000_RXM870	119
34	ETH000_RXM88	0.5UF	C1978	ETH000_RXM88_C	J97	ETH000_RXM880	120
34	ETH000_RXM89	0.5UF	C1979	ETH000_RXM89_C	J98	ETH000_RXM890	121
34	ETH000_RXM90	0.5UF	C1980	ETH000_RXM90_C	J99	ETH000_RXM900	122
34	ETH000_RXM91	0.5UF	C1981	ETH000_RXM91_C	J100	ETH000_RXM910	123
34	ETH000_RXM92	0.5UF	C1982	ETH000_RXM92_C	J101	ETH000_RXM920	124
34	ETH000_RXM93	0.5UF	C1983	ETH000_RXM93_C	J102	ETH000_RXM930	125
34	ETH000_RXM94	0.5UF	C1984	ETH000_RXM94_C	J103	ETH000_RXM940	126
34	ETH000_RXM95	0.5UF	C1985	ETH000_RXM95_C	J104	ETH000_RXM950	127
34	ETH000_RXM96	0.5UF	C1986	ETH000_RXM96_C	J105	ETH000_RXM960	128
34	ETH000_RXM97	0.5UF	C1987	ETH000_RXM97_C	J106	ETH000_RXM970	129
34	ETH000_RXM98	0.5UF	C1988	ETH000_RXM98_C	J107	ETH000_RXM980	130
34	ETH000_RXM99	0.5UF	C1989	ETH000_RXM99_C	J108	ETH000_RXM990	131
34	ETH000_RXM100	0.5UF	C1990	ETH000_RXM100_C	J109	ETH000_RXM1000	132
34	ETH000_RXM101	0.5UF	C1991	ETH000_RXM101_C	J110	ETH000_RXM1010	133
34	ETH000_RXM102	0.5UF	C1992	ETH000_RXM102_C	J111	ETH000_RXM1020	134
34	ETH000_RXM103	0.5UF	C1993	ETH000_RXM103_C	J112	ETH000_RXM1030	135
34	ETH000_RXM104	0.5UF	C1994	ETH000_RXM104_C	J113	ETH000_RXM1040	136
34	ETH000_RXM105	0.5UF	C1995	ETH000_RXM105_C	J114	ETH000_RXM1050	137
34	ETH000_RXM106	0.5UF	C1996	ETH000_RXM106_C	J115	ETH000_RXM1060	138
34	ETH000_RXM107	0.5UF	C1997	ETH000_RXM107_C	J116	ETH000_RXM1070	139
34	ETH000_RXM108	0.5UF	C1998	ETH000_RXM108_C	J117	ETH000_RXM1080	140
34	ETH000_RXM109	0.5UF	C1999	ETH000_RXM109_C	J118	ETH000_RXM1090	141
34	ETH000_RXM110	0.5UF	C2000	ETH000_RXM110_C	J119	ETH000_RXM1100	142
34	ETH000_RXM111	0.5UF	C2001	ETH000_RXM111_C	J120	ETH000_RXM1110	143
34	ETH000_RXM112	0.5UF	C2002	ETH000_RXM112_C	J121	ETH000_RXM1120	144
34	ETH000_RXM113	0.5UF	C2003	ETH000_RXM113_C	J122	ETH000_RXM1130	145
34	ETH000_RXM114	0.5UF	C2004	ETH000_RXM114_C	J123	ETH000_RXM1140	146
34	ETH000_RXM115	0.5UF	C2005	ETH000_RXM115_C	J124	ETH000_RXM1150	147
34	ETH000_RXM116	0.5UF	C2006	ETH000_RXM116_C	J125	ETH000_RXM1160	148
34	ETH000_RXM117	0.5UF	C2007	ETH000_RXM117_C	J126	ETH000_RXM1170	149
34	ETH000_RXM118	0.5UF	C2008	ETH000_RXM118_C	J127	ETH000_RXM1180	150
34	ETH000_RXM119	0.5UF	C2009	ETH000_RXM119_C	J128	ETH000_RXM1190	151
34	ETH000_RXM120	0.5UF	C2010	ETH000_RXM120_C	J129	ETH000_RXM1200	152
34	ETH000_RXM121	0.5UF	C2011	ETH000_RXM121_C	J130	ETH000_RXM1210	153
34	ETH000_RXM122	0.5UF	C2012	ETH000_RXM122_C	J131	ETH000_RXM1220	154
34	ETH000_RXM123	0.5UF	C2013	ETH000_RXM123_C	J132	ETH000_RXM1230	155
34	ETH000_RXM124	0.5UF	C2014	ETH000_RXM124_C	J133	ETH000_RXM1240	156
34	ETH000_RXM125	0.5UF	C2015	ETH000_RXM125_C	J134	ETH000_RXM1250	157
34	ETH000_RXM126	0.5UF	C2016	ETH000_RXM126_C	J135	ETH000_RXM1260	158
34	ETH000_RXM127	0.5UF	C2017	ETH000_RXM127_C	J136	ETH000_RXM1270	159
34	ETH000_RXM128	0.5UF	C2018	ETH000_RXM128_C	J137	ETH000_RXM1280	160
34	ETH000_RXM129	0.5UF	C2019	ETH000_RXM129_C	J138	ETH000_RXM1290	161
34	ETH000_RXM130	0.5UF	C2020	ETH000_RXM130_C	J139	ETH000_RXM1300	162
34	ETH000_RXM131	0.5UF	C2021	ETH000_RXM131_C	J140	ETH000_RXM1310	163
34	ETH000_RXM132	0.5UF	C2022	ETH000_RXM132_C	J141	ETH000_RXM1320	164
34	ETH000_RXM133	0.5UF	C2023	ETH000_RXM133_C	J142	ETH000_RXM1330	165
34	ETH000_RXM134	0.5UF	C2024	ETH000_RXM134_C	J143	ETH000_RXM1340	166
34	ETH000_RXM135	0.5UF	C2025	ETH000_RXM135_C	J144	ETH000_RXM1350	167
34	ETH000_RXM136	0.5UF	C2026	ETH000_RXM136_C	J145	ETH000_RXM1360	168
34	ETH000_RXM137	0.5UF	C2027	ETH000_RXM137_C	J146	ETH000_RXM1370	169
34	ETH000_RXM138	0.5UF	C2028	ETH000_RXM138_C	J147	ETH000_RXM1380	170
34	ETH000_RXM139	0.5UF	C2029	ETH000_RXM139_C	J148	ETH000_RXM1390	171
34	ETH						



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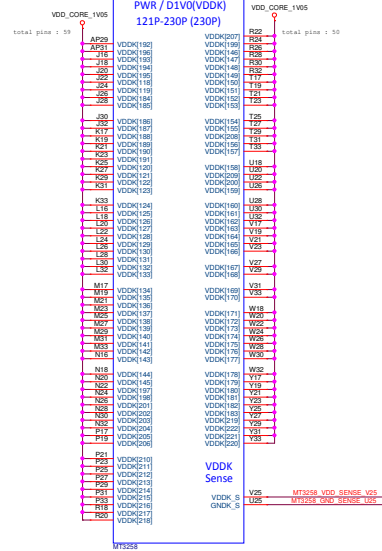
PWR / D1V0(VDDK)
1P-120P (230P)



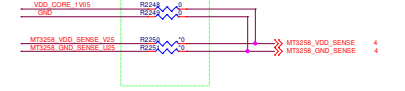
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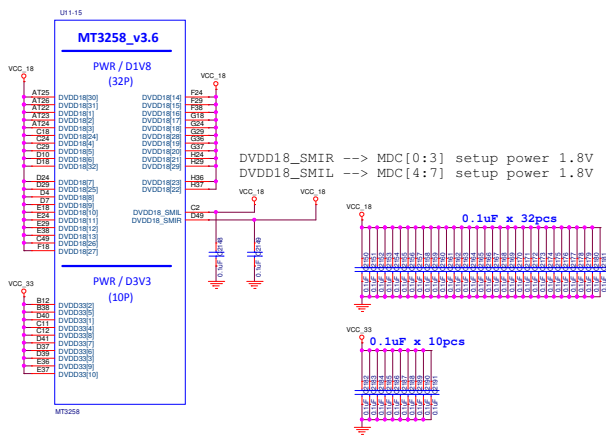
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PWR / D1V0(VDDK)
121P-230P (230P)



To connect C901 for sense the lowest Voltage :





MT3258_v3.6

MT3258_v3.6

MT3258_v3.6

MT3258_v3.6

AVSS10
1P-200P (773p)

AVSS10
201P-400P (773p)

AVSS10
401P-600P (773p)

AVSS
601P-773P (773p)

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AA18	AVSS10 ETH06	AVSS10 ETH006	AG16
AA19	AVSS10 ETH07	AVSS10 ETH007	AG17
AA20	AVSS10 ETH08	AVSS10 ETH008	AG18
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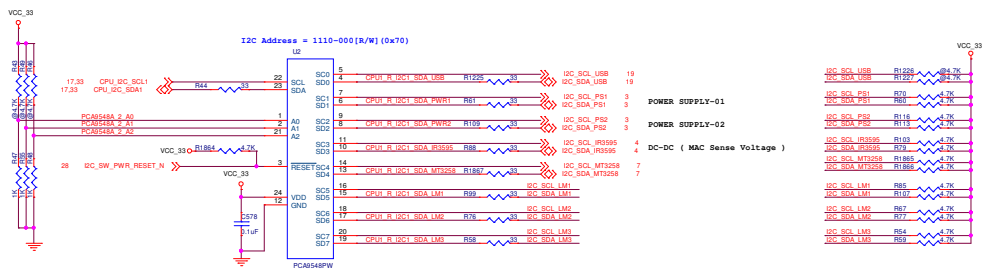
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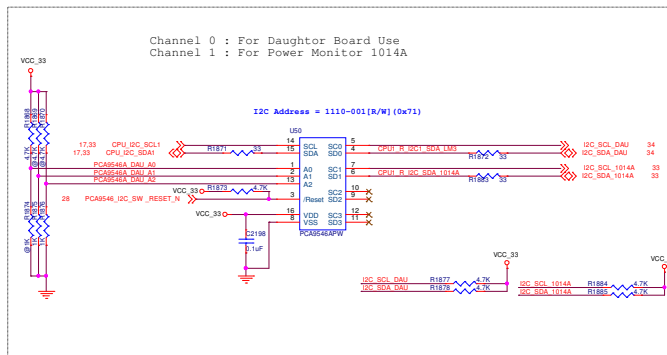
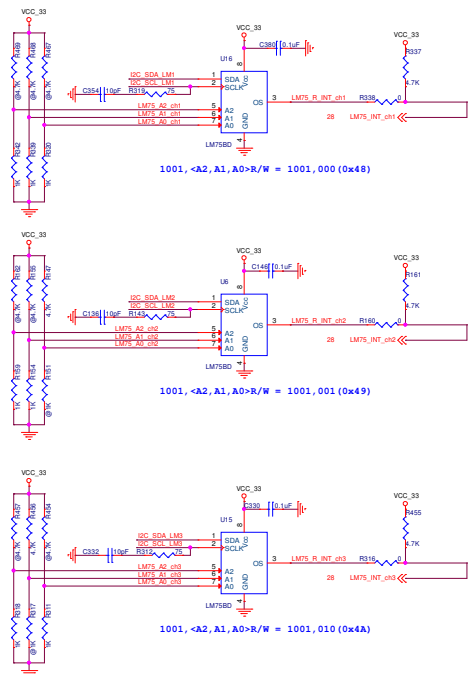
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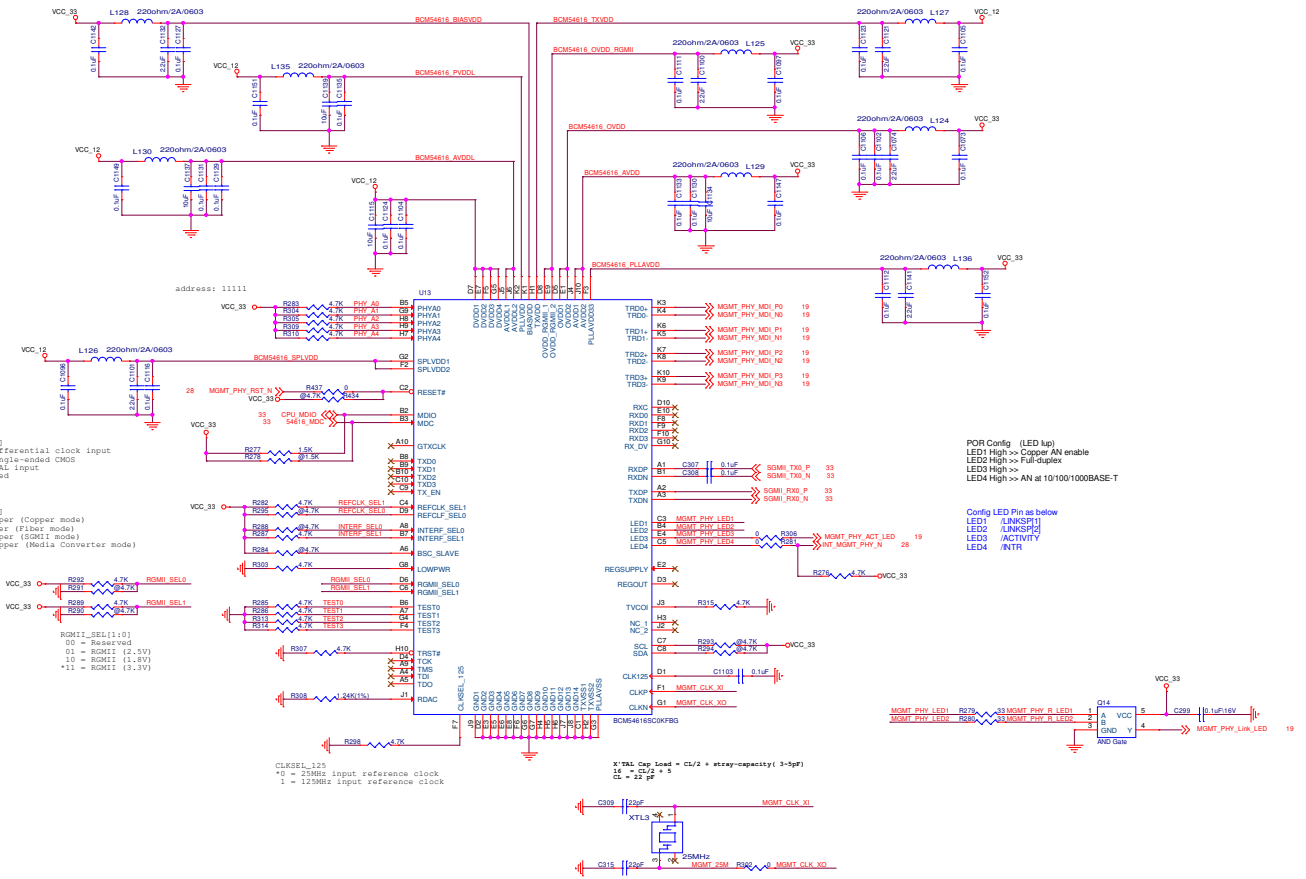
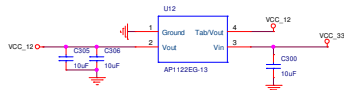
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A150	GNDK240	A259	GNDK240
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A156	GNDK246	A265	GNDK246
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A307	GNDK397	A416	GNDK397
A308	GNDK398	A417	GNDK398
A309	GNDK399	A418	GNDK399
A310			



THERMAL SENSOR





REFCLK_SEL [1:0]
 00 = Select differential clock input
 01 = Select single-ended CMOS
 10 = Select XVAL input
 11 = Not allowed

INTERF_SEL [1:0]
 00 = RGMII-Copper (Copper mode)
 01 = RGMII-Fiber (Fiber mode)
 10 = SGMII-Copper (SGMII mode)
 11 = SerDes-Copper (Media Converter mode)

RGMII_SEL [1:0]
 00 = Reserved
 01 = RGMII (2.5V)
 10 = RGMII (1.8V)
 11 = RGMII (3.3V)

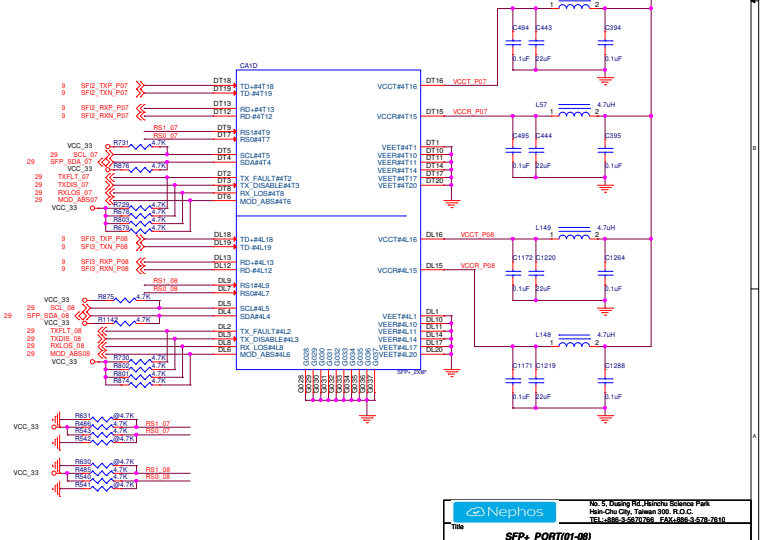
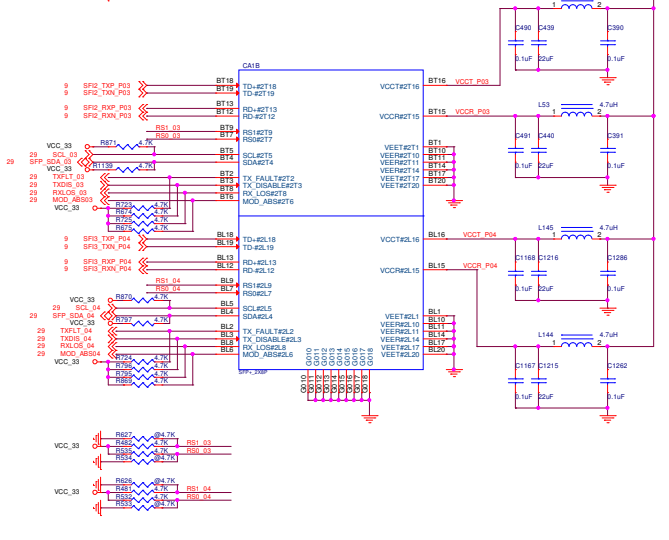
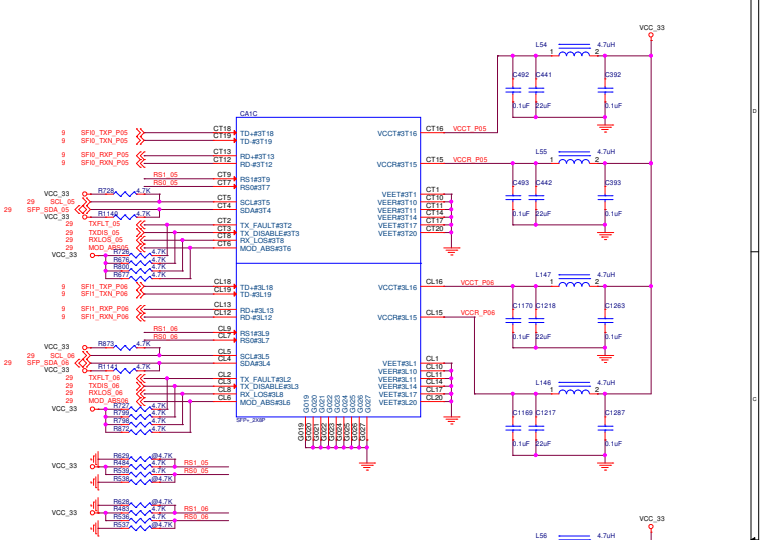
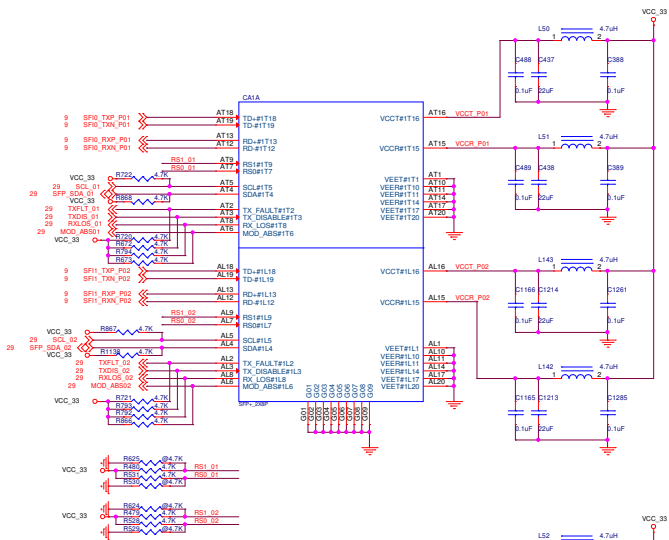
CLKSEL_125
 *0 = 25MHz input reference clock
 *1 = 125MHz input reference clock

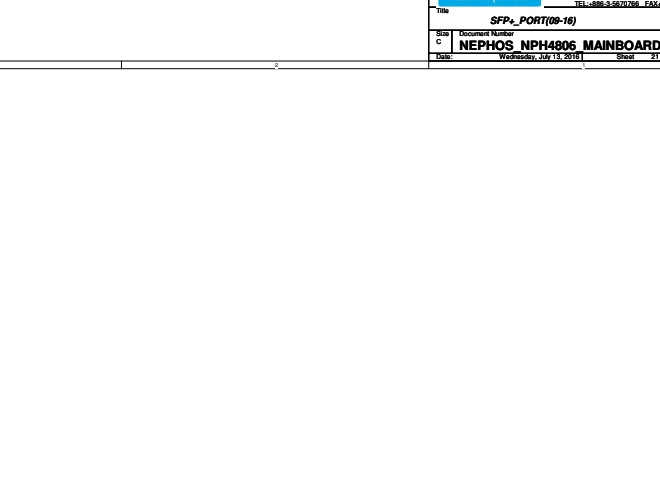
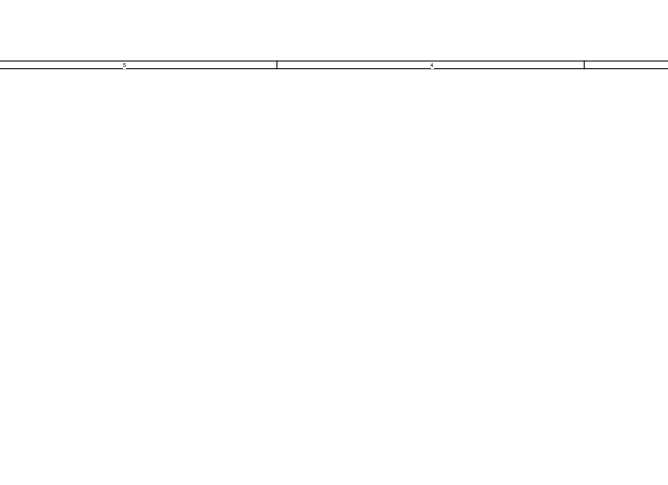
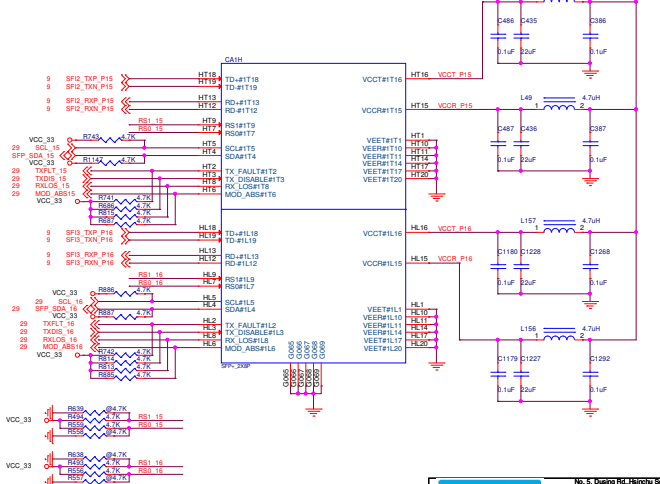
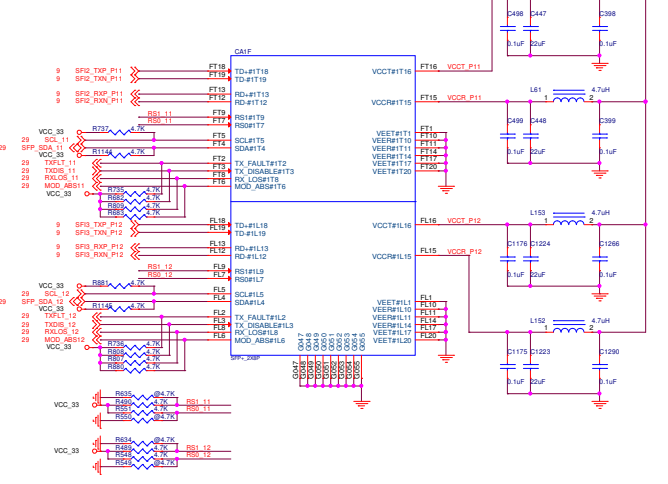
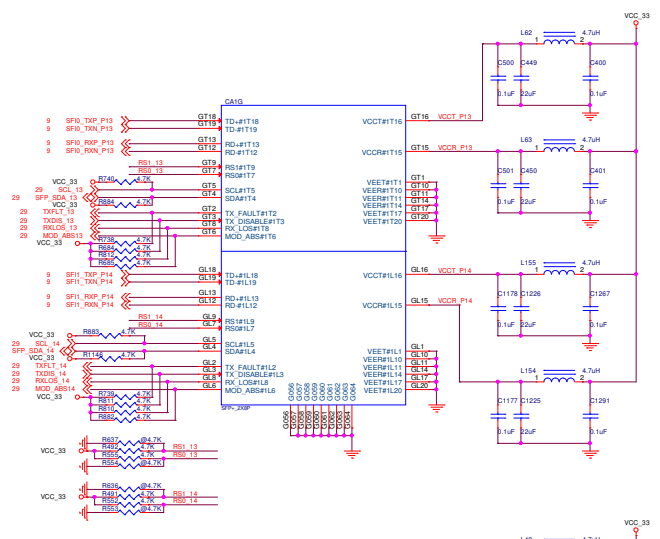
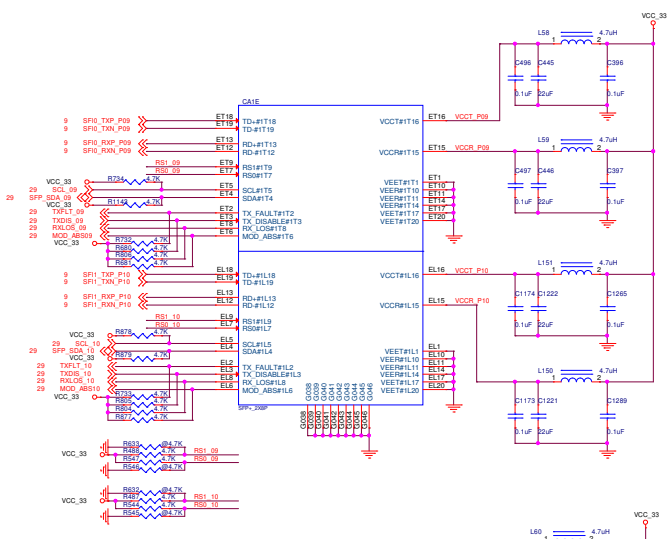
X-TAL Cap Load = $CL/2 + \text{stray-capacity}$; 3-9pF
 $CL = \frac{25}{f} = 5$
 $CL = 12 \text{ pF}$

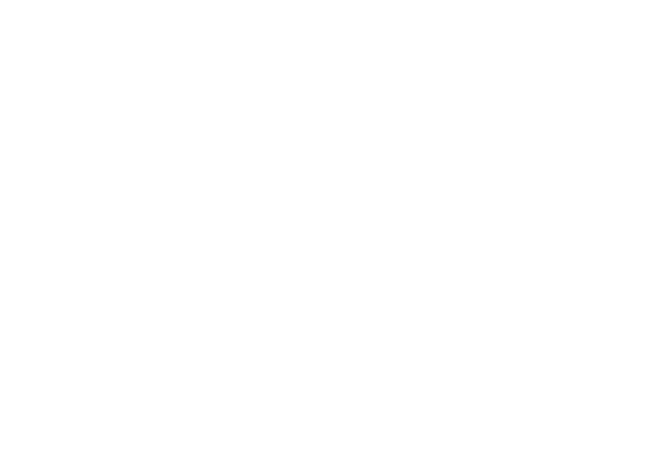
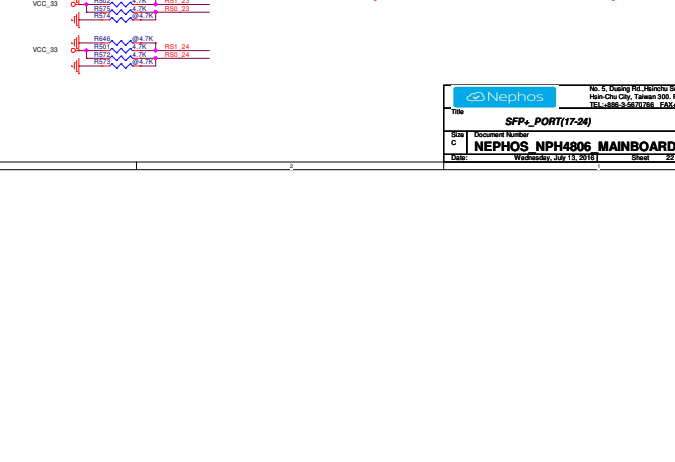
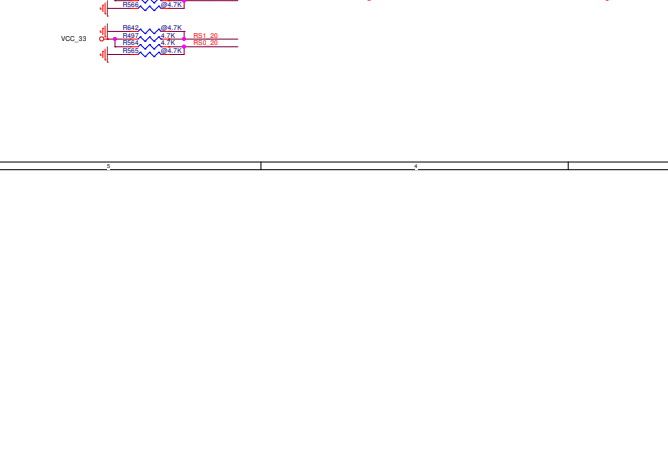
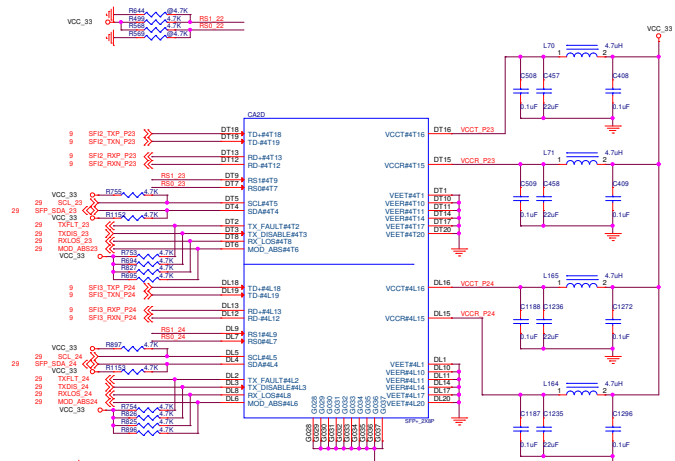
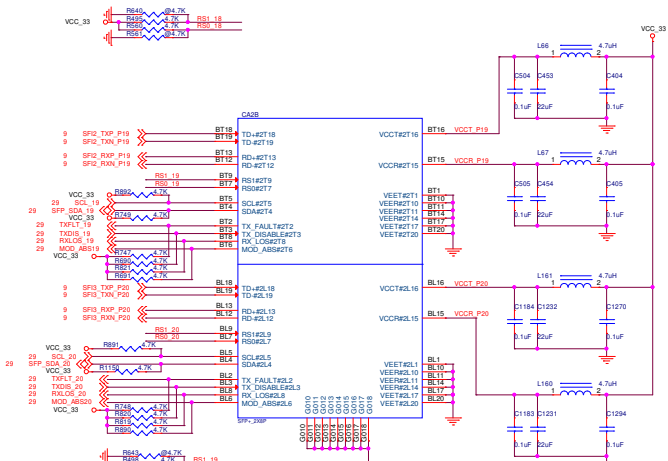
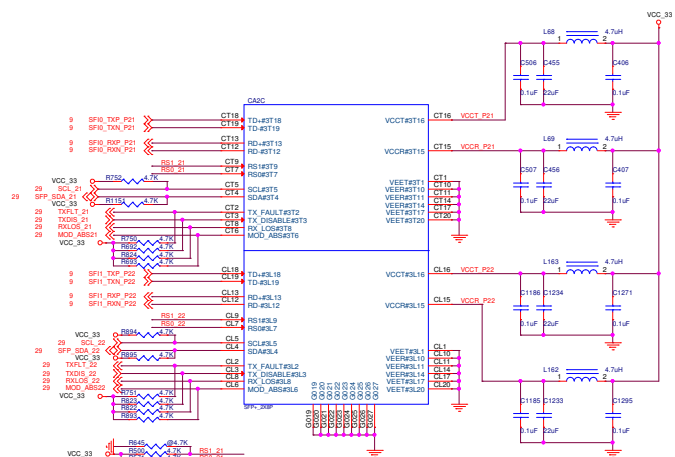
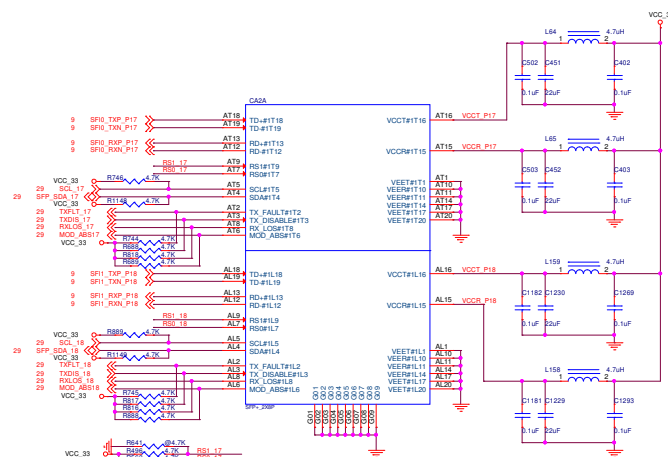
POR Config (LED up)
 LED1 High >> Copper AN enable
 LED2 High >> Full-Speed
 LED3 High >> AN at 10/100/1000BASE-T

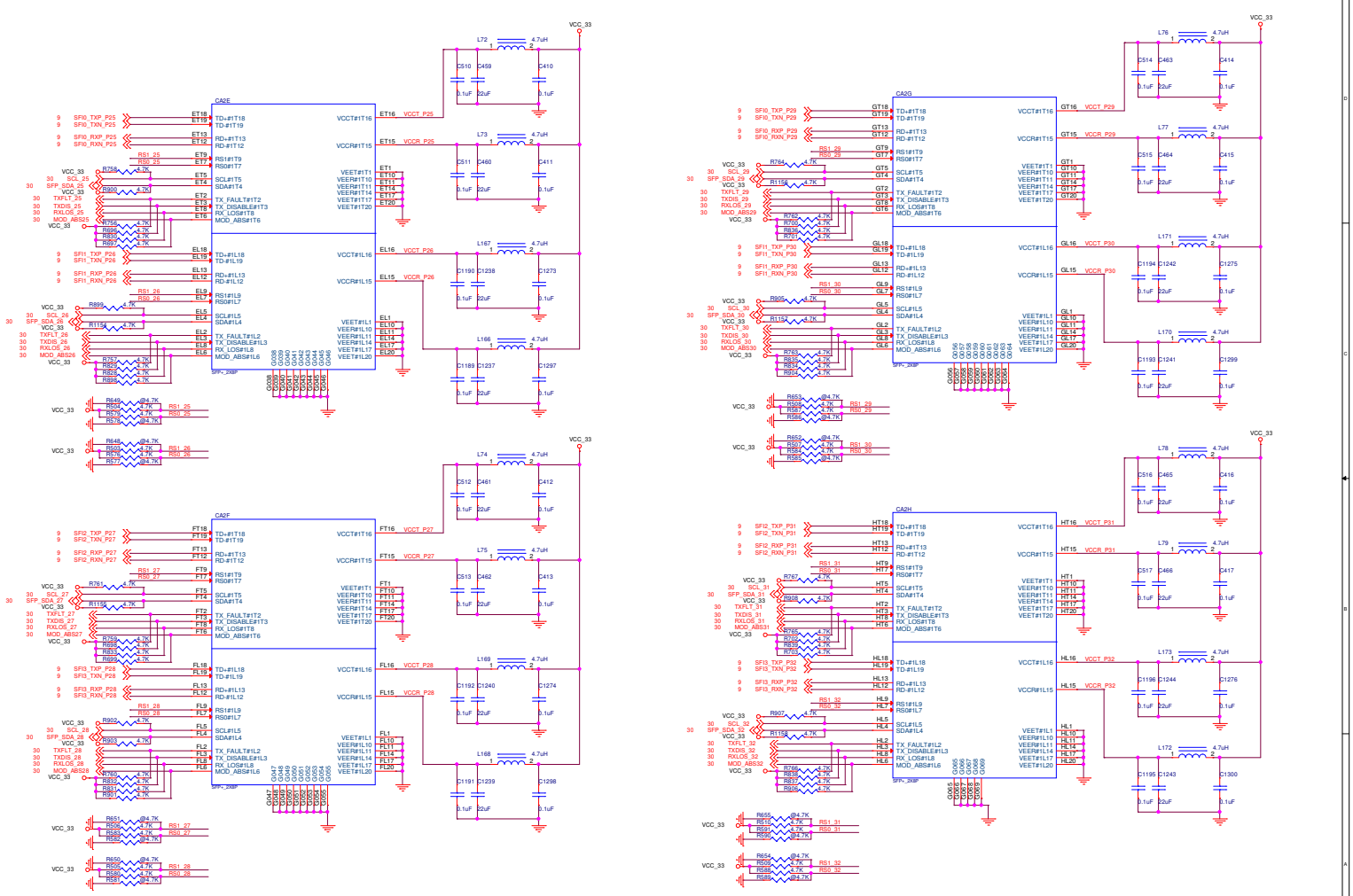
Config LED Pin as below
 LED1 LINKSPRI1
 LED2 LINKSPRI2
 LED3 ACTIVITY
 LED4 INTR

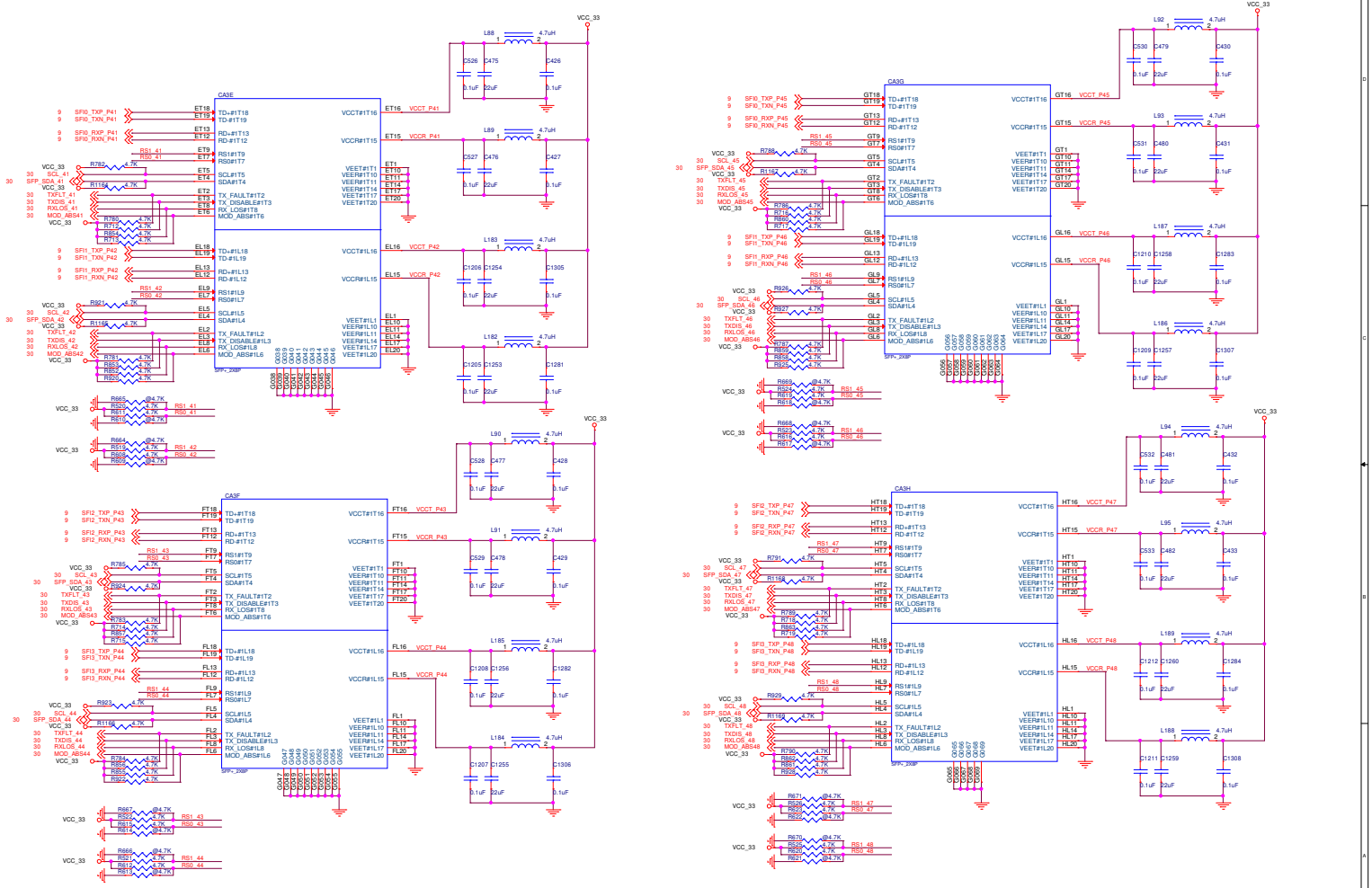
Nephos		No. 1, Dazig Rd, Hsinchu Science Park Hsin-Chu City, Taiwan 300, R.O.C. TEL: +886-3-5720788 FAX: +886-3-5787810	
Title BCM5416S			
Doc C	Document Number NEPHOS NPH4806 MAINBOARD ROB	Rev R0B	
Date 2010/05/28 13:29:31	Sheet 18	of 24	





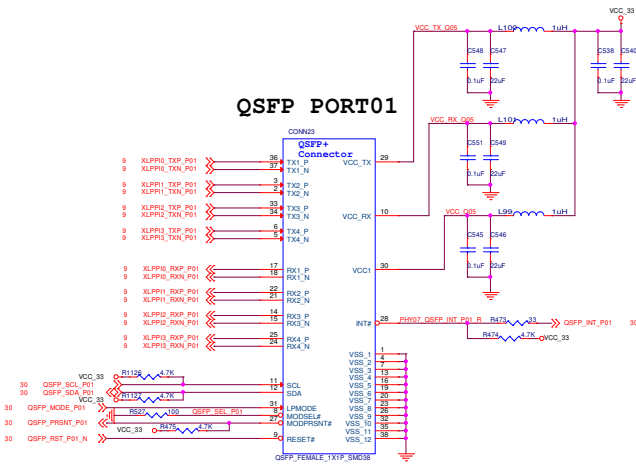




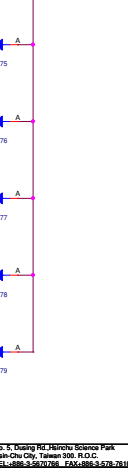
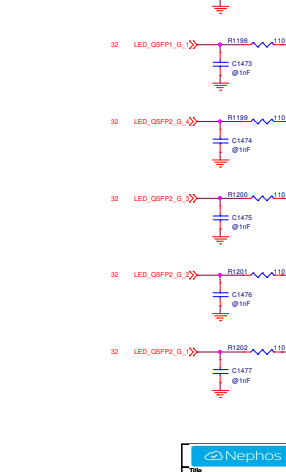
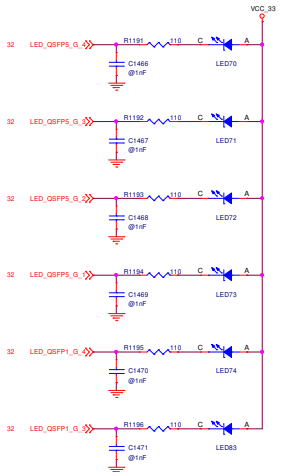
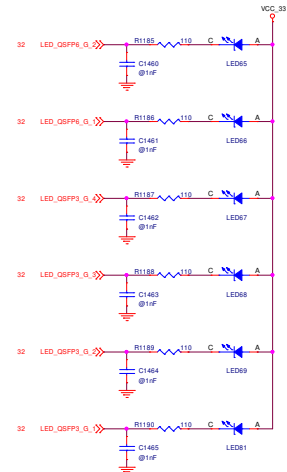
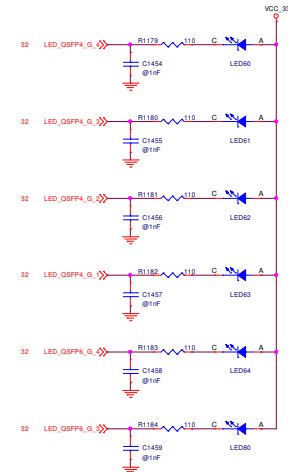
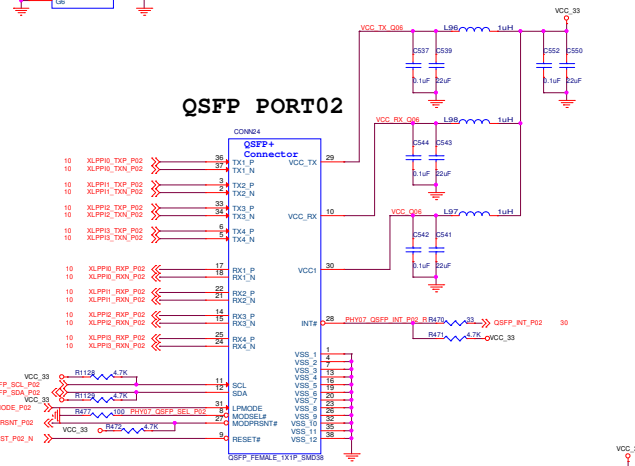


CONN2	CONN3	CONN4	CONN5
G1	G1	G7	G7
G2	G2	G8	G8
G3	G3	G9	G9
G4	G4	G10	G10
G5	G5	G11	G11
G6	G6		

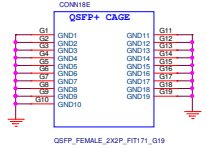
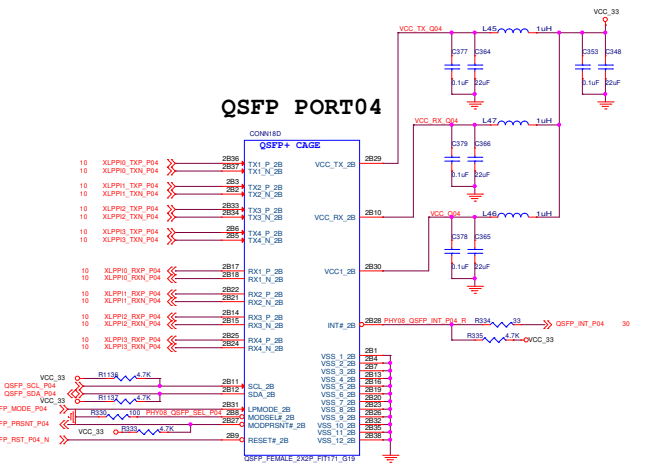
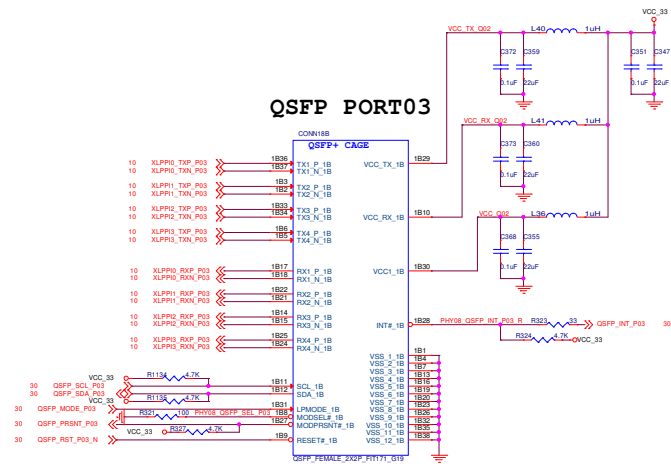
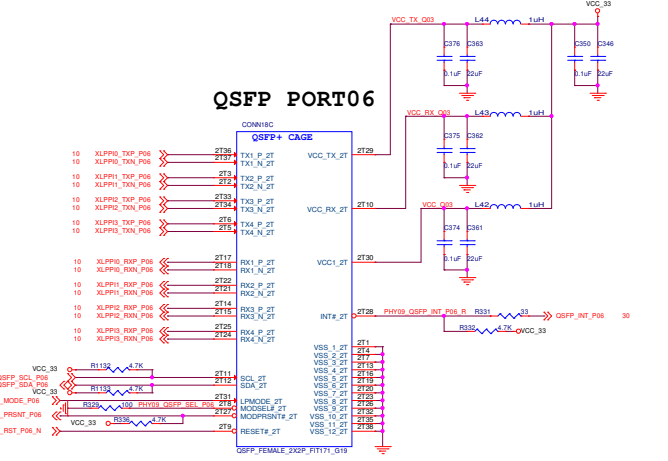
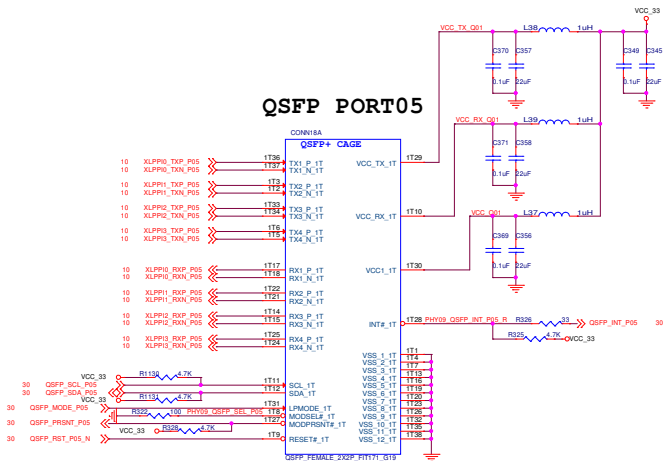
QSFP PORT01



QSFP PORT02



		No. 1, Guozheng Rd., Hsinchu Science Park Hsin-Chu City, Taiwan 300, R.O.C. TEL: +886-3-5720788 FAX: +886-3-5726710	
Title: QSFP+ PORT(01-02)			
Doc C	Document Number: NEPHOS NPH4806 MAINBOARD ROB	Rev R0B	Date: Wed Aug 29 13:29:31
Page: 25 of 24			



Nephos

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Hsin-Chu City, Taiwan 300, R.O.C.
TEL: +886-3-5720788 FAX: +886-3-5728710

Title: **QSFPP+ PORT(03-06)**

Doc. C

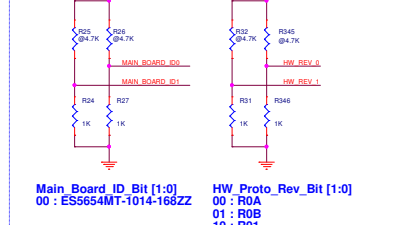
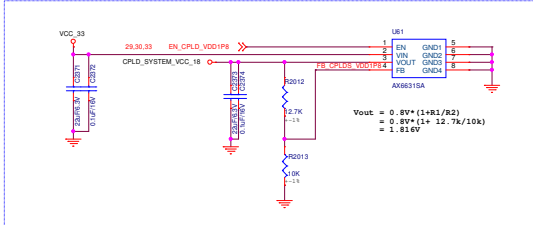
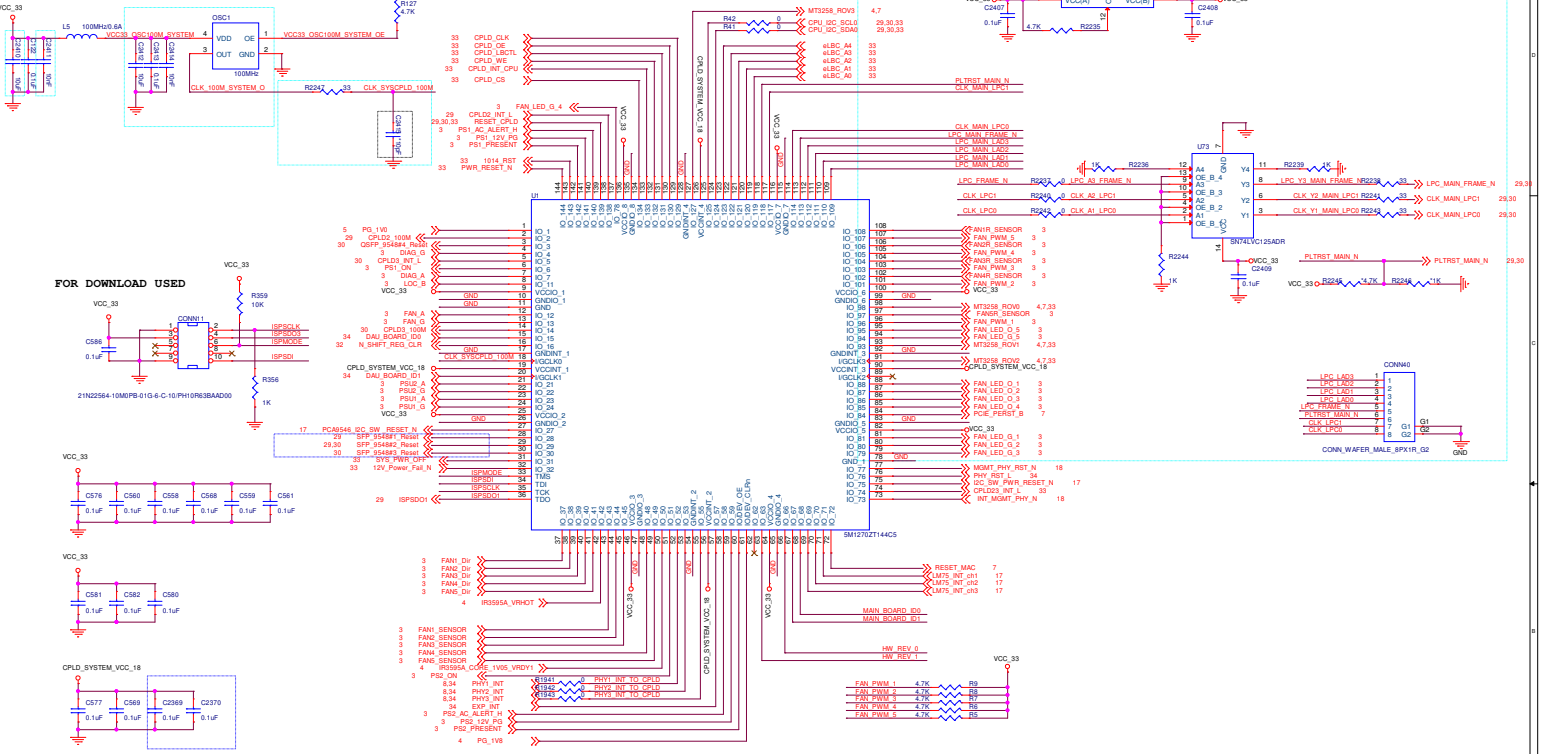
Document Number: **NEPHOS NPH4806 MAINBOARD ROB**

Rev: **1**

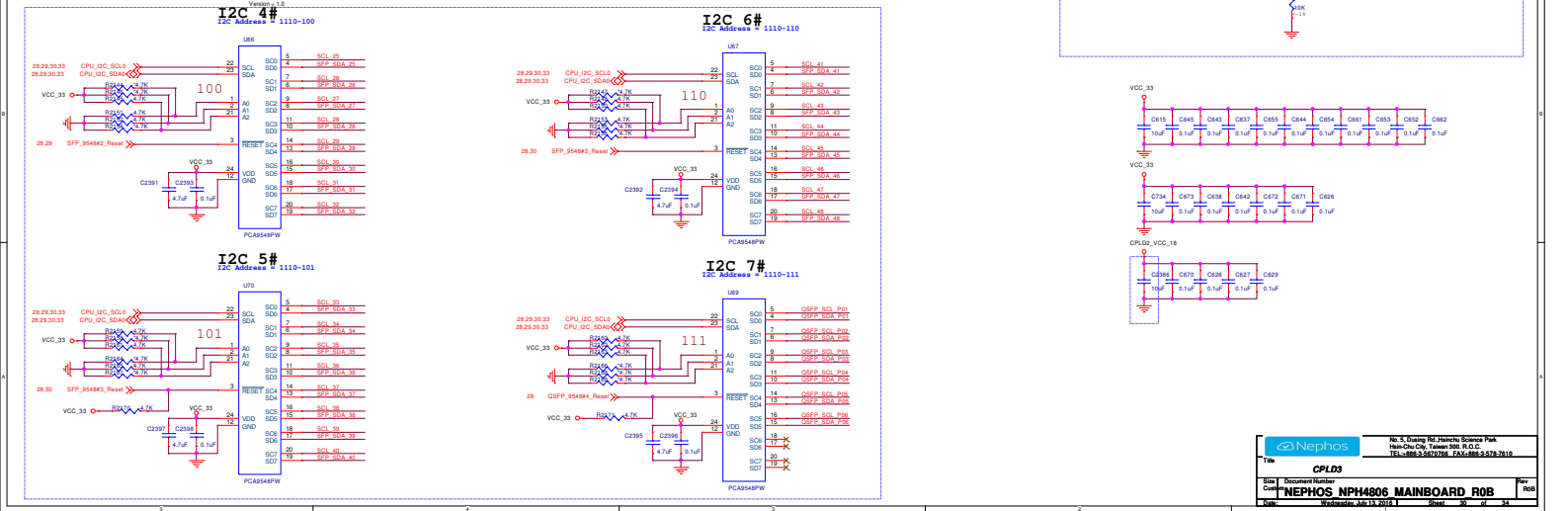
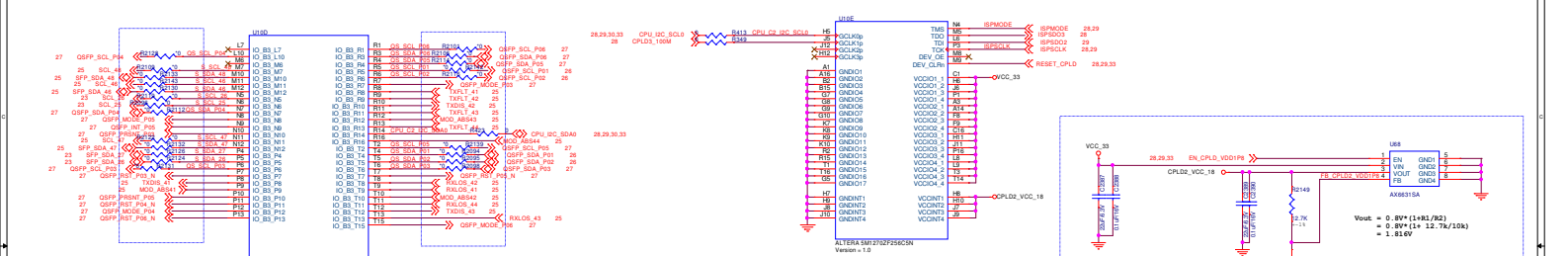
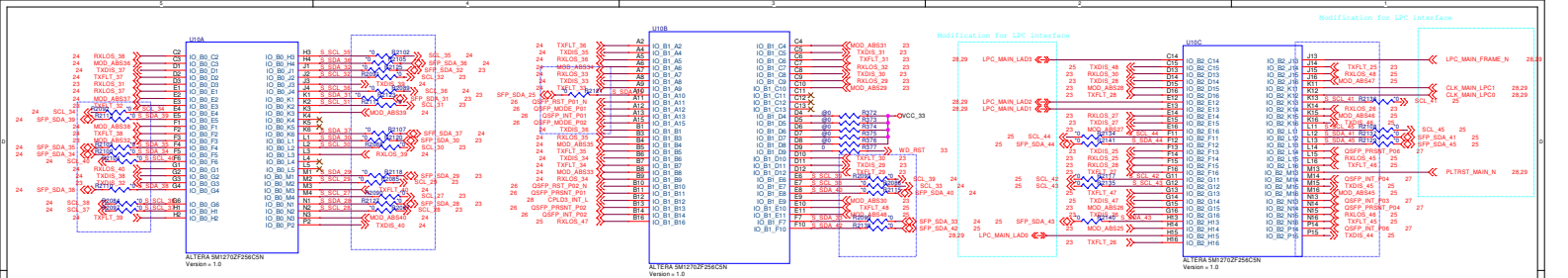
Date: **Wednesday, 26 11, 2013** | Page: **27** of **24**

Modification for LPC interface

Modification for LPC interface



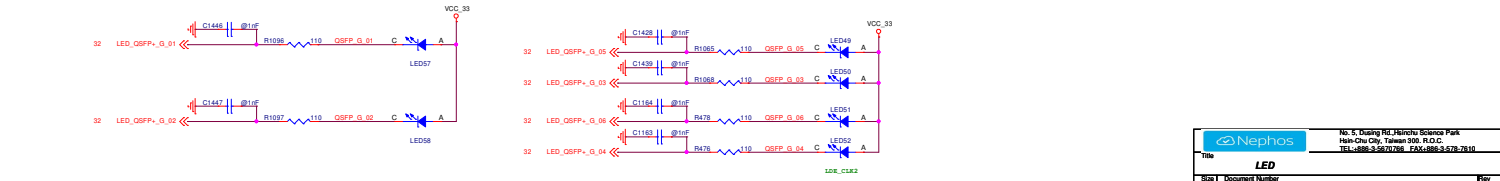
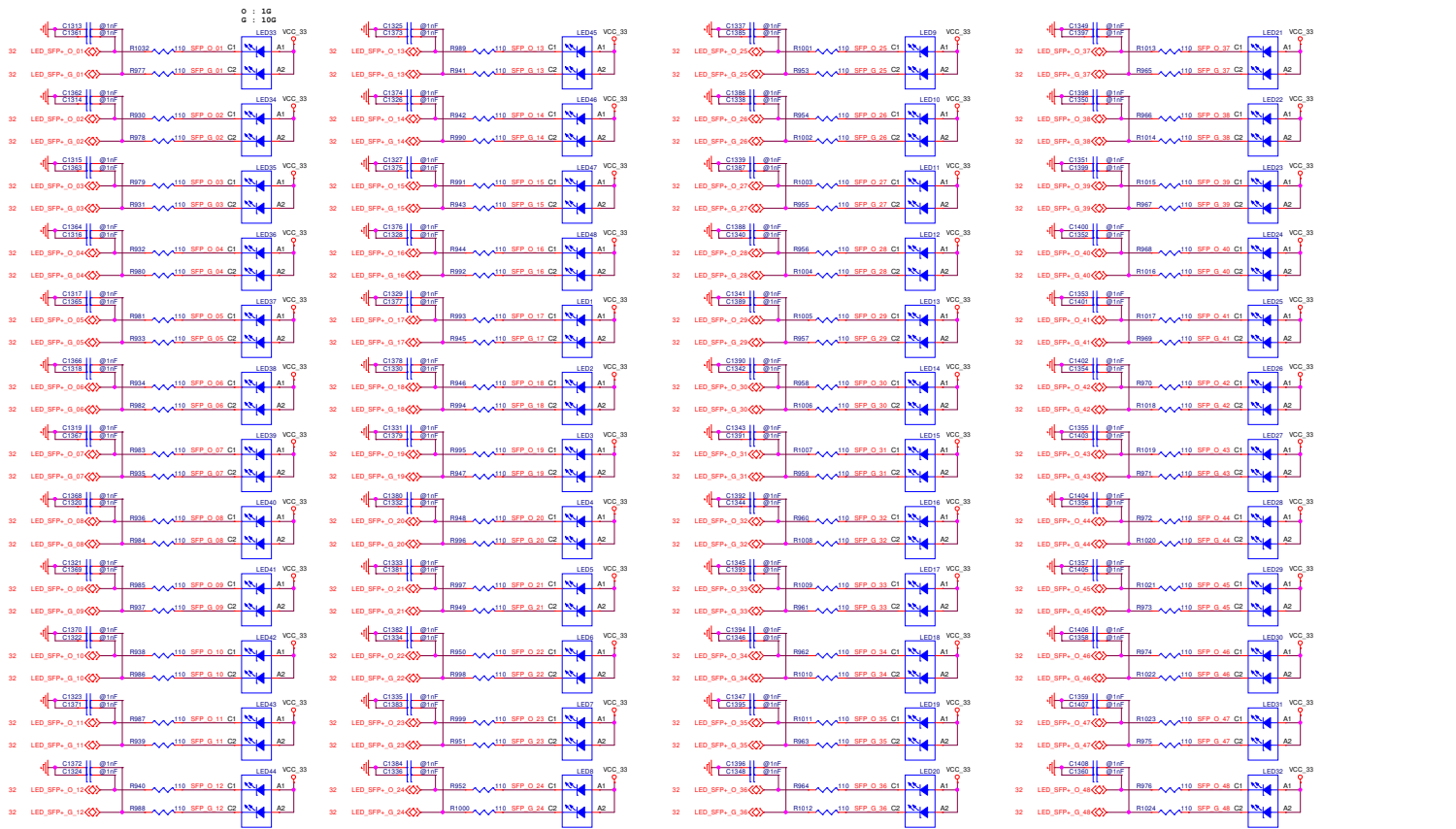
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Title: CPLD			
Doc No:	Document Number: NEPHOS_NPH4806_MAINBOARD_ROB		Rev:
Date:	WebPage: 2013.09.03		Page: 25 of 24




 No. 5, Gung Hui, Jinhua Science Park
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Part No: **CPLD3**
 Component Name: **NEPHOS NPH4806 MAINBOARD ROB**
 Date: **Wednesday, Aug 13, 2008**

Title: **CPLD3**
 Sheet: **3** of **4**



MT3258 Requirement :
DVDD33 (3.3V) to DVDD18 (1.8V) to VDDK(1.05V) and Analog PWR (1V) < 5ms

Power sequence :
3.3V->1.8V->1.05V and 1.0V

MT3258 Requirement :
DVDD33 (3.3V) to DVDD18 (1.8V) to VDDK(1.05V) and Analog PWR (1V) < 5ms

Power sequence :
3.3V->1.8V->1.05V and 1.0V

