

Cell Site Gateway Router

Janet Peng

Director AT&T Labs

Network Cloud & Infrastructure

jp5859@att.com



Agenda

Why New Cell Site Gateway Router?

Open and Flexible

Whitebox Requirement Framework

Cell Site Gateway Router Topology

Deployment Environment

Major Requirements

High Level Systems Block Diagram

Summary of AT&T Whitebox Experience



Why New Cell Site Gateway Router?

Exponential Data Traffic Growth

- Increased internet adoption, faster broadband, next generation wireless networks, the Internet of Things, and increased live streaming of online videos are expected to drive exponential traffic growth
- The old hardware model simply can't keep up

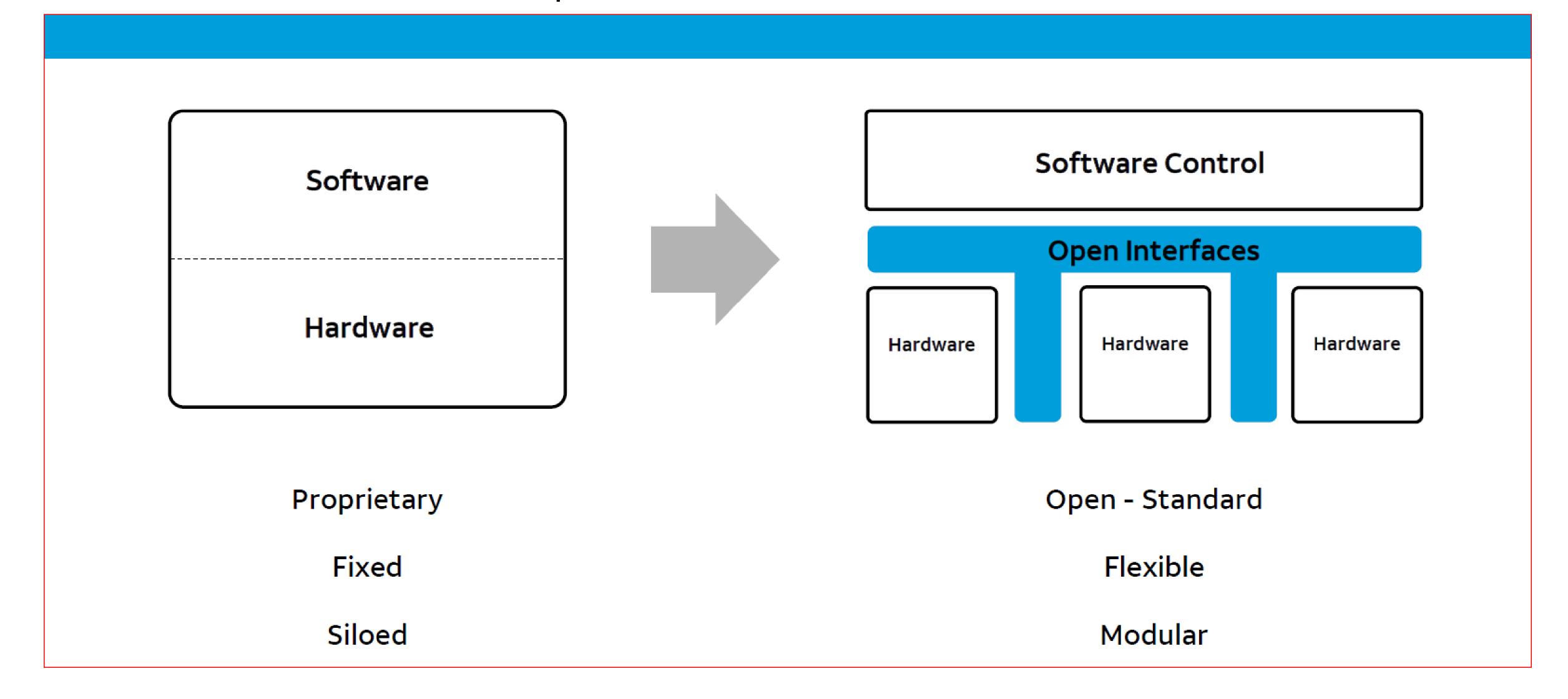
5G Deployment

- Stringent demands of 5G on speed, bandwidth and performance with low latency
- 5G mobile technology can send massive amounts of data with low latency, little downtime in between
- Changing needs of backhaul transport requirements as mobile service providers make the transition from legacy technologies toward 5G RAN technologies

Need a high-performance, versatile router to meet service requirements



Open & Flexible





Open Platform

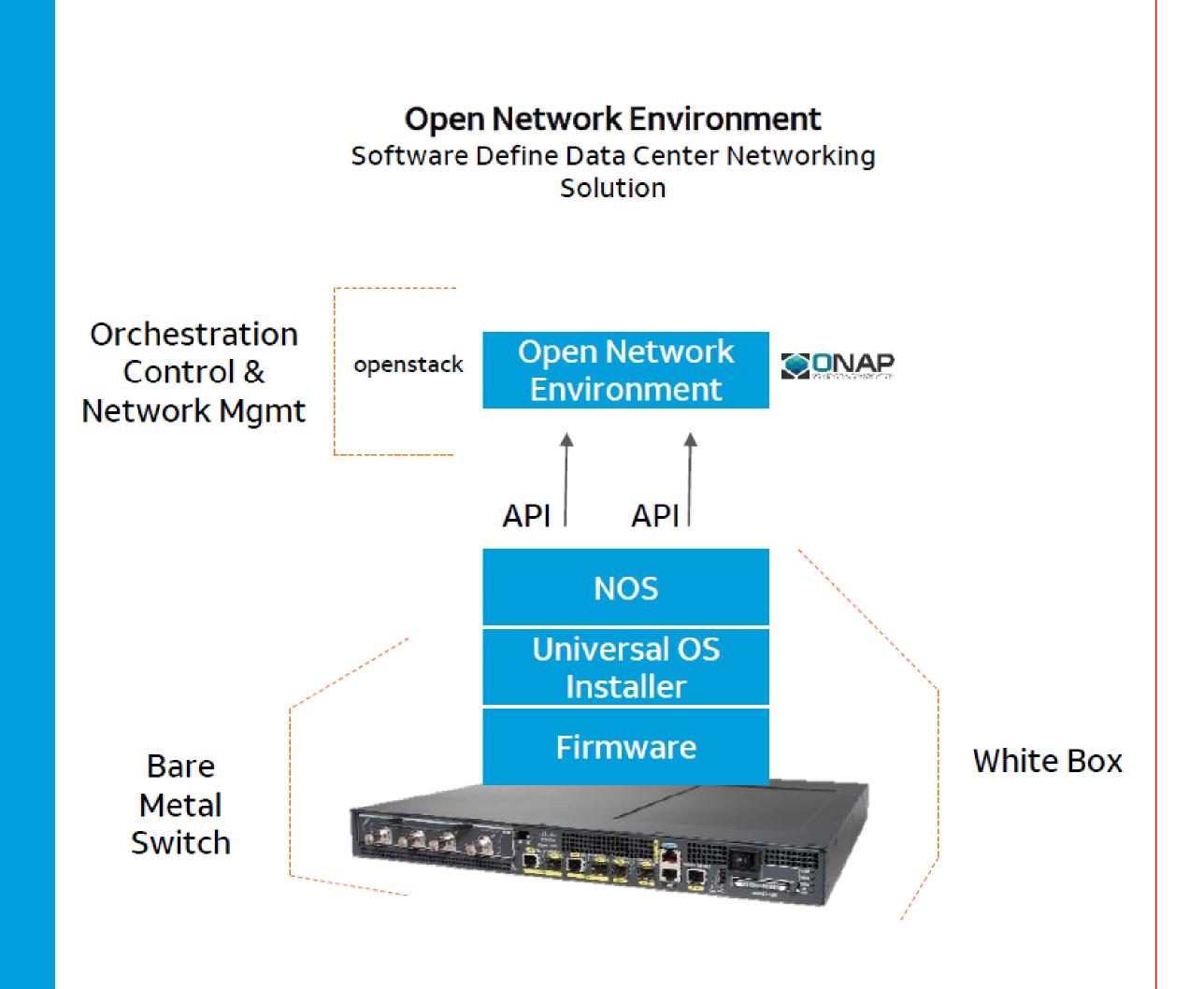
Whitebox

Not Custom Built

Open Platform/Interfaces

Off-the-Shelf Technology

Multi-Vendor Sourced





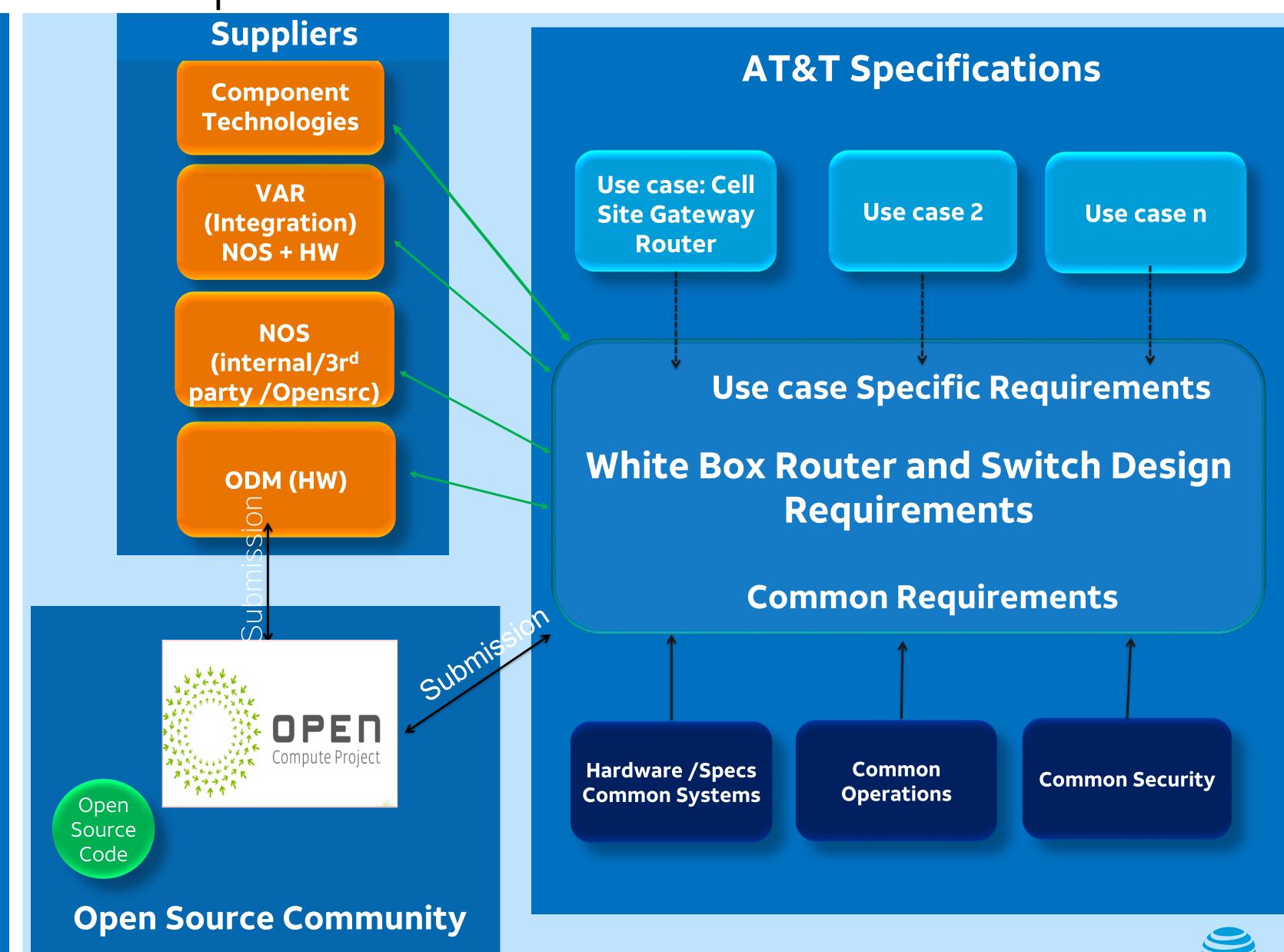
Whitebox Requirement Framework

Principle

- Open-standard, flexible, modular
- Interactive approach, engage all key stakeholders
- Work towards a solution with the end game in mind
- Common spec
- Use case specific spec

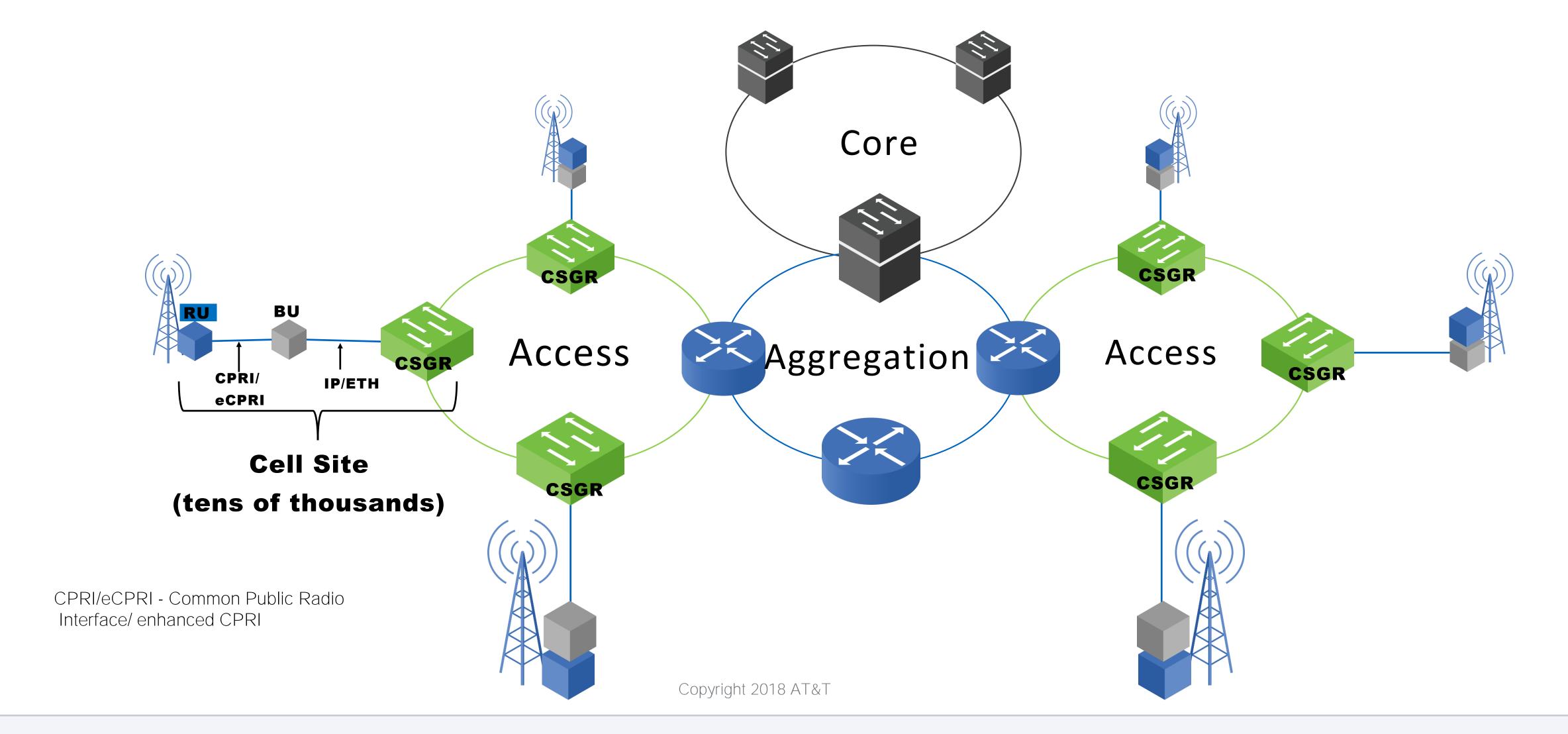
Cell Site Gateway Router

- AT&T is releasing hardware specs to OCP
- ODMs are contributing detailed design package



AT&T Cell Site Gateway Router (CSGR) Topology

5G requires new technologies and solutions to deliver an order of magnitude higher capacity, performance and low cost

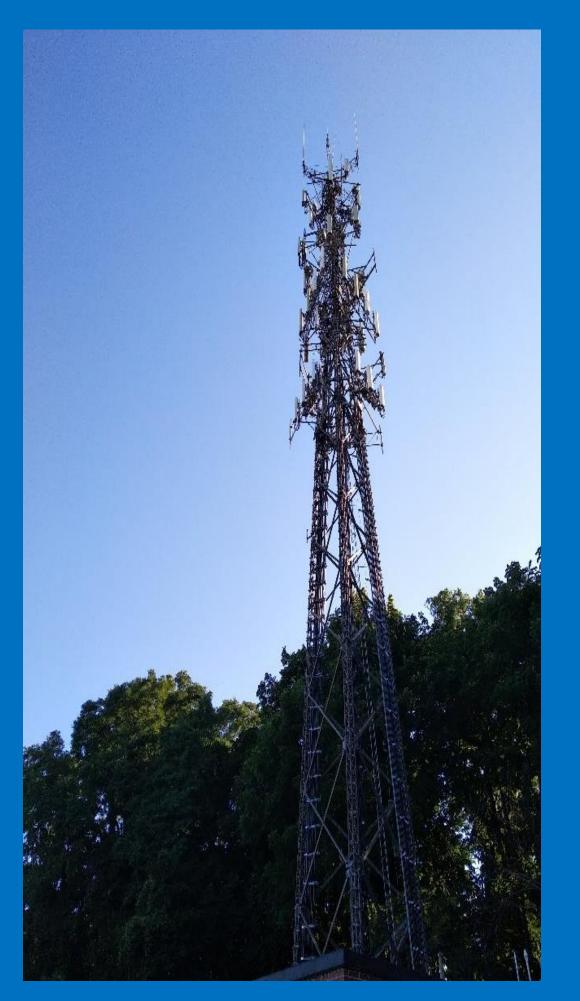




Deployment Environment

- Designed to operate at hardened temperature ranges (-40C to + 65C)
- Meet NEBS Level 3 in both Carrier Communications and Class 2 OSP (Outside Plant) space
- Physical Dimension: 1RU, 19", shallow depth
- Front to Back Air flow. Front access to power and ports
- Redundant, Replaceable Fans and Power Supply modules





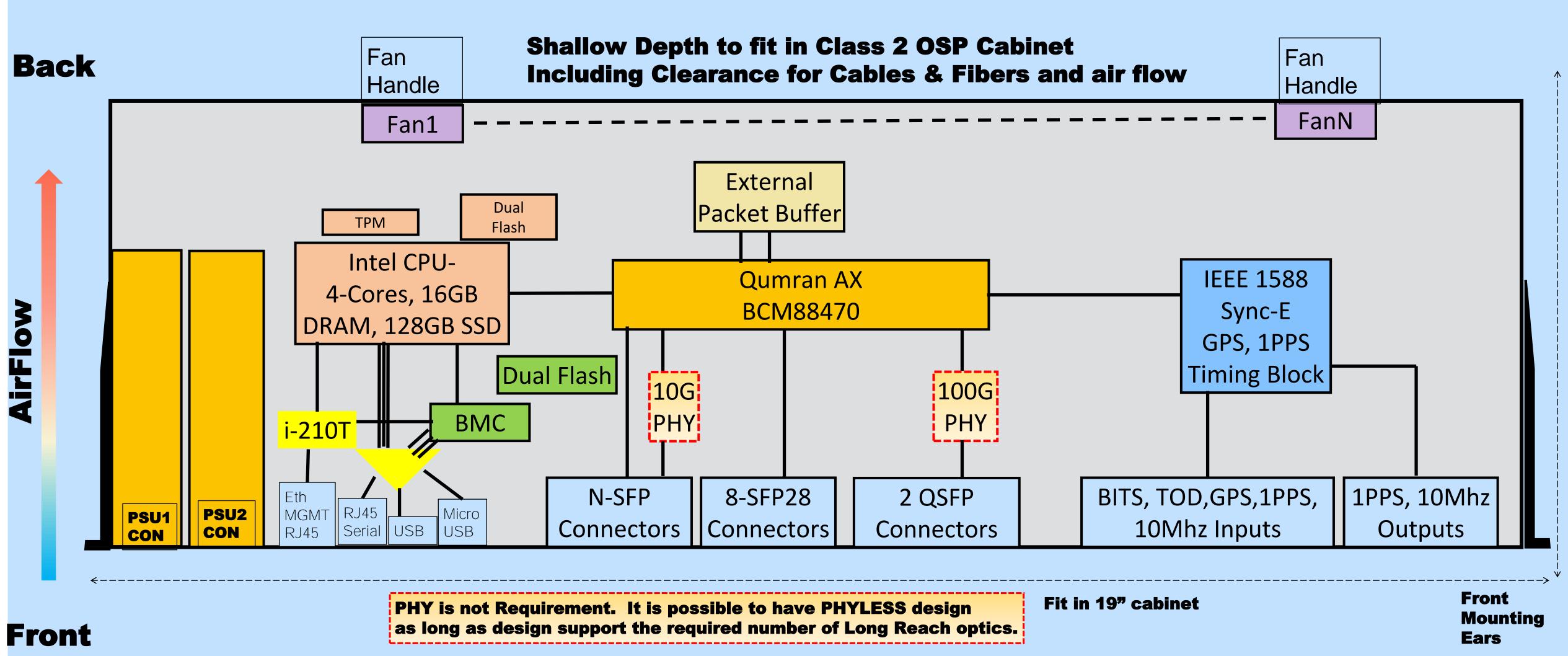


Major High Level Requirements

- 1+1 redundant high efficiency power supply
- Intel x86 for the NOS routing software
- BMC for platform health status monitoring and recovery
- Broadcom Qumran-AX switching chip with deep buffers to support advanced features and QoS
- Interfaces: support up to 2x100G/40G, 8x25G/10G/1G, and N {10G, 1G,100M}
- Support Long Range Optics (ZR-80km for 1G/10G) and (ER4-40km for 100G)
- Timing circuitry block that supports a variety of inputs and outputs support to the evolving timing requirements and implementations in the 5G technology evolution, including 1588v2 and SyncE



High-Level Systems Block Diagram



Summary of AT&T Whitebox Experience

- AT&T has made great progress and is on track to deploy whitebox hardware
- Provide leadership and direction on Telco/Service Provider space
- Educate ODMs and suppliers on AT&T use cases and collaborate on systems design to ensure hardware and software have the flexibility to support the disaggregated model
- Learn about ODMs and component technology suppliers' development processes and products
- Define and create new whitebox integration processes and operational model
- Opportunity to provide feedback on component technologies of features and requirements that are important for Telco use cases
- Stable hardware, agile firmware and software development process to support dev ops model
- Direct interaction with ODMs and suppliers to make sure designs meet common systems requirements and deployment practices such as cabling, powering cooling constraints
- Continue collaboration with open source community and other service providers to advance work in Telco space



Q&A

Thank You

