

# **ADAPTER BOARD**

## **TYPE1**

**Hybrid X8 PCIe on connector A, X4 KR on connector B**

**PCB REV: B**

**PCBA REV: B2B**

**FAB: 3**

**SCH Rev: 02**

**LAST UPDATE: 2015/10/20**

<b>Quanta</b>	Department CCBU	Designer <Designer>	Project <b>Adapter Board_type1</b>	Doc Number <Doc>	Rev Av6
	Reviewer	<Reviewer>		Page Title Cover Page	

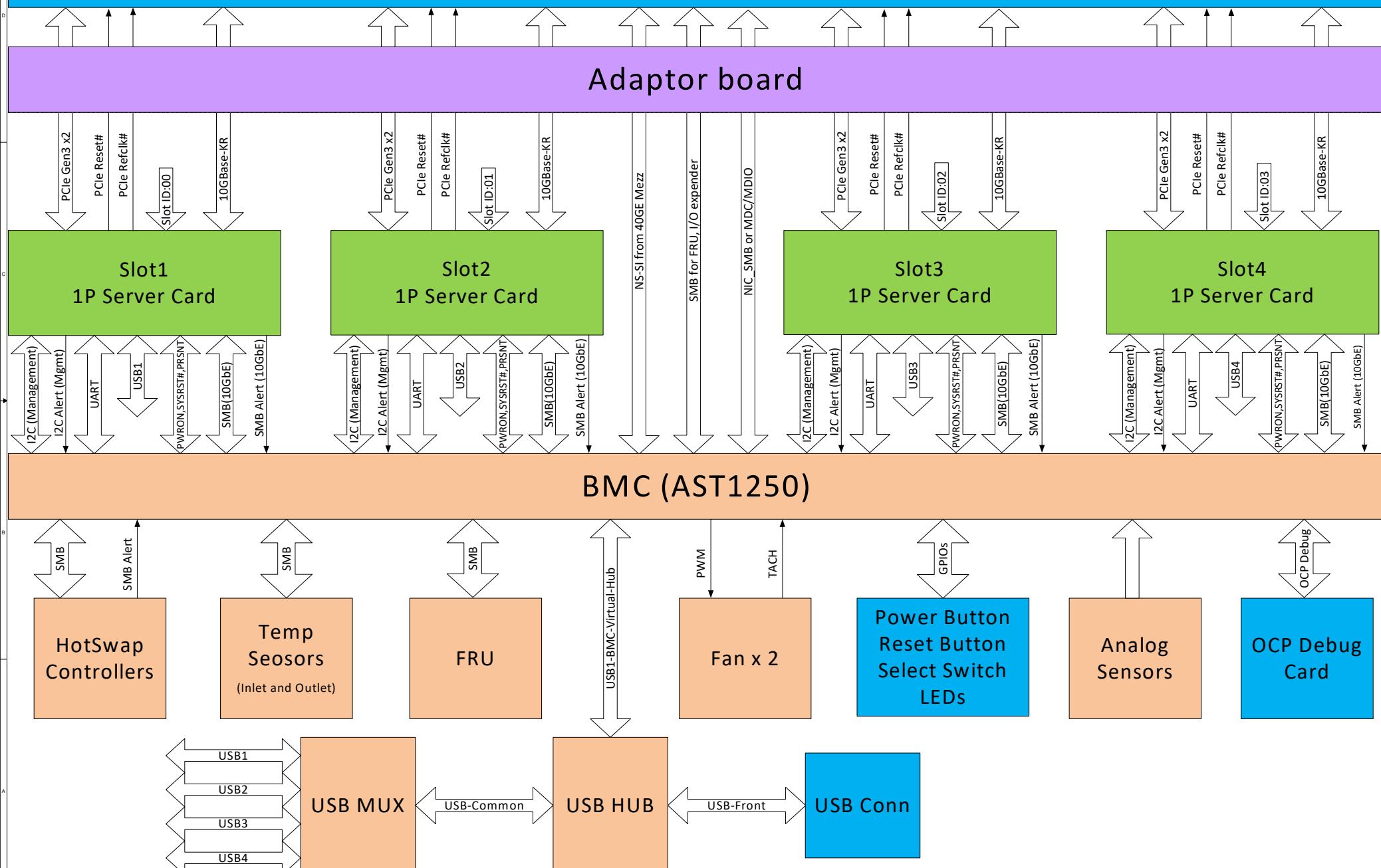
# INDEX

---

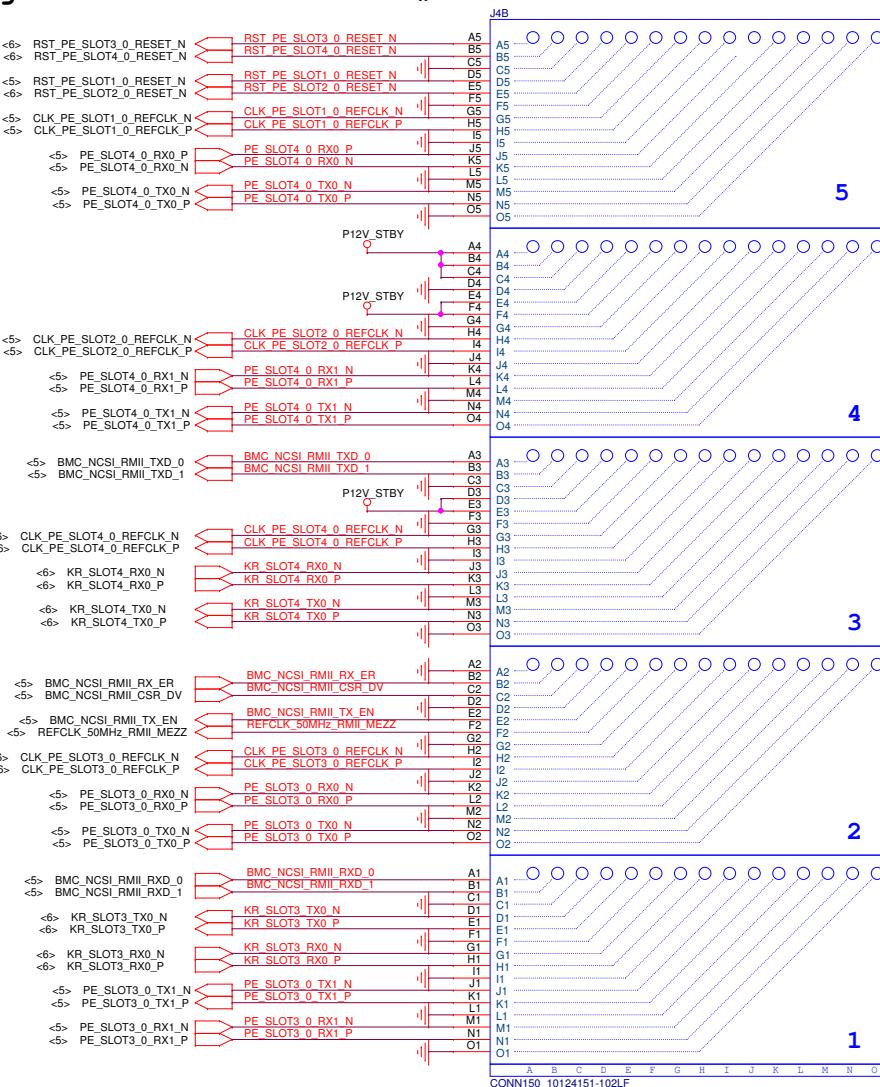
- 1 Cover Page
- 2 Page Index
- 3 Board Block Diagram
- 4 Adapter Board CONN
- 5 OCP v2.0 Mezz Conn A
- 6 OCP v2.0 Mezz Conn B
- 7 Mechanical
- 8 Baseboard ID

# Yosemite Block Diagram V0.06

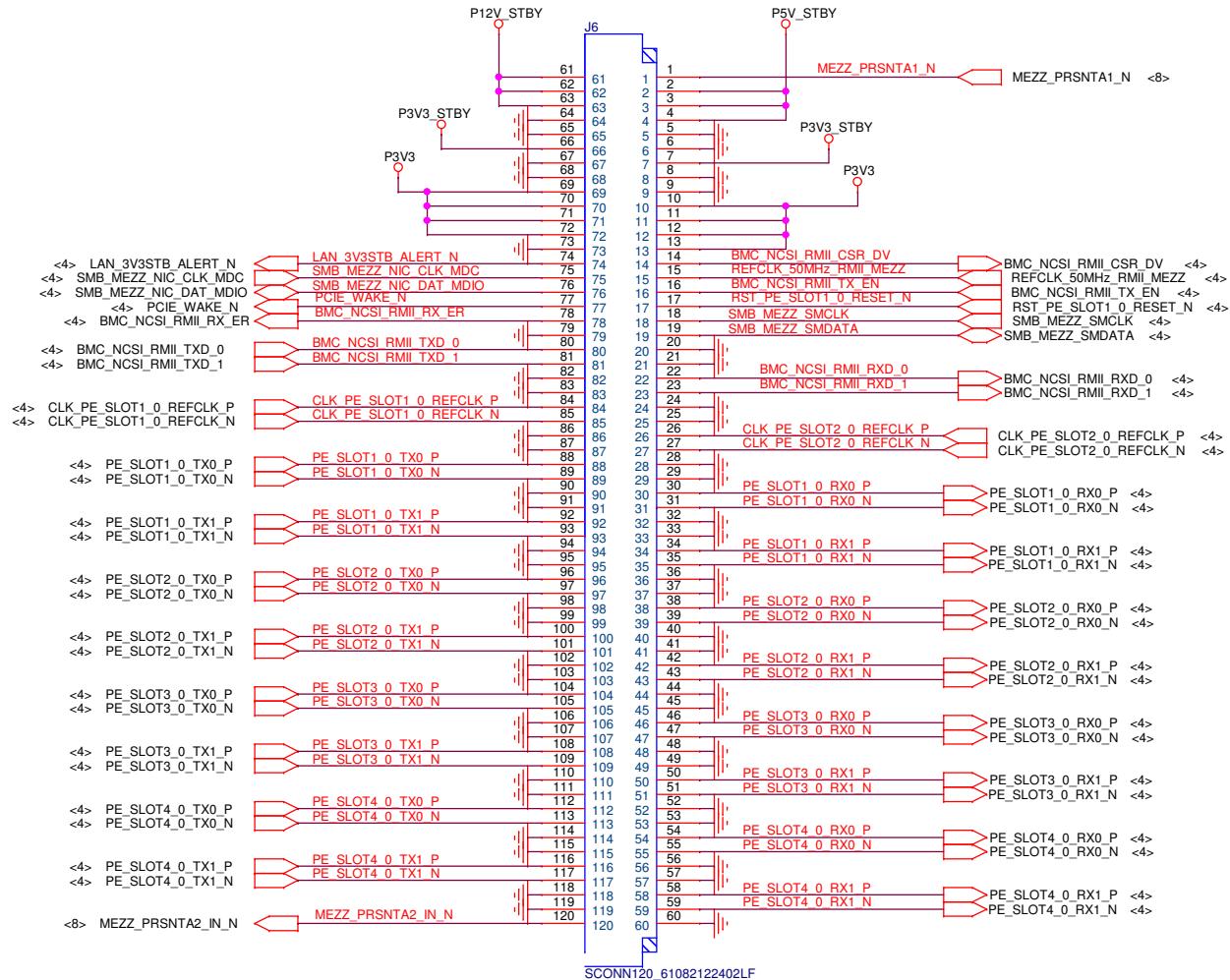
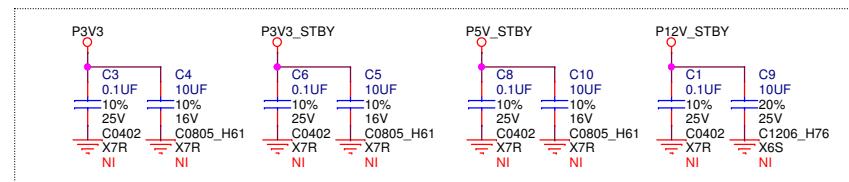
## OCP V2 40GE Mezz or 10GBASE-KR Mezz with QSFP+ Conn

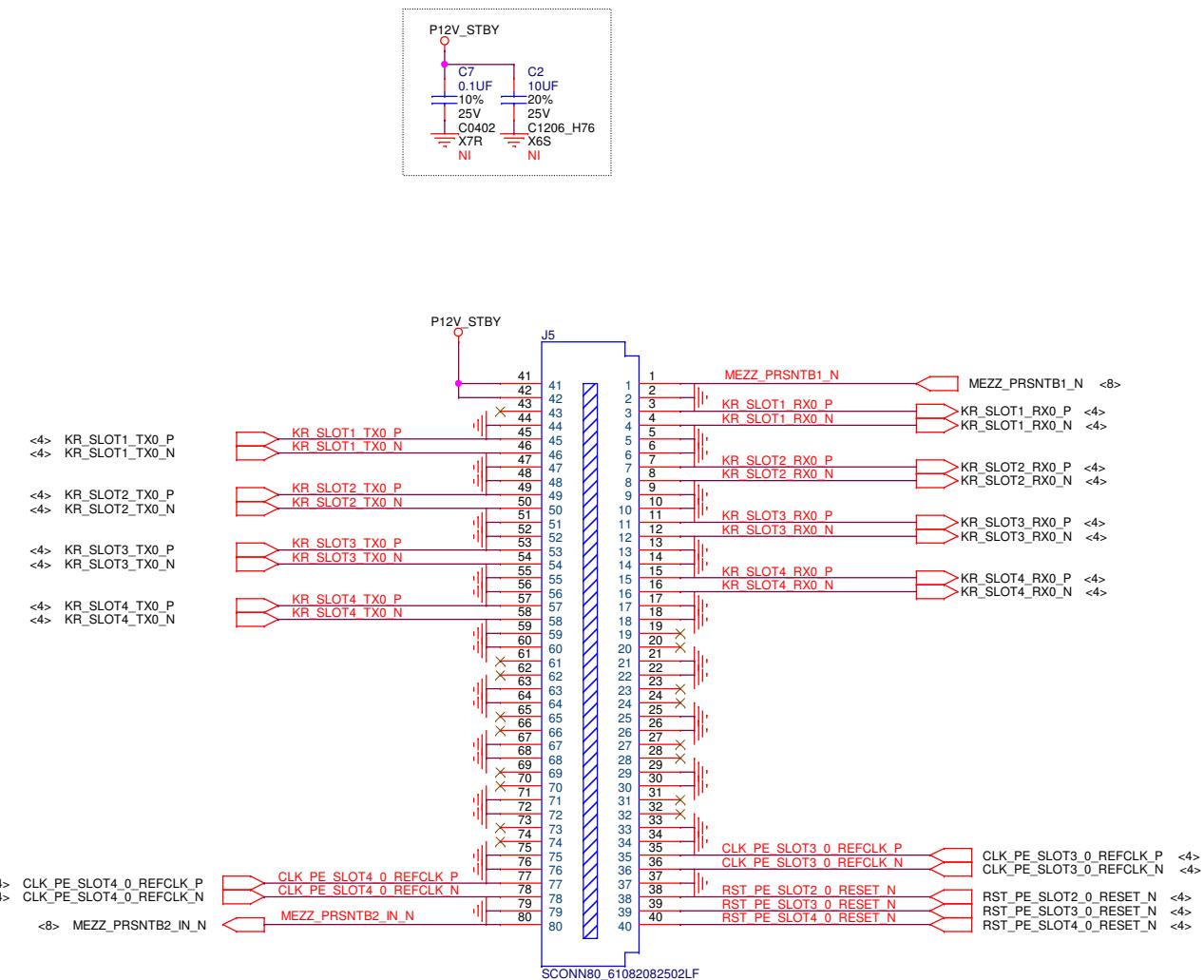


# Right angle Air Max conn: FCI# 10124151-102LF

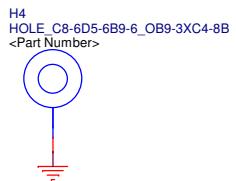


**Adapter Board CONN**

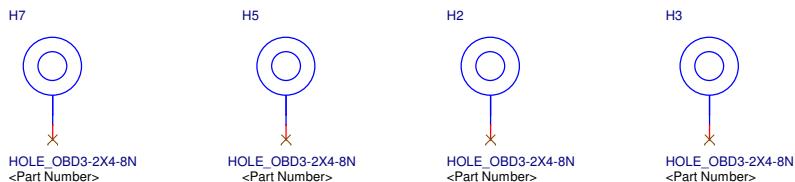




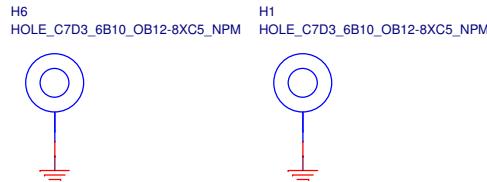
# SMT THUMB SCREW \* 1pcs



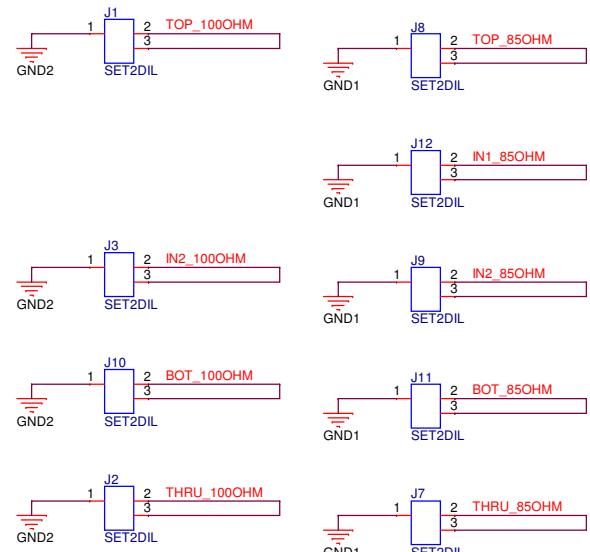
# Mezzanine holes \* 4pcs



# Key Holes \* 2pcs

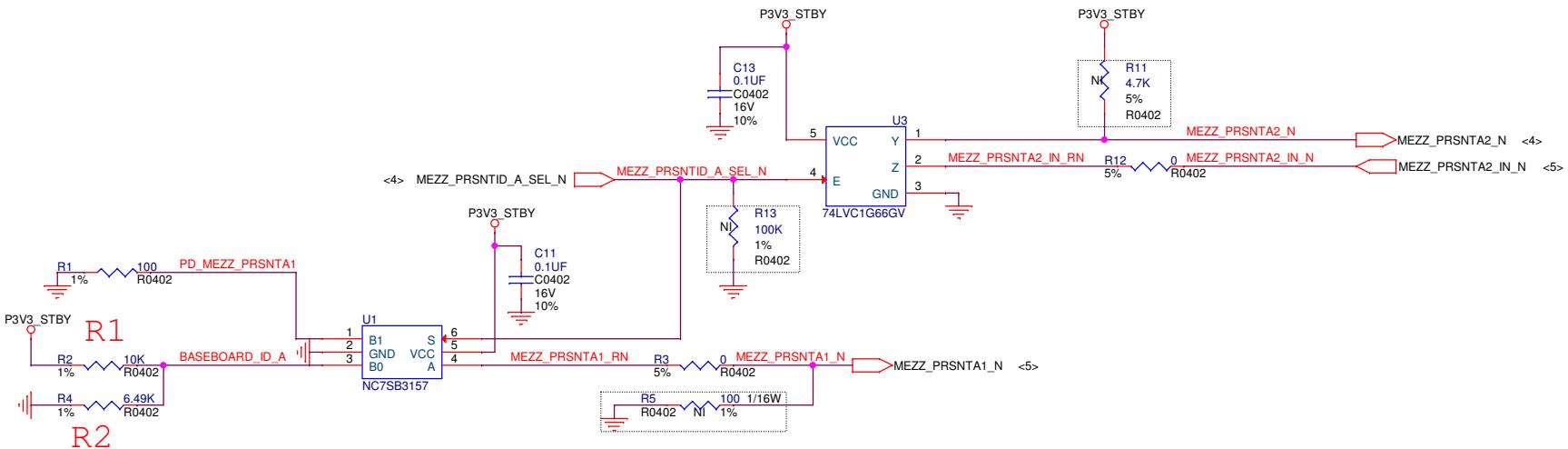


# SET2DIL



# Mechanical

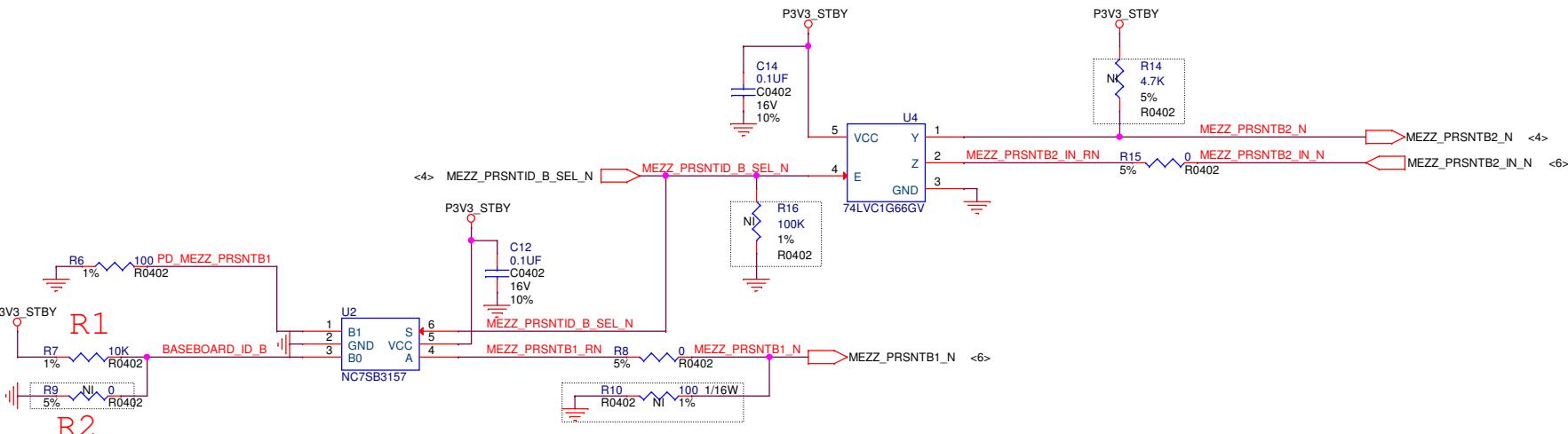
Quanta	Department CCBU	Designer <Designer> Reviewer <Reviewer>	Project <b>Adapter Board_type1</b>	Doc Number <Doc> Page Title Mechanical	Rev Av6
5	4	3	2	1	



<b>PRSNTID_A_SEL_N</b>	<b>Function</b>	<b>MEZZ_PRSNTA2_N</b>	<b>MEZZ_PRSNTA1_N</b>
<b>H</b>	<b>B1 Connected to A</b>	<b>Pullup</b>	<b>Pulldown</b>
<b>L</b>	<b>B0 Connected to A</b>	<b>high-impedance</b>	<b>Baseboard ID</b>

<b>PRSNTID_B_SEL_N</b>	<b>Function</b>	<b>MEZZ_PRSNTB2_N</b>	<b>MEZZ_PRSNTB1_N</b>
<b>H</b>	<b>B1 Connected to A</b>	<b>Pullup</b>	<b>Pulldown</b>
<b>L</b>	<b>B0 Connected to A</b>	<b>high-impedance</b>	<b>Baseboard ID</b>

	<b>ConnA R1</b>	<b>ConnA R2</b>	<b>ConnB R1</b>	<b>ConnB R2</b>	<b>Base Board type</b>
<b>Adapter type1*</b>	<b>10kΩ</b>	<b>6.49kΩ</b>	<b>10kΩ</b>	<b>NC</b>	Four x2 PCIe Root Ports on baseboard Connector A Up to 8x KR on baseboard Connector B
<b>Adapter type2</b>	<b>10kΩ</b>	<b>3.83kΩ</b>	<b>10kΩ</b>	<b>3.83kΩ</b>	Two x4 PCIe Root Ports on baseboard Connector A Two x4 PCIe Root Ports on baseboard Connector B



## Baseboard ID