



Rackgo X Big Basin Overview

AI Will Change Everything

QCT can leverage SDK work, combined with frameworks to enter new market verticals

(1) DL (2) Cars (3) VR (4) Gaming (5) Higher Education (6) Design (7) Robot

DEEP LEARNING

Deep Learning SDK

High-performance tools and libraries for deep learning



SELF-DRIVING CARS

NVIDIA DriveWorks™

Deep learning, HD mapping and supercomputing solutions, from ADAS to fully autonomous



VIRTUAL REALITY

NVIDIA VRWorks™

A comprehensive SDK for VR headsets, games and professional applications



GAME DEVELOPMENT

NVIDIA GameWorks™

Advanced simulation and rendering technology for game development



ACCELERATED COMPUTING

NVIDIA ComputeWorks™

Everything scientists and engineers need to build GPU-accelerated applications



DESIGN & VISUALIZATION

NVIDIA DesignWorks™

Tools and technologies to create professional graphics and advanced rendering applications



AUTONOMOUS MACHINES

NVIDIA JetPack™

Powering breakthroughs in autonomous machines, robotics and embedded computing



ADDITIONAL RESOURCES

More resources for GPU Developers



Rackgo X Big Basin

Industry First JBOG with NVLink Support

Execute Deep Learning Algorithms Like Never Before

- **Industry First JBOG – Just a Bunch of GPUs**
 - The first ever designed pure GPU enclosure with up to 8 SXM2 modules
- **NVLink Enabled Architecture**
 - High-bandwidth, energy efficient GPU interconnect for ultra fast communication
- **Flexible GPU Topologies for Specific Workloads**
 - Deep Learning SKU: 1x host with 8x SXM2
 - AI Simulation SKU: 2x host with 4x SXM2/each



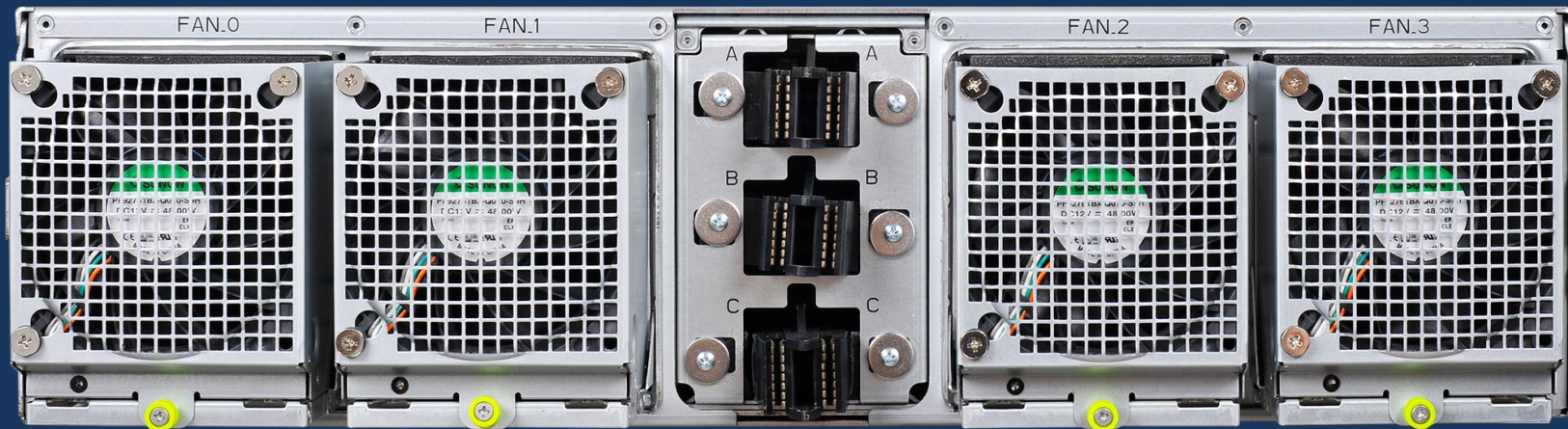
2x PCIe x8 Expansion slots

4x PCIe x16 Host uplink or
downlink Expansion slots

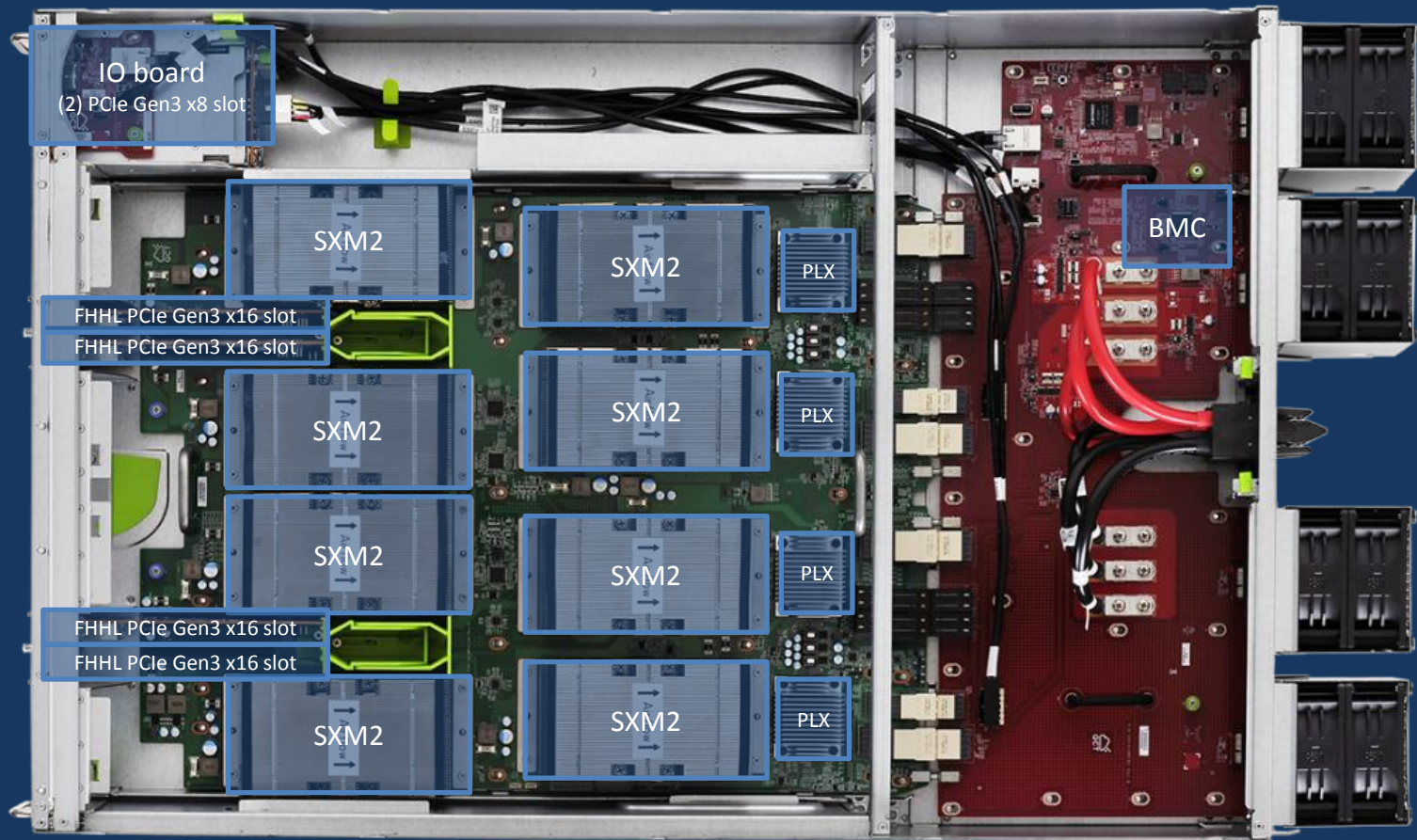
Debug Port v2

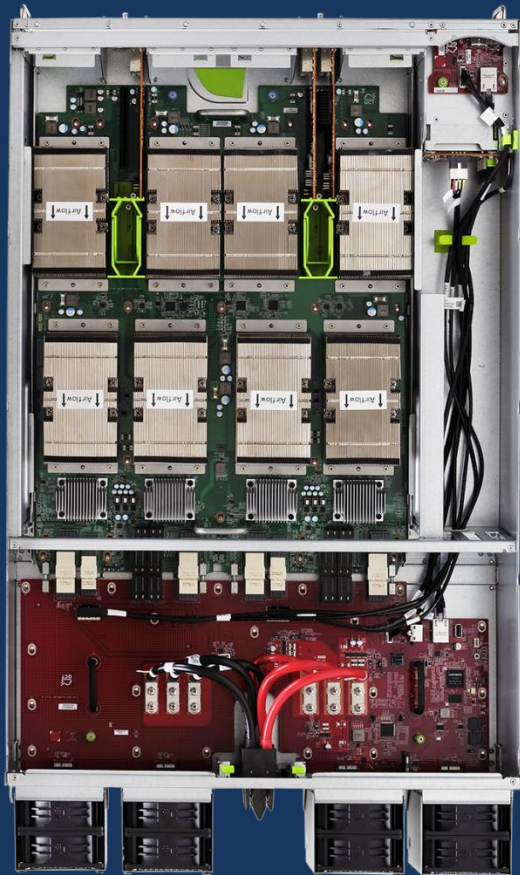
Dedicated
Mgmt Port

Debug Port v1

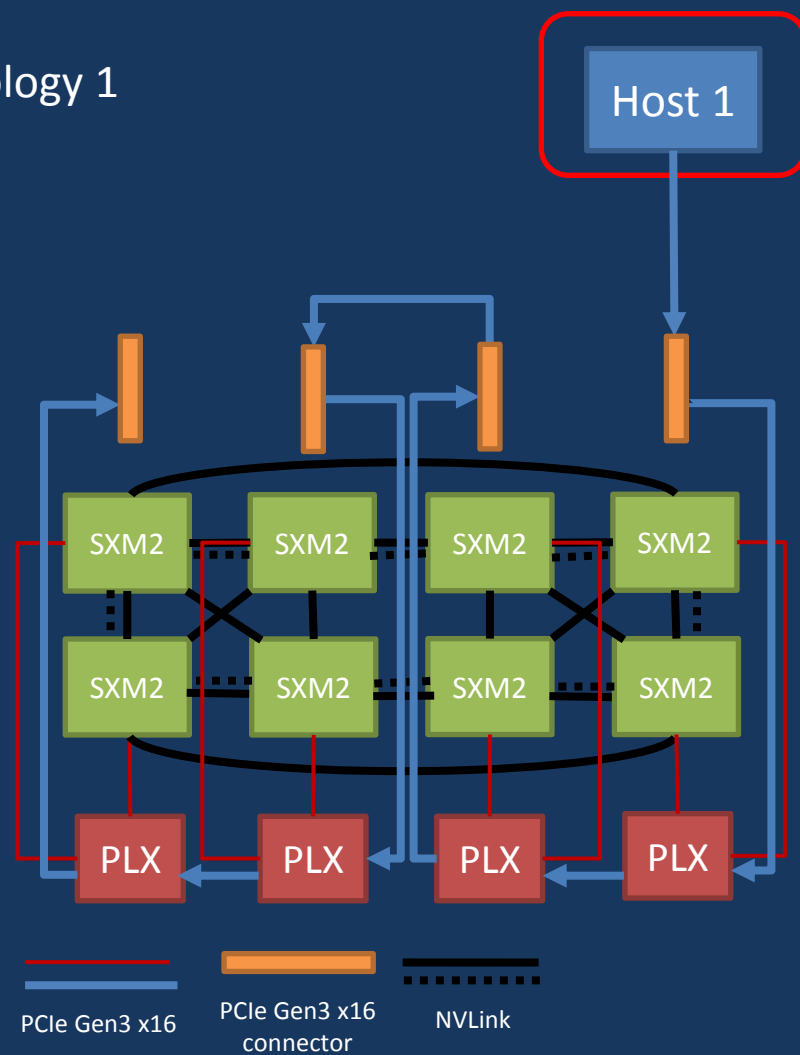


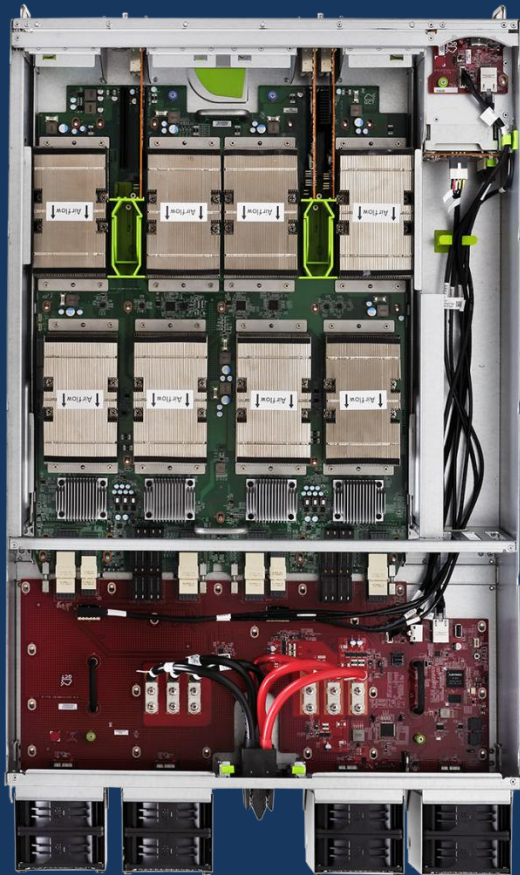
Rackgo X Big Basin Top View



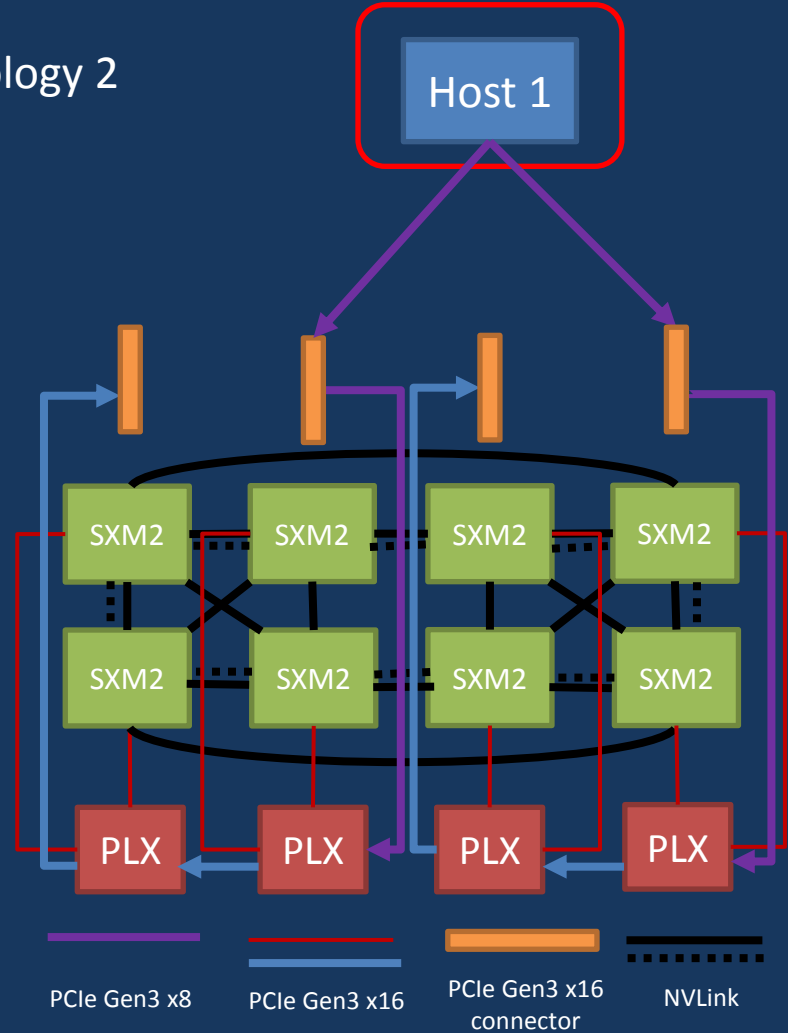


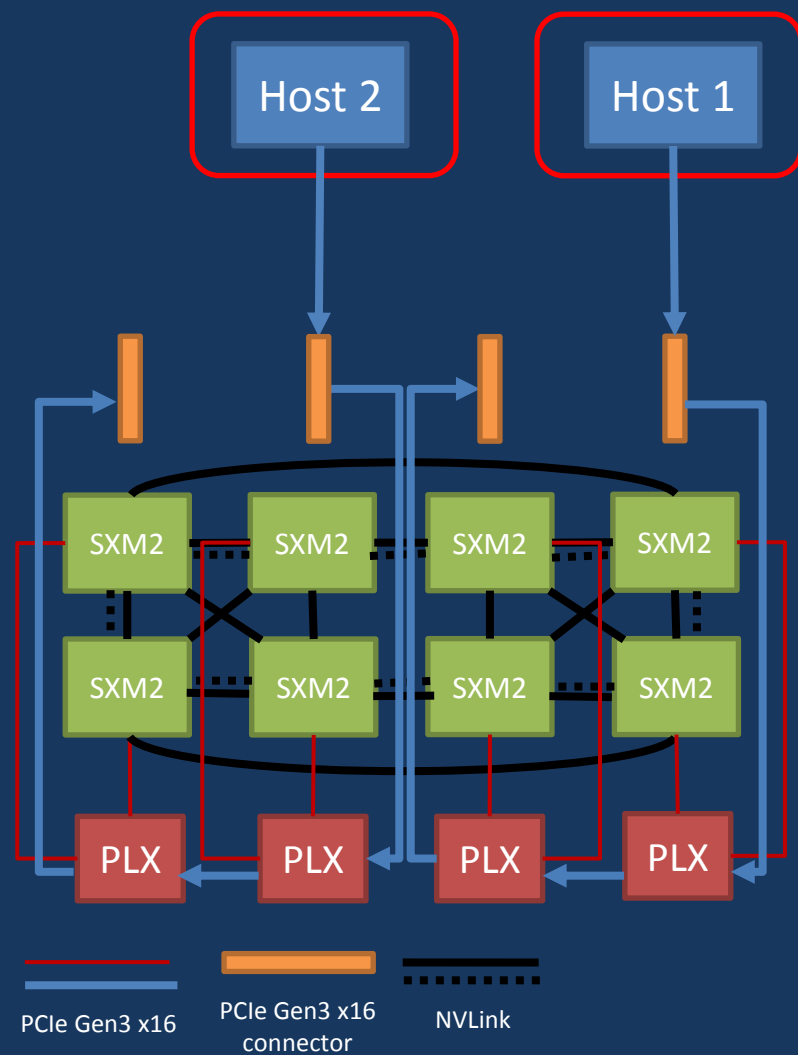
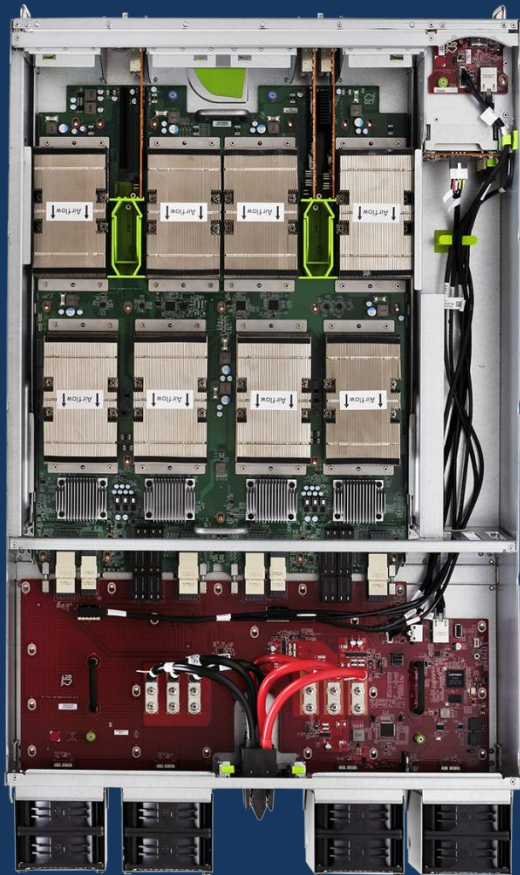
Topology 1





Topology 2

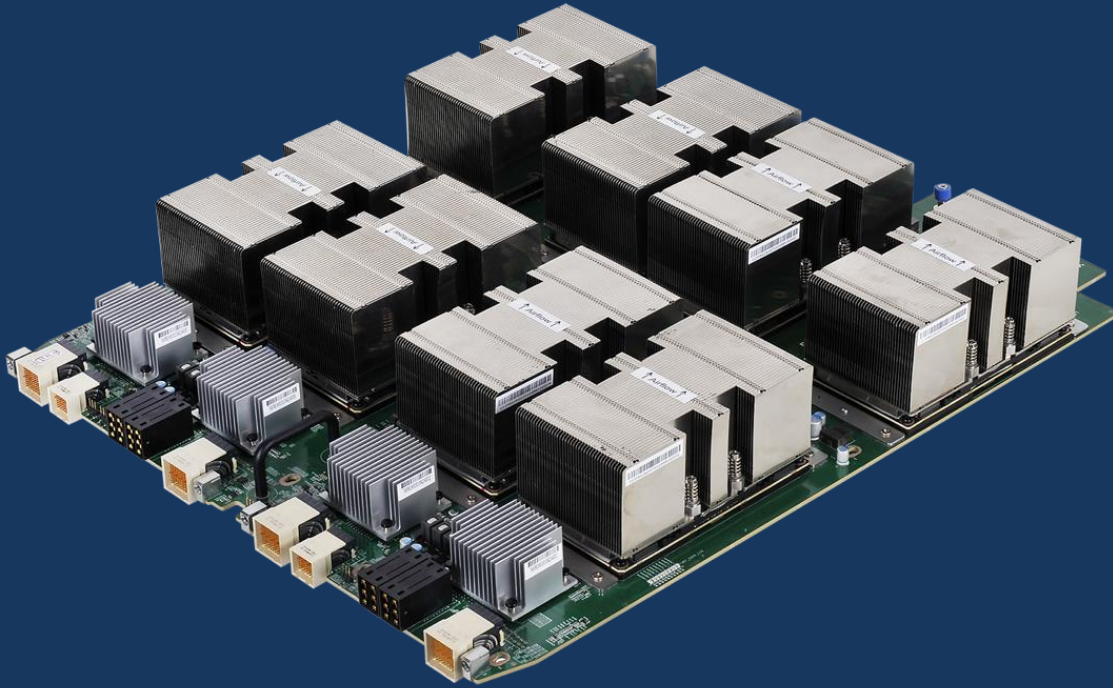




Rackgo X Big Basin System Specifications

Feature	Description
GPUs	(8) Nvidia Tesla P100
GPU Memory	128GB HBM2 (8x 16GB)
Expansion Slot	(4) PCIe gen 3 x16 FHHL slot (2) PCIe gen 3 x8 FHHL slot
Integrated BMC chip	Aspeed AST2500
Management port	(1) dedicated 1GbE RJ45 mgmt port
Front I/O	(1) Debug port
Form Factor	3 OU (Open Rack) Rackmount
Rack Compatible	Open Rack v.2
GPU Throughput	FP16: 170 TFLOPs FP32: 85 TFLOPs FP64: 42.5 TFLOPs

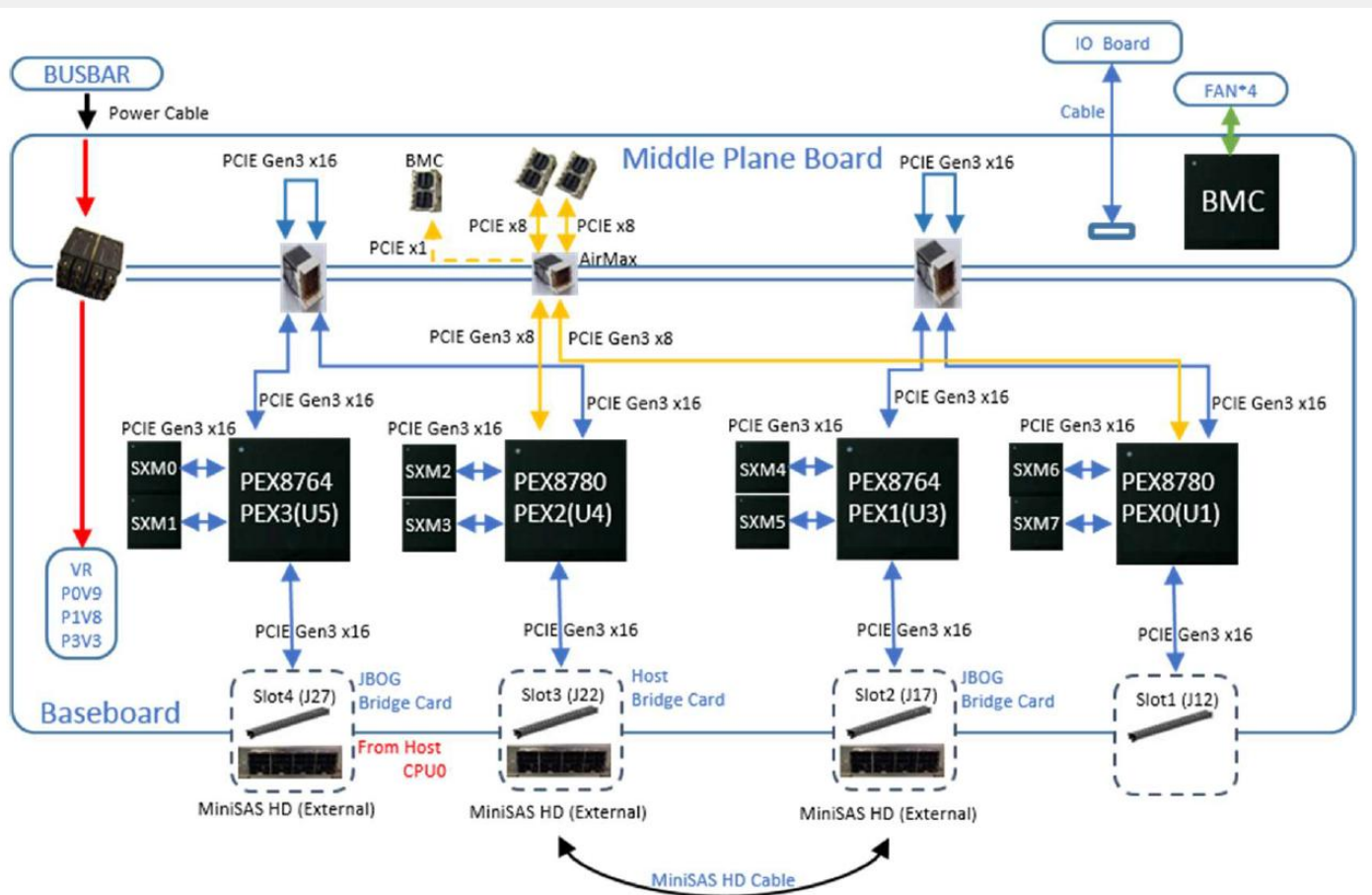
(8x) P100 Baseboard



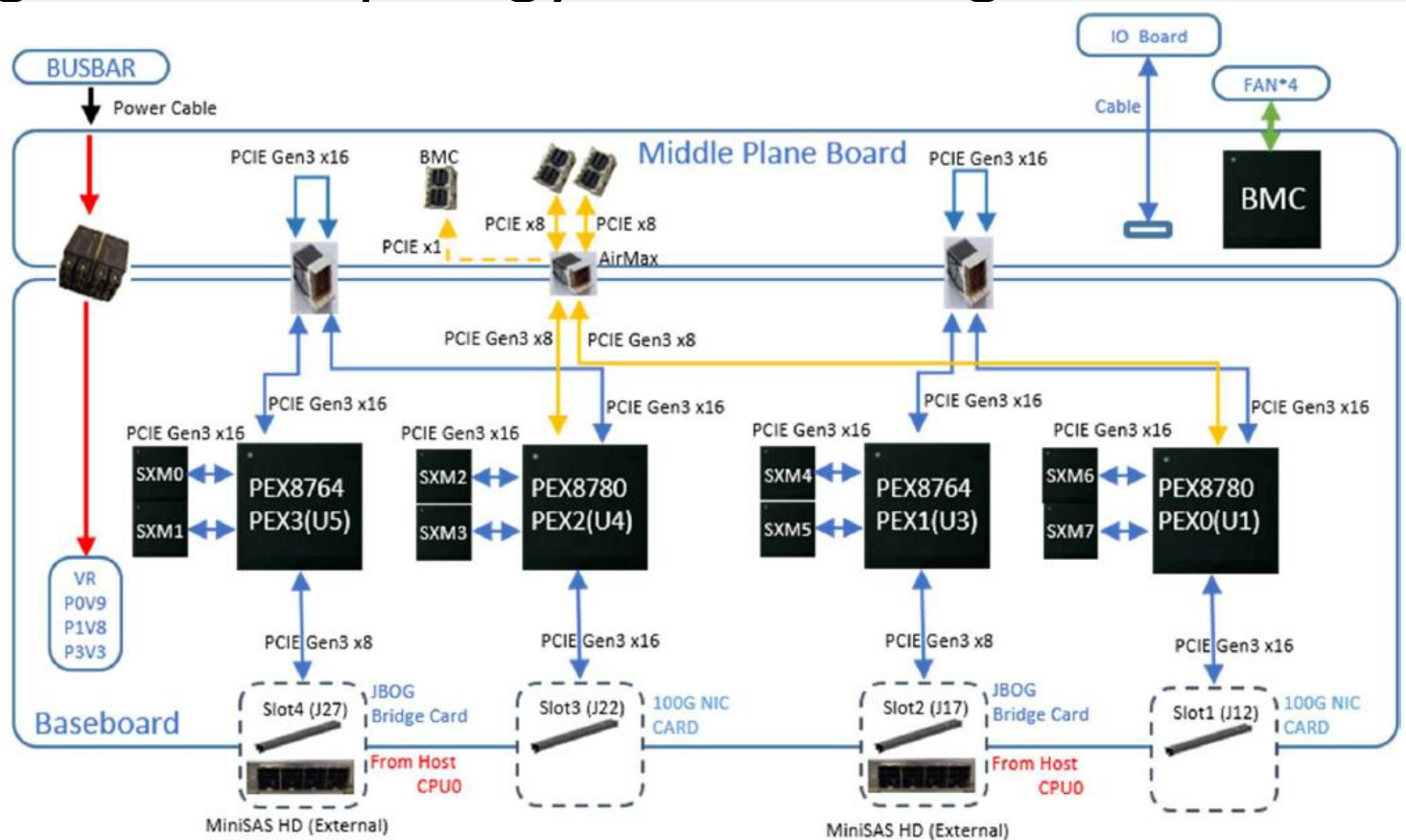
*-- Highest Density (8)
Tesla P100 baseboard*



Single Host Topology 1 Block Diagram



Single Host Topology 2 Block Diagram

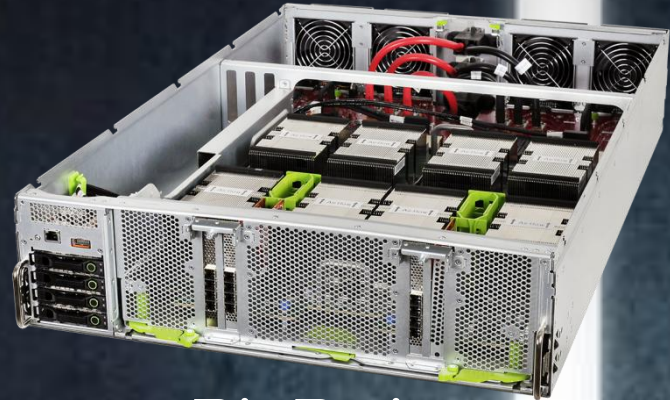


Take “Deep-Learning” to the Next Level !!

AVAILABLE TODAY



Big Sur



Big Basin



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