

Agenda

- JDA Group updates
- OAM Spec v1.0 release candidate
- UBB
- OAM Reference system Enablement
- Next Steps

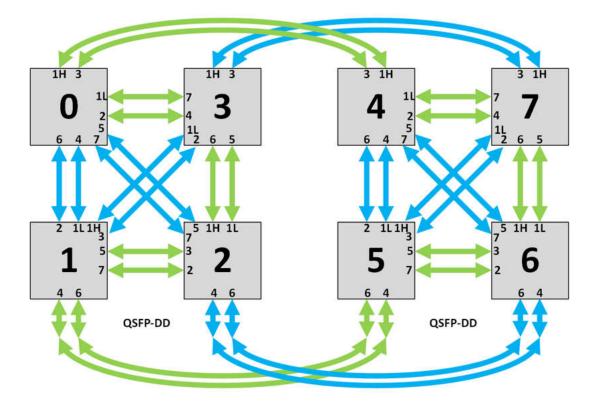
JDA Group Update

- Group members
- UBB Spec lockdown meeting in Beijing
- JDA Addendum

OAM Spec v1.0 Candidate Release by 7/26

- Pin list
- Pin Map
- OAM specification
 - Insertion loss
 - Add 8-port Interconnect Topology
 - Change port R to port 7

Add 8-link HCM Topology to spec

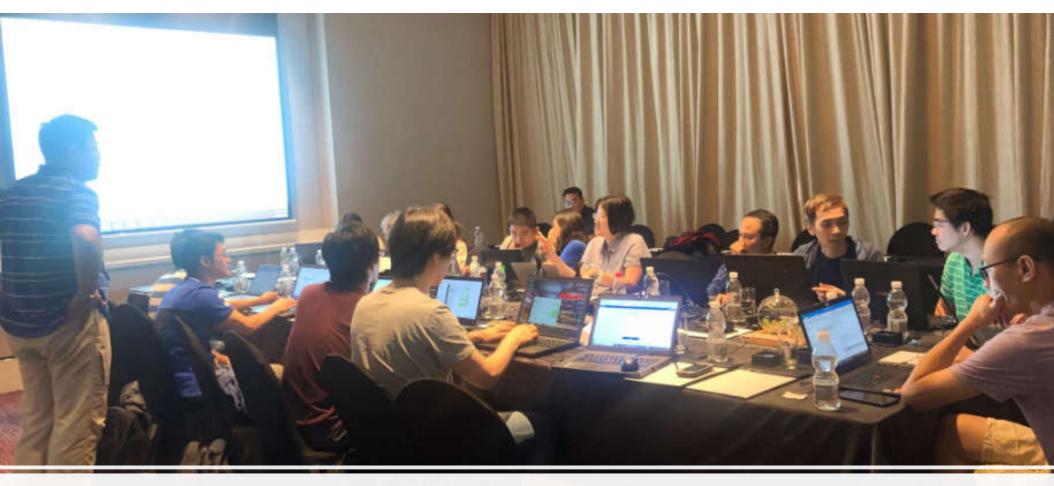


Other Changes

- Restrict OAM Vref range to 1.5V-3.3V
- Add baseboard design recommendation information to pin list

OCP Signal name	Mezz Module Direction POV	OCP Signal Description	Voltage ▽	OAM Baseboard implemention Recommendation
MODULE_ID[4:0]	Input	Module node identifier (e.g. Module #0, #1,#n). Module has weak PU to drive to 1 by default.		Required. Tied to GND through 1K resistor on baseboard for logic 0, leave open for logic 1
LINK_CONFIG[4:0]	Input	Mezz Module Host Interface/SerDes Link Configuration and topology. See link config table for details. Module has weak PU to Vref to drive high by default.		Required. Tied to GND through 1K resistor on baseboard for logic 0, leave open for logic 1

OAM Universal Baseboard(UBB)

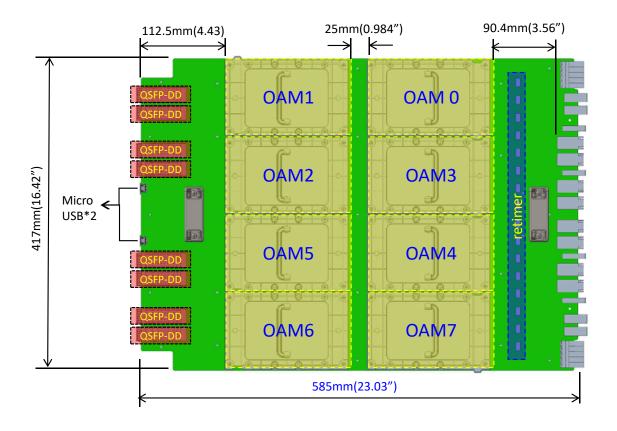


UBB Spec Lock Down Meeting in Beijing

6/26-6/27/2019

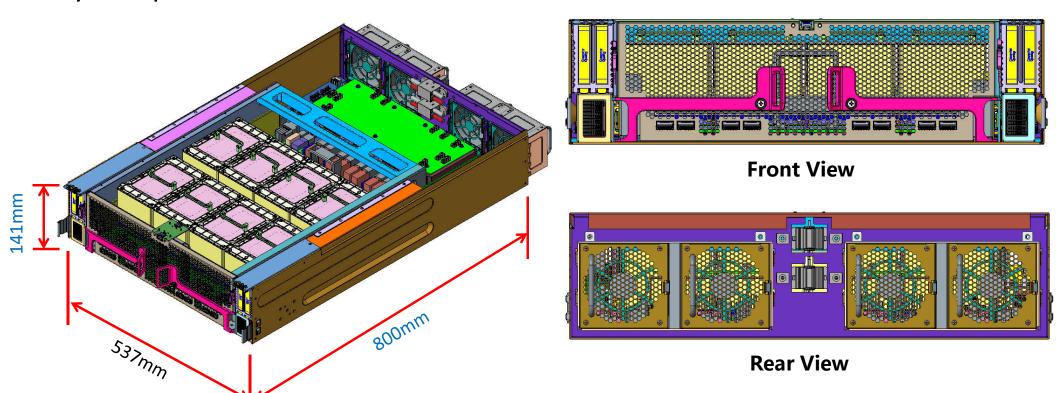
UBB Board Placement

updated



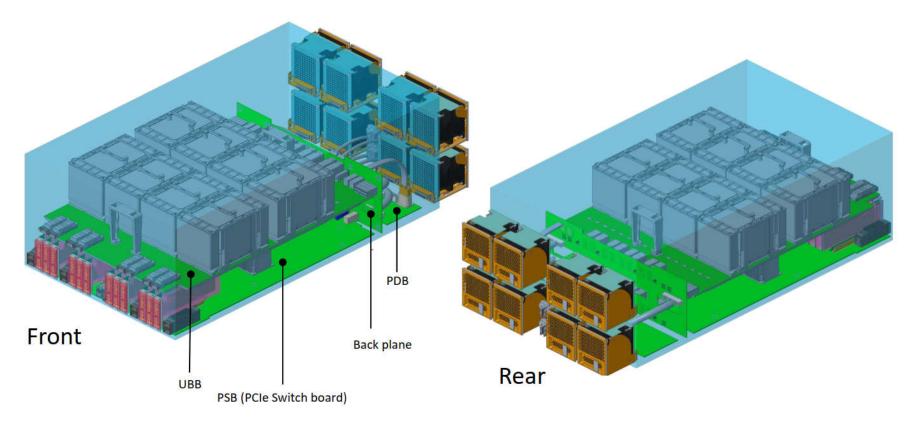
OAM Reference Systems

Reference Systems Design Proposal 1 by Inspur



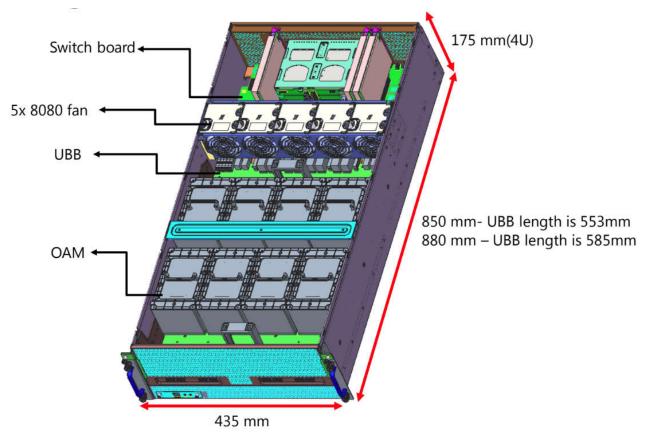
21" Rack, air cool, front I/O, 4*AICs

Reference Systems Design Proposal 2, by HyveDesignSolution



19" Rack, air-cool/liquid cool, front I/O, up to 12*AICs, flexible host

Reference Systems Design Proposal 3 by ZT systems and Inventec



19" Rack, air cool/liquid cool, front/rear I/O, 4/8 AICs, flexible host

UBB

Habana OAM and OAM based system





Next Steps

- OAM test vehicle enablement
 - Provide reference systems to OAM suppliers for validation
 - Provide OAM samples to system providers for validation
- Lockdown UBB design spec
- Amsterdam OAI experience lab