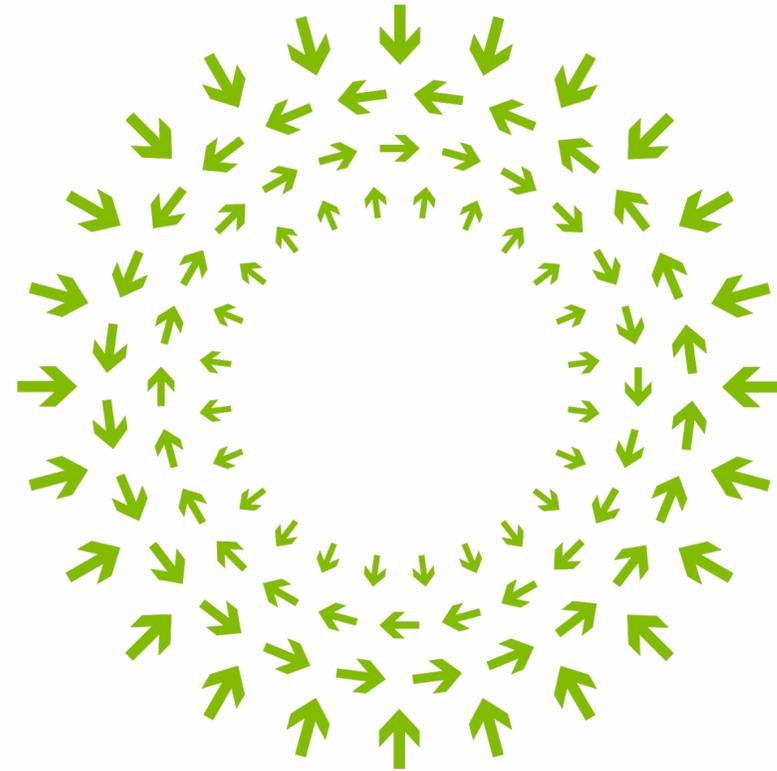


Open FLEX-PON OLT

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OPEN
Compute Project

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Use of FPGA for FlexPON OLT

Future-proof hardware design for current and next generation PON standards - XGS-PON, NG-PON2, 25G PON

Make use of high performing FPGA's in the PON OLT area comparable or exceeding current merchant silicon

Dynamically configure the FPGA's to support higher speeds as subscribers transition to the next gen PON (FPGA's are field upgradable)

Equipment Longevity and faster Time to market with PON evolution

Embedded CPU's in FPGA's

Help reduce CapEx with a migration path to 25G PON

Lower power consumption and efficient space design

Increased port density

FlexPON design goals

OLT burst mode receiver architecture should be optimized for 10G/25G operations

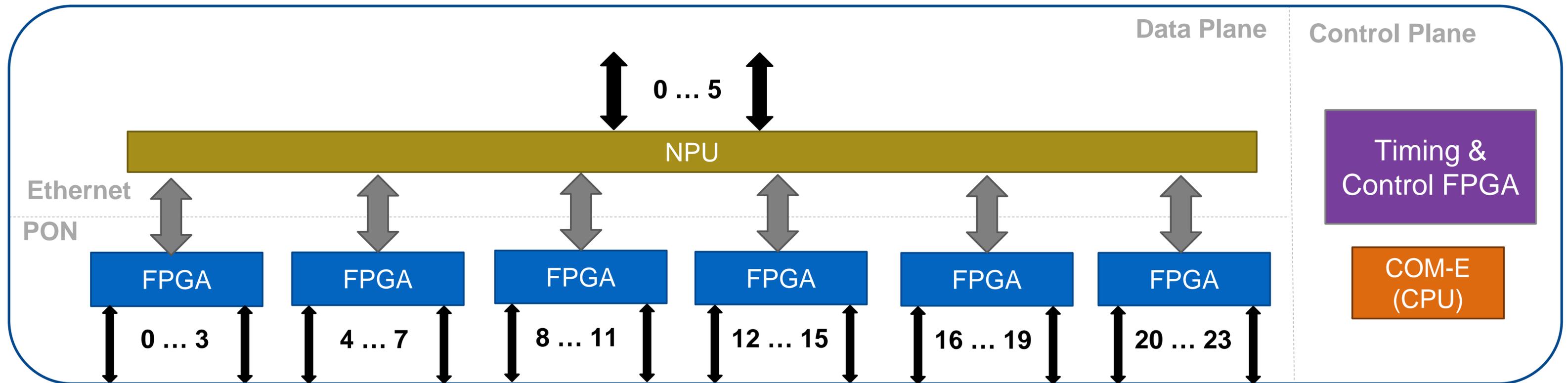
Increase port density to 24 PON ports

Optimal chassis design for efficient power and cooling

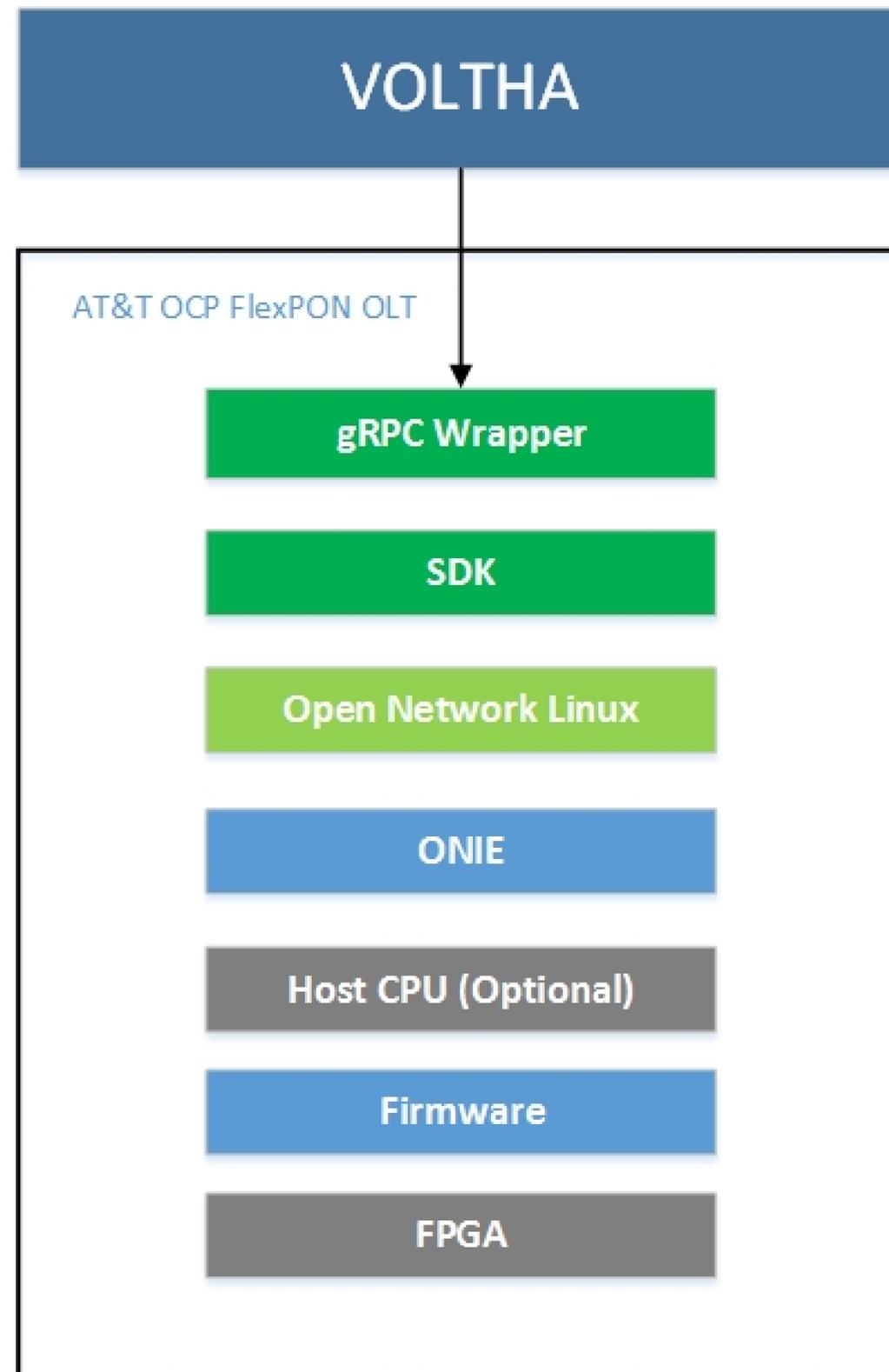
Common optical transceiver interface – 25G (IEEE 802.3) is moving towards SFP28 – challenge with NGPON2?

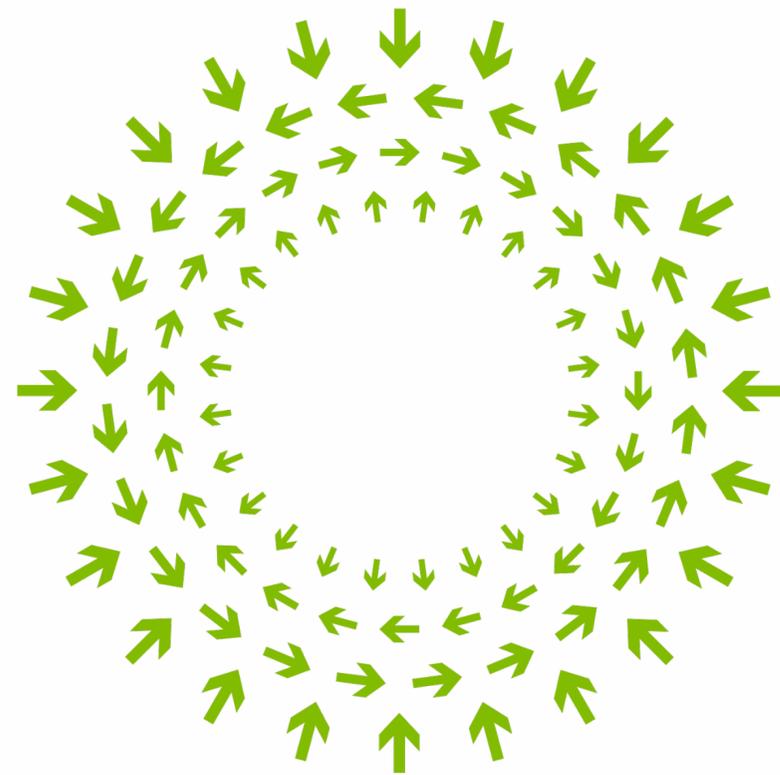
Use of open code FPGA software

High-Level Architecture



FPGA SW Architecture





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