

Lightning PDPB PVT

PCB P/N: 15669

Version: 1

Project Code: BPD00Q010001


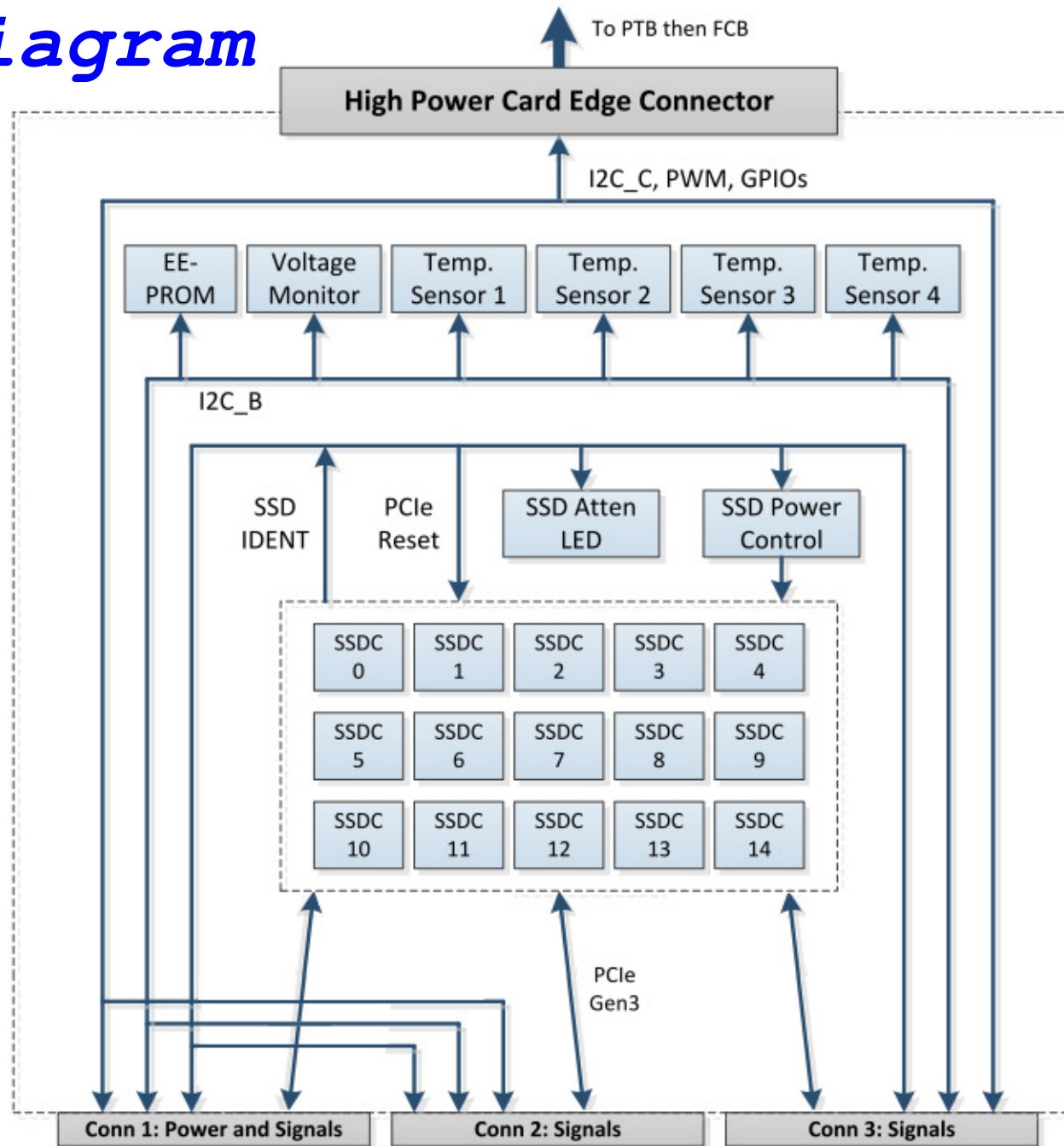
		Wiwynn 8F,90,Sec. 1,Hsin Tai Wu Rd, Hsichih, Taipei Hsien 221, Taiwan, R.O.C <i>H/W R&D Dept. II</i>	
Title COVER PAGE			
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Block Diagram



Wiwynn
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Hsichih, Taipei Hsien 221, Taiwan, R.O.C
H/W R&D Dept. II

Title
BLOCK DIAGRAM

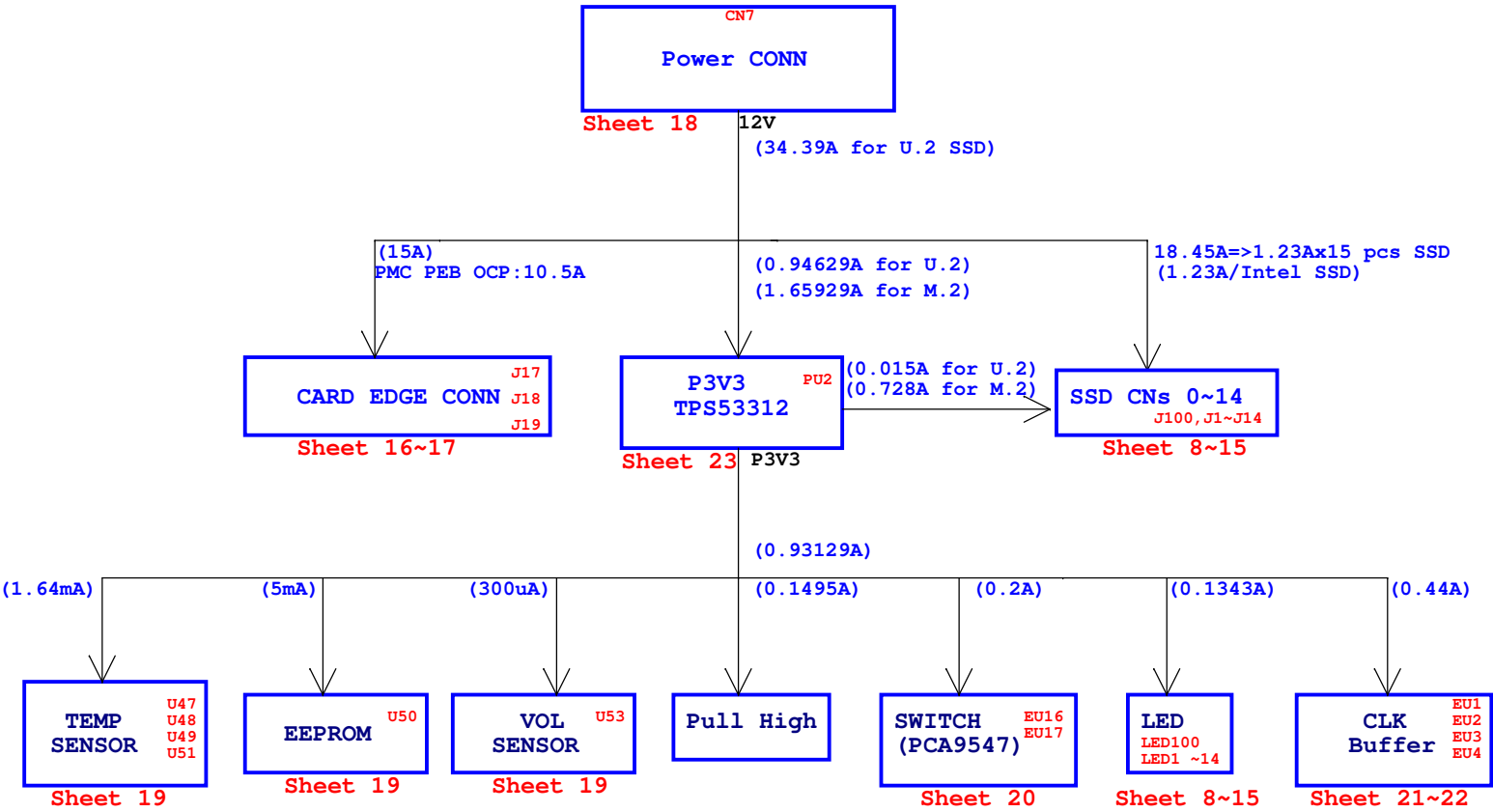
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Rev
1

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POWER MAP

NOPOP=no populated

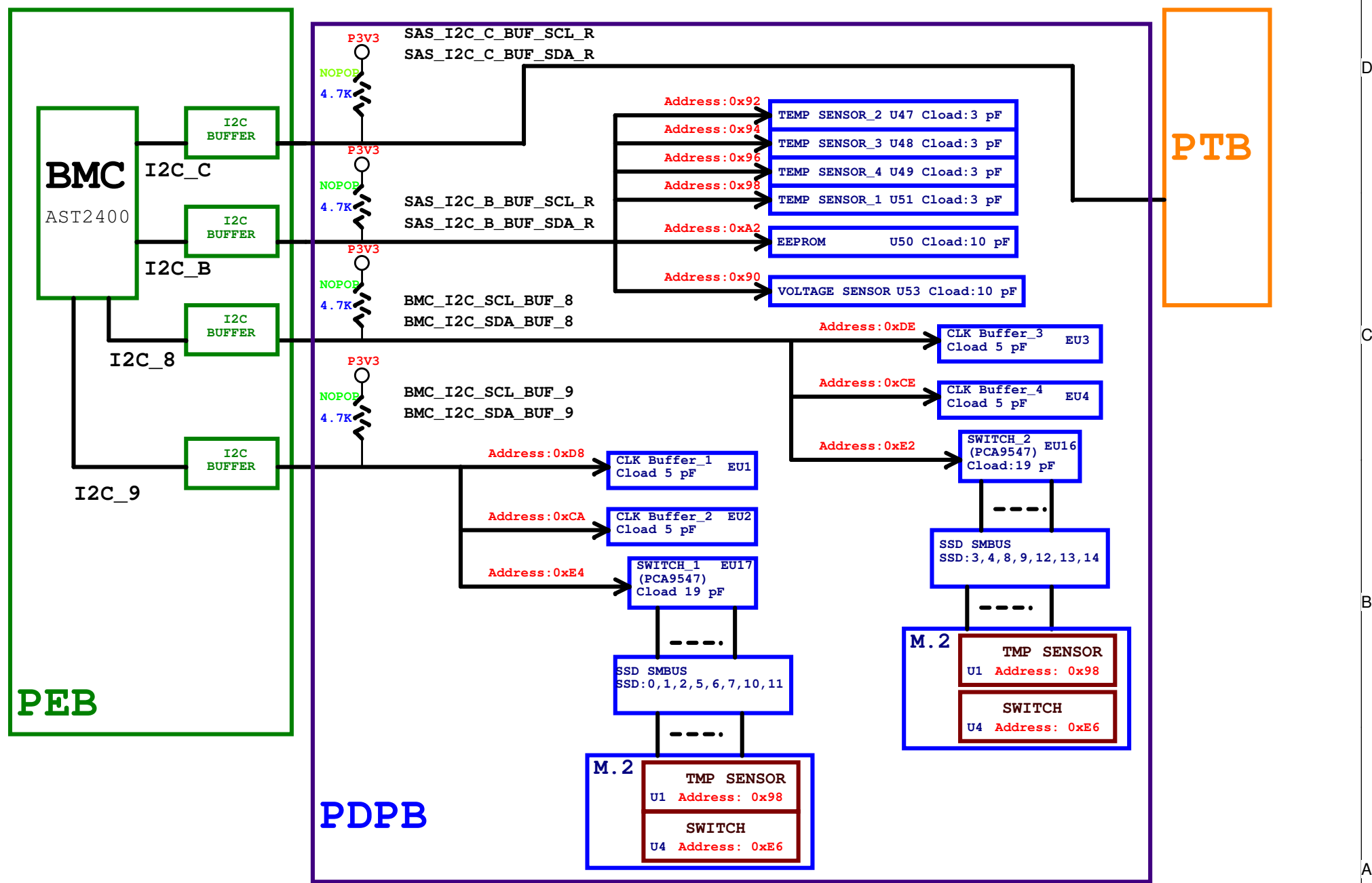


562R2F-GP
562 = 562 ohm, (2K2R means 2.2K ohm)
2 = size 0402
F = 1% tolerance
GP= Green Part (RoHS)
Wiwynn RC size code as below:
1 = 0201
2= 0402
3= 0603
5= 0805
6= 1206
Wiwynn R tolerance code as below:
D=0.5%
F= 1%
J= 5%

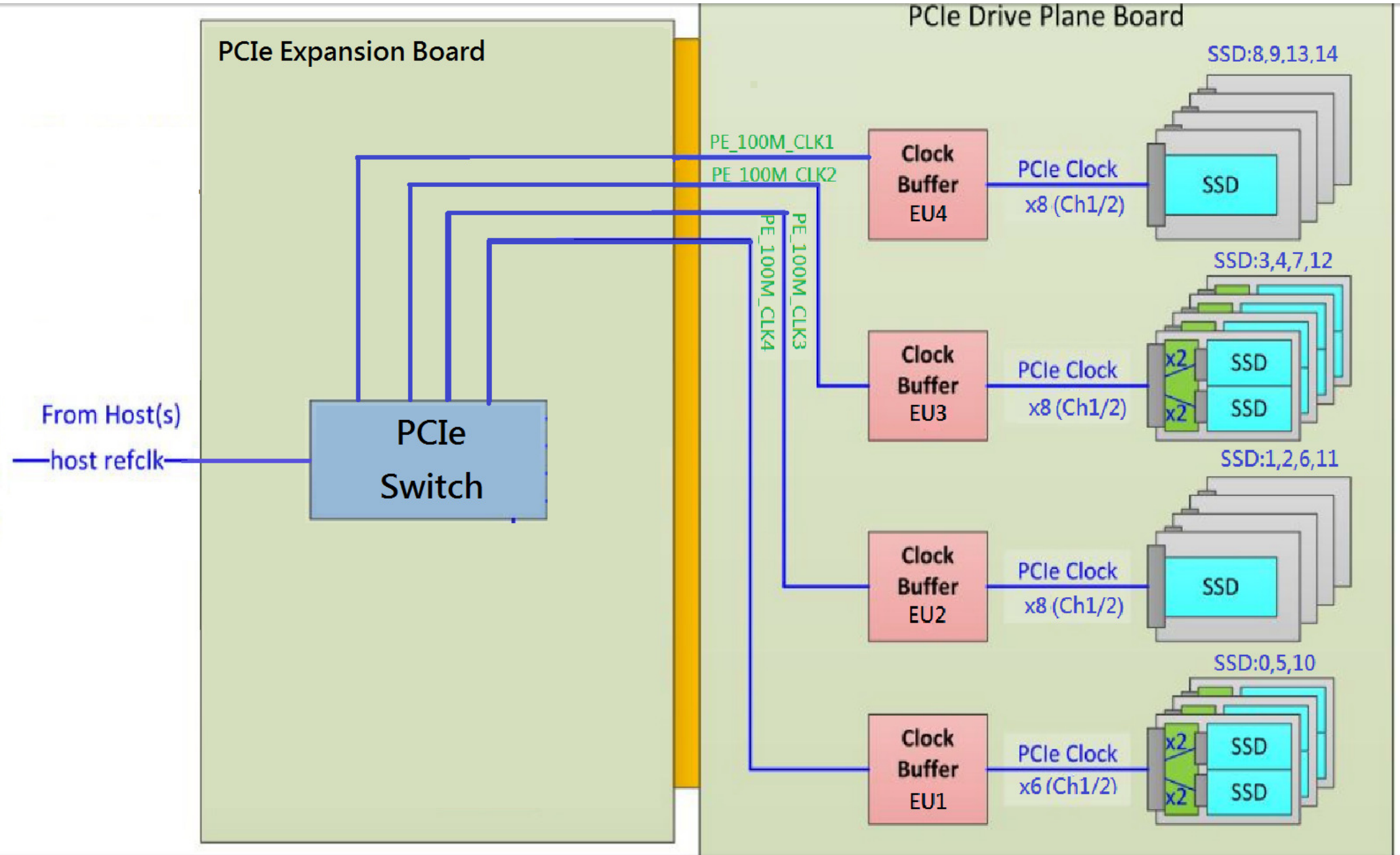
SCD1U10V2KX-5GP
D1U = 0.1uF (2D2U means 2.2uF)
10Voltage (6D3V means 6.3V)
2 = size 0402, K tolerance
K=tolerance
G=2%
J=5%
K=10%
M=20%
X=temp characteristics
[(RoHS) Wiwynn C Series/Temp]
N=NPO
X=X7R/X5R
Y=Y5V
-5=different symbol/customer
GP= Green Part (RoHS)

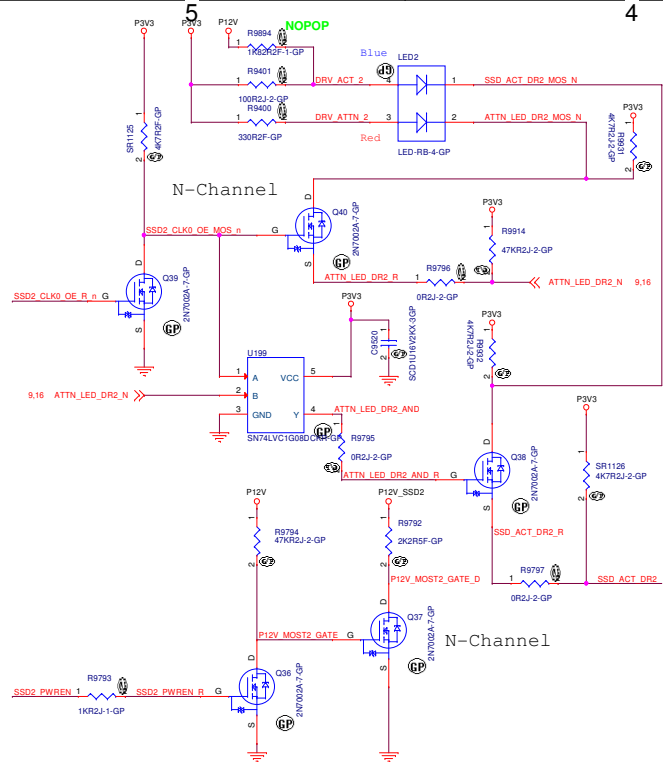
Board Number: 15669-1										Version: 02									
Project name: Lightning_PMC																			
Model Name: PDPB																			
Layer Count: 8 Layer																			
Date: 2016/10/19																			
Material: TU863+VLP / NPG171+VLP																			
Gold Finger(Y/N): N																			
Customer: XXXXX																			
EE engineer: Leon Du																			
SI Engineer: Eason YS Chen																			
										</									

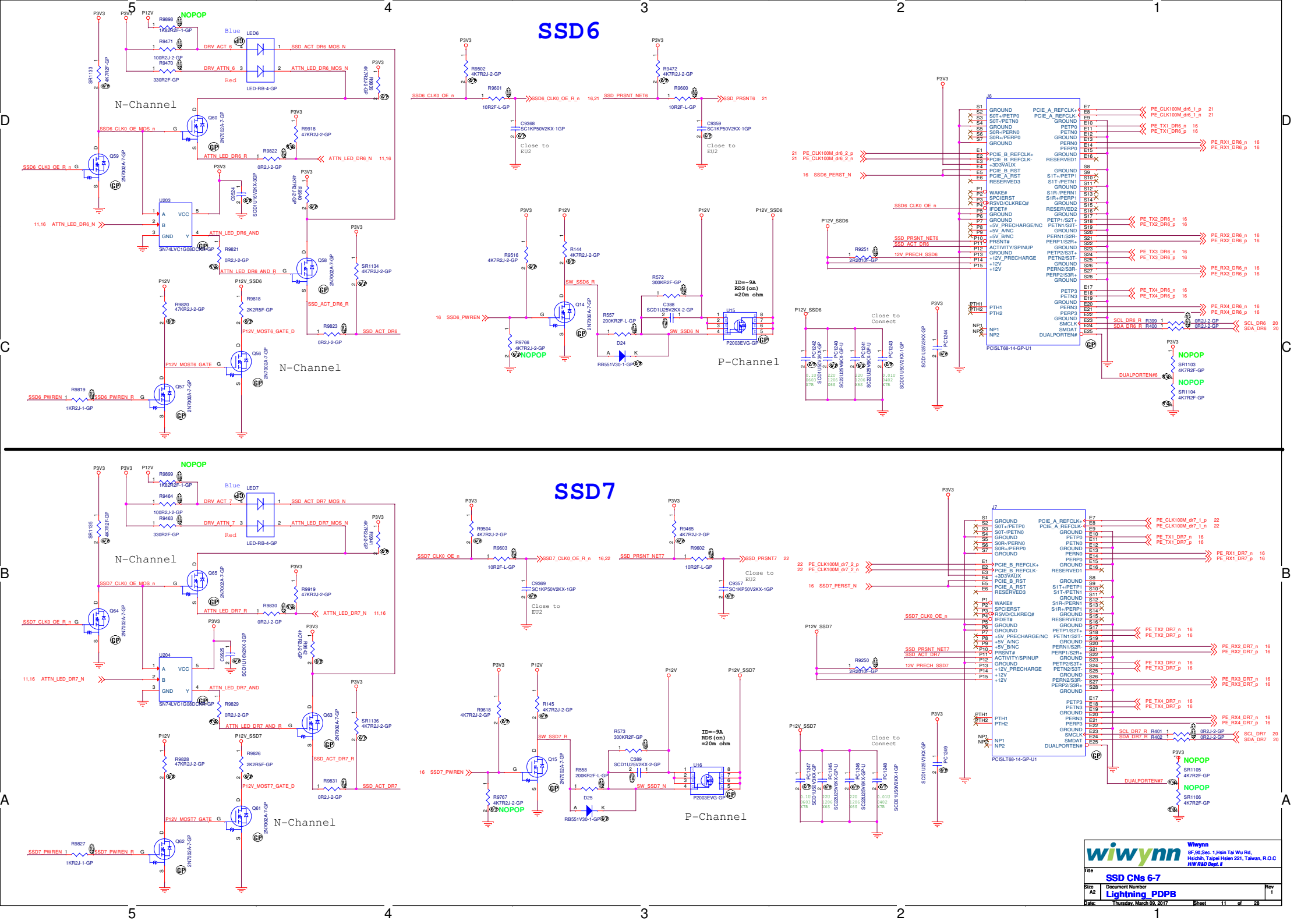
I2C TOPOLOGY

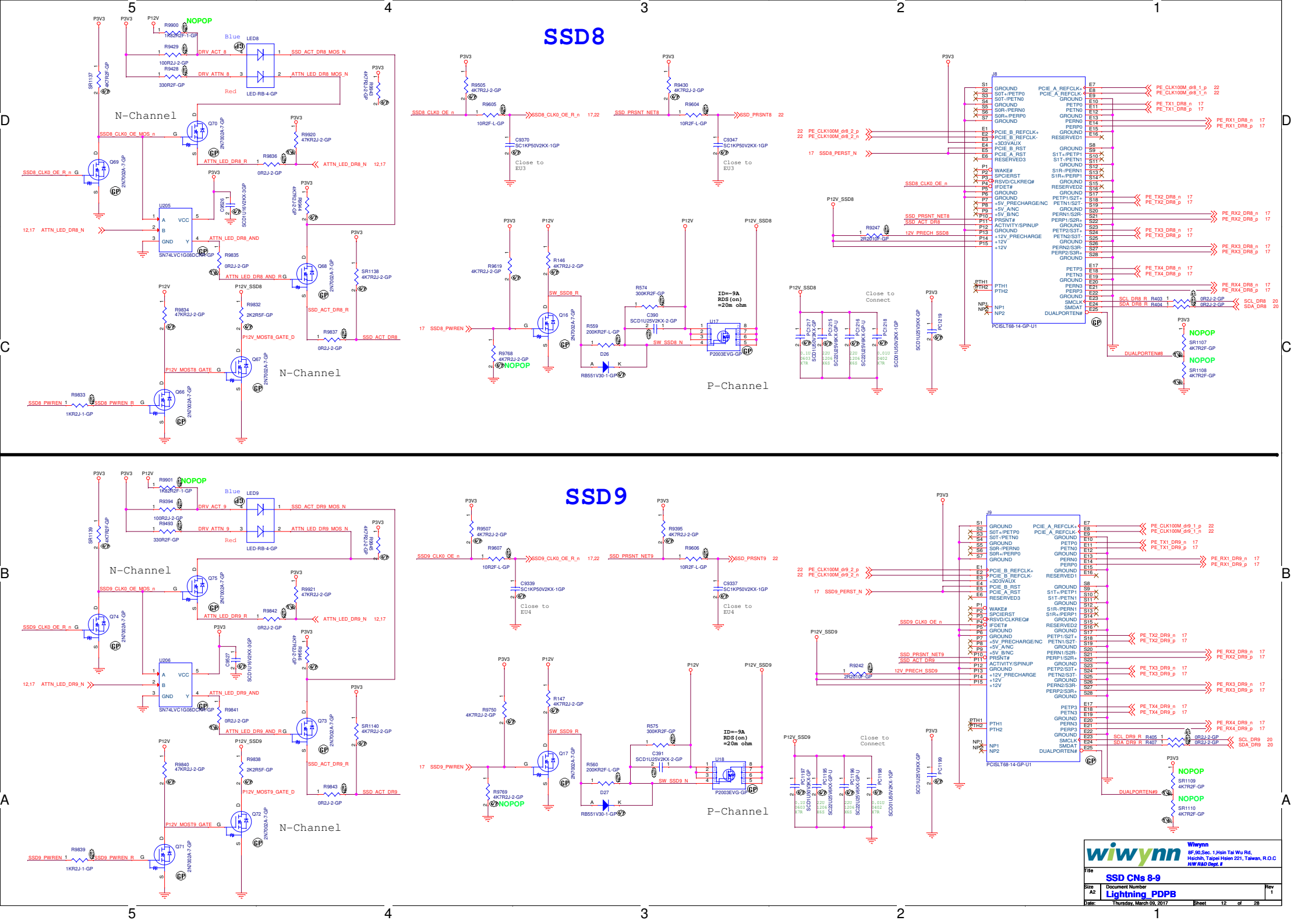


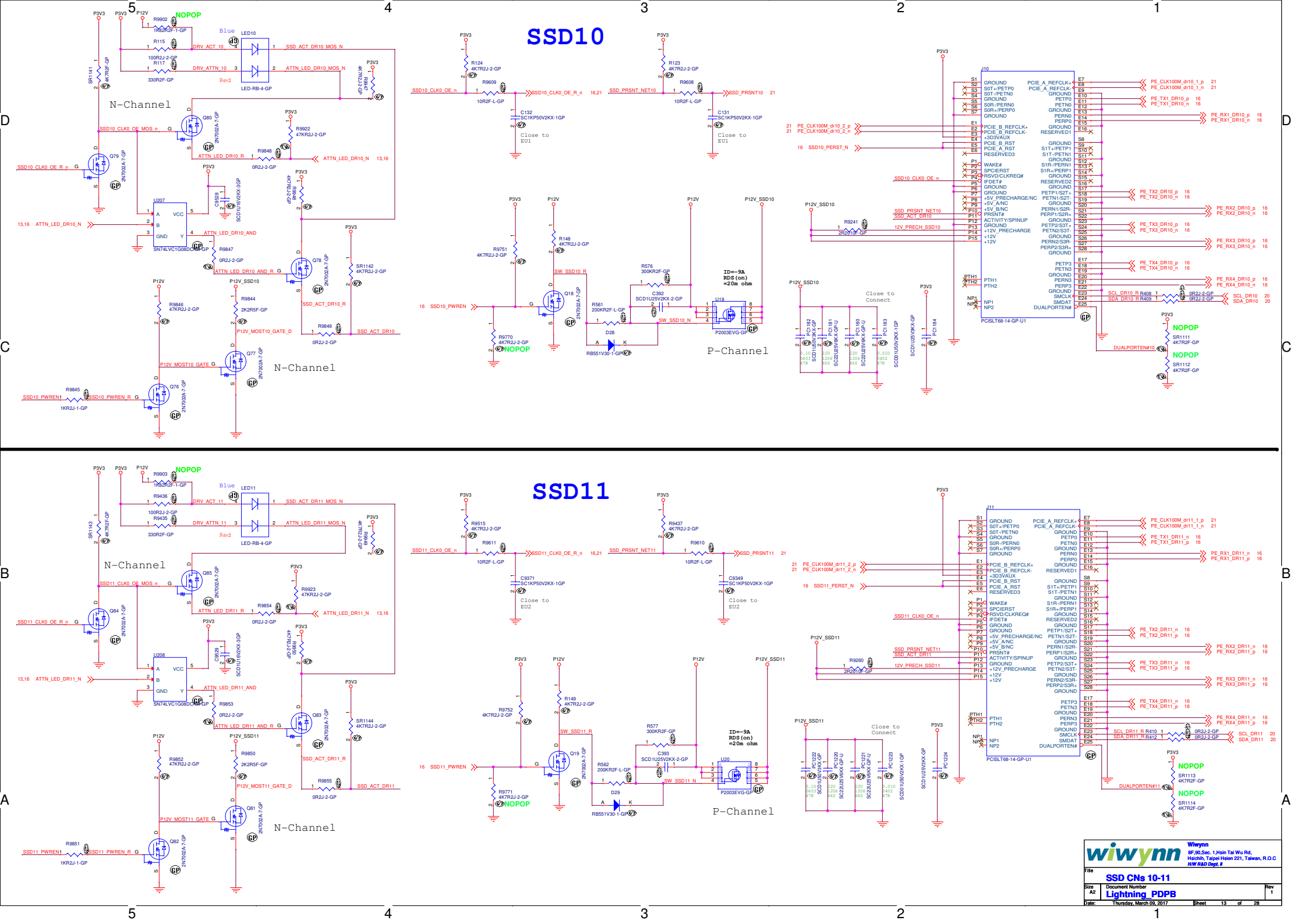
CLOCK TOPOLOGY

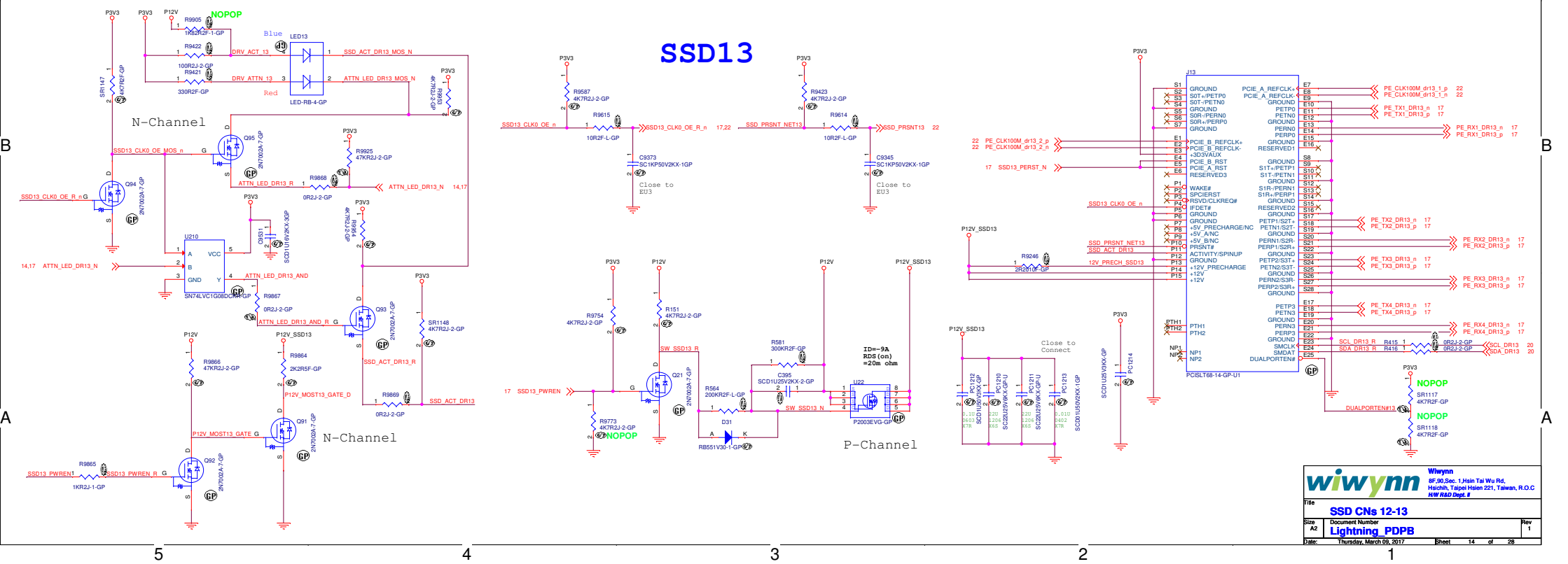
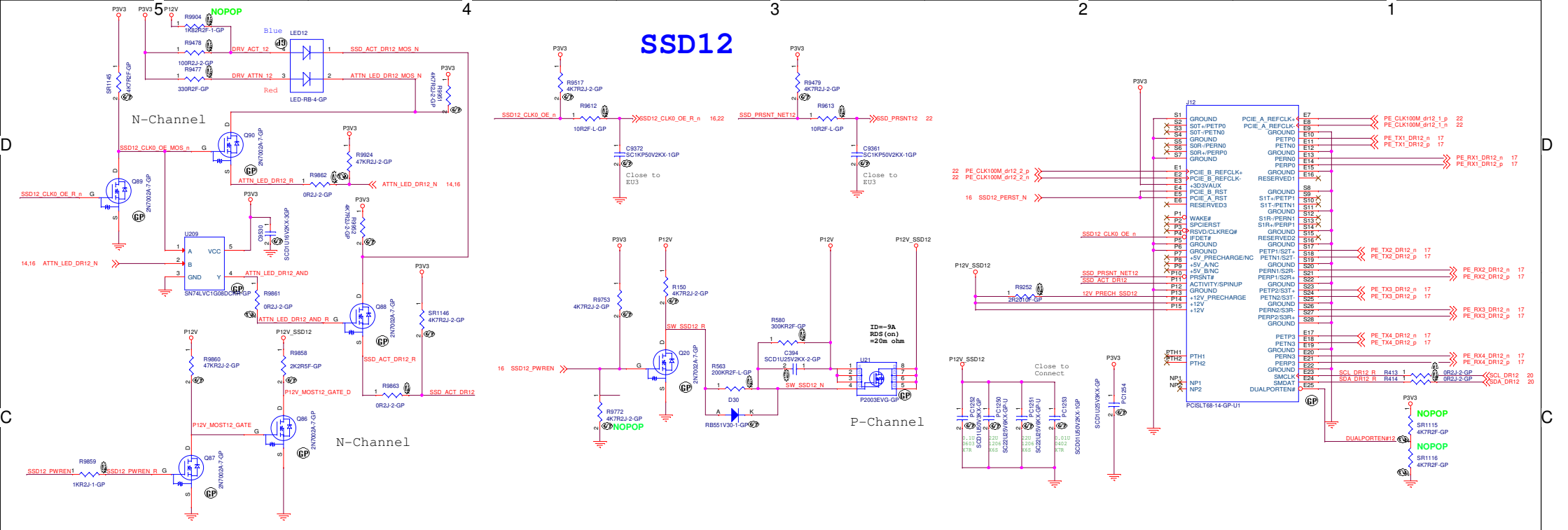


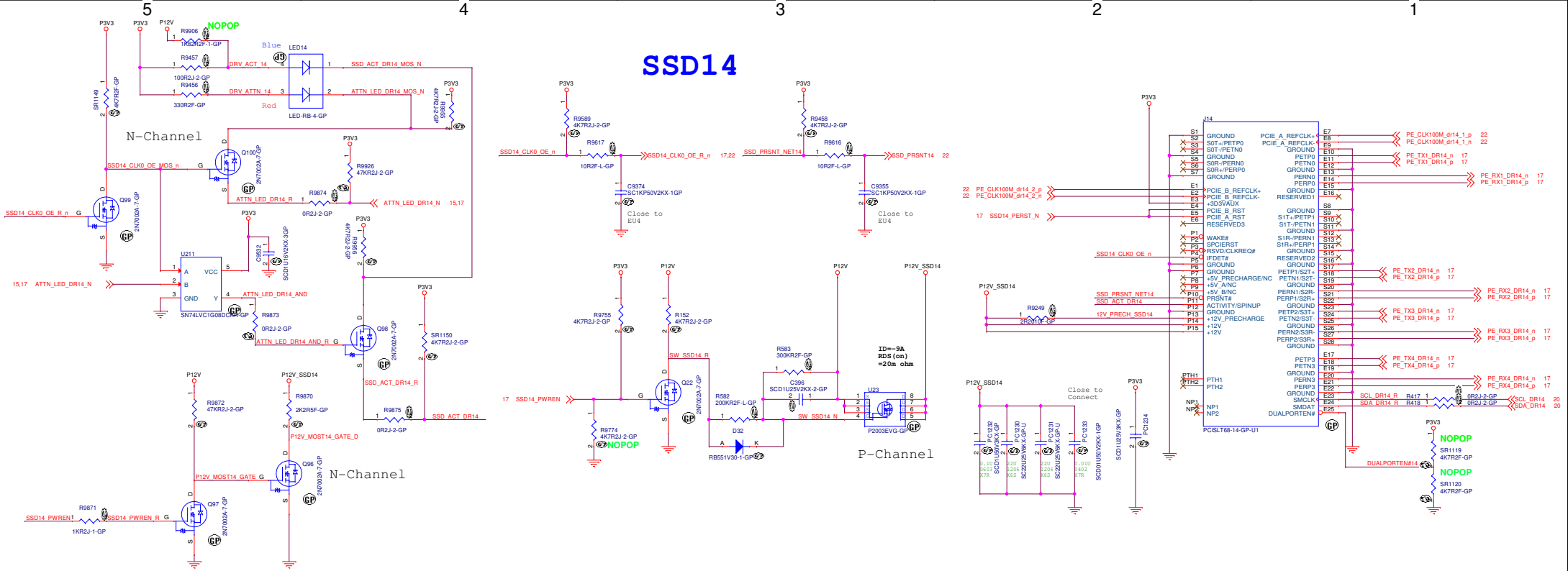








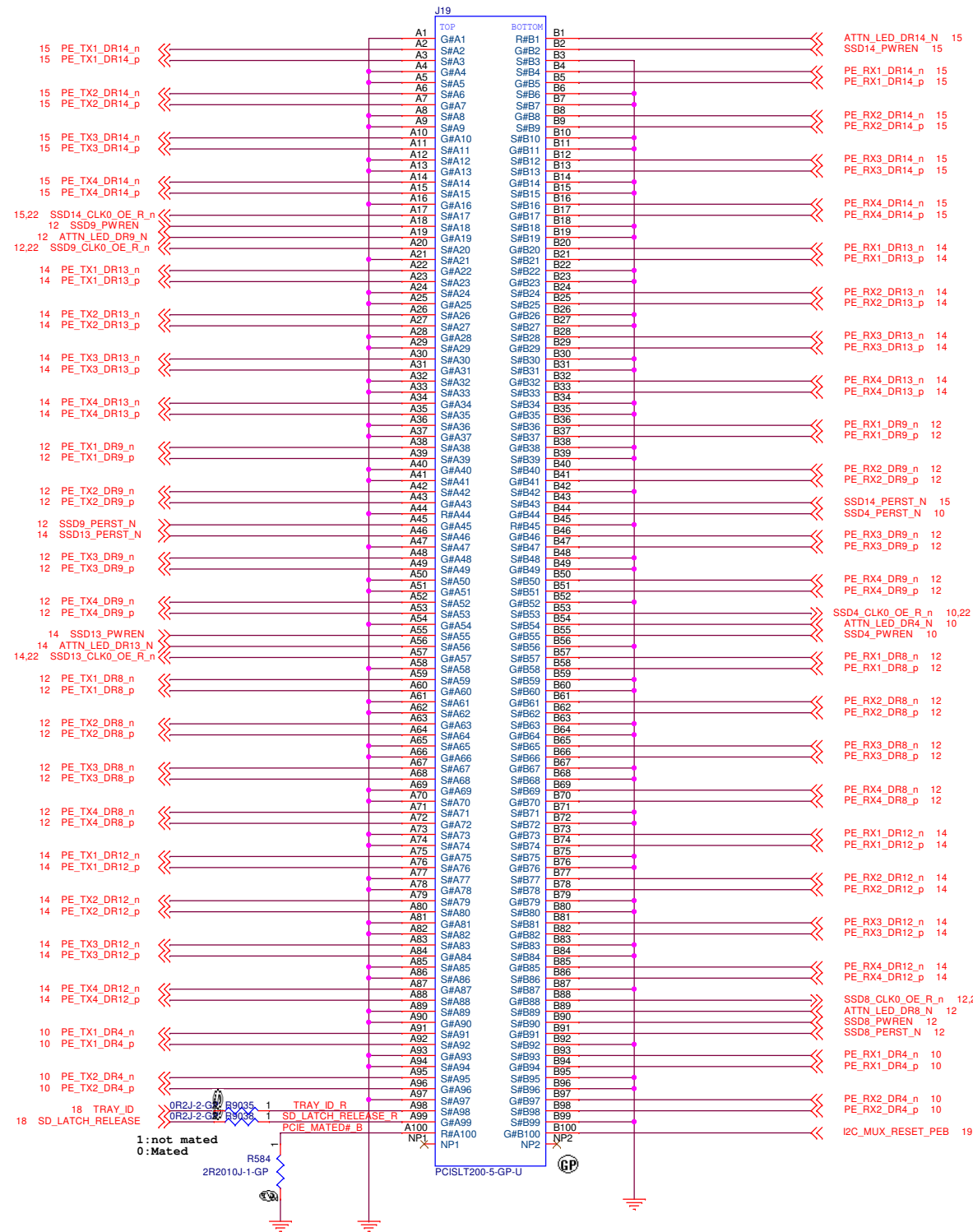




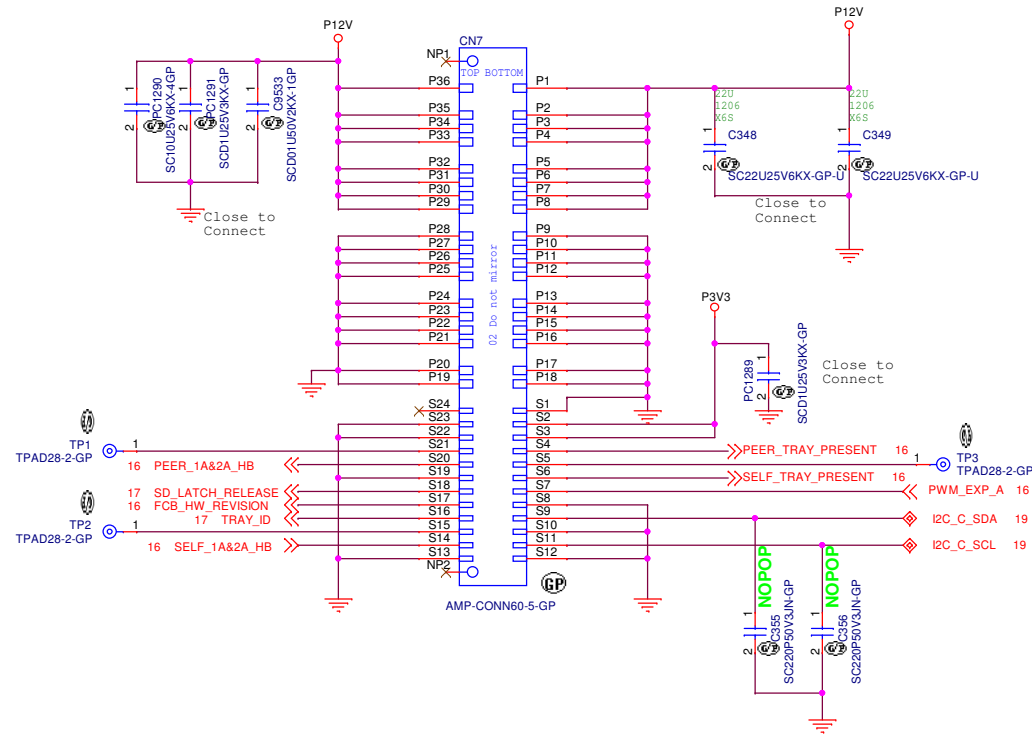
PDPB LED Status

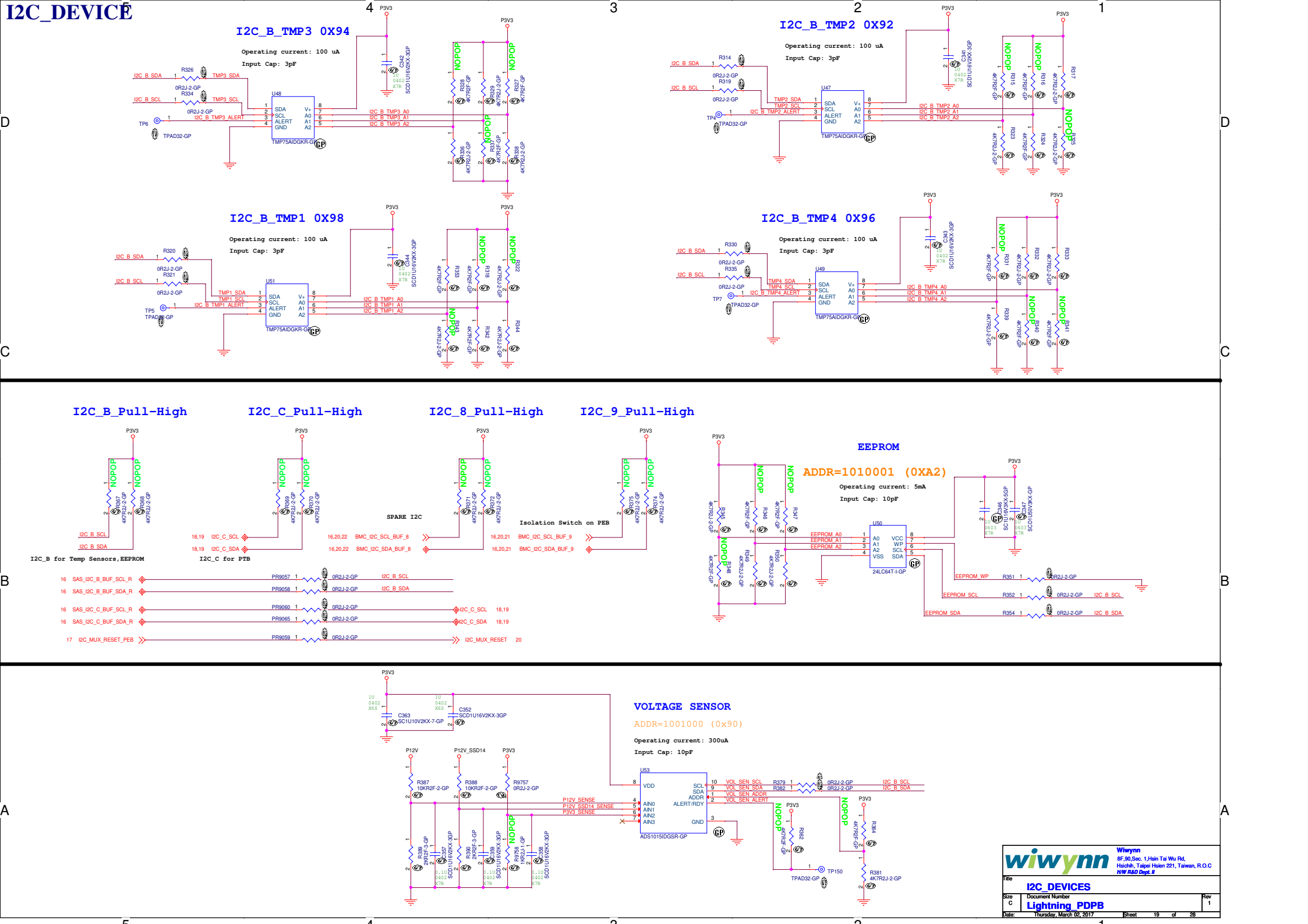
Disk Drive Status	Blue LED	Red LED	IFDET#	ATN#	ACT#	Comments
Drive Online(idle)	ON	OFF	LOW	HIGH	LOW	OS need to identify SSD, IFDET# and PWR_EN status and communicate it to switch to control ATN# SSD vendor need to control ACT#
Drive Online(IO activity)	Blinking(25 0mS on and 250mS off)	OFF	LOW	HIGH	TOGGLING	OS need to identify SSD, IFDET# and PWR_EN status and communicate it to switch to control ATN# SSD vendor need to control ACT#
Drive Failure	OFF	ON	LOW	LOW	Doesn't matter	OS need to identify SSD, IFDET# and PWR_EN status and communicate it to switch to control ATN#
Drive Not Present	OFF	OFF	HIGH	HIGH	Doesn't matter	OS need to identify SSD, IFDET# and PWR_EN status and communicate it to switch to control ATN#
Drive Identify	Blinking	Blinking	LOW	TOGGLING	LOW	OS need to identify SSD, IFDET# and PWR_EN status and communicate it to switch to control ATN# SSD vendor need to control ACT# Red on, Blue off; Red off, Blue on; both LEDs toggling

CARD EDGE CONN3

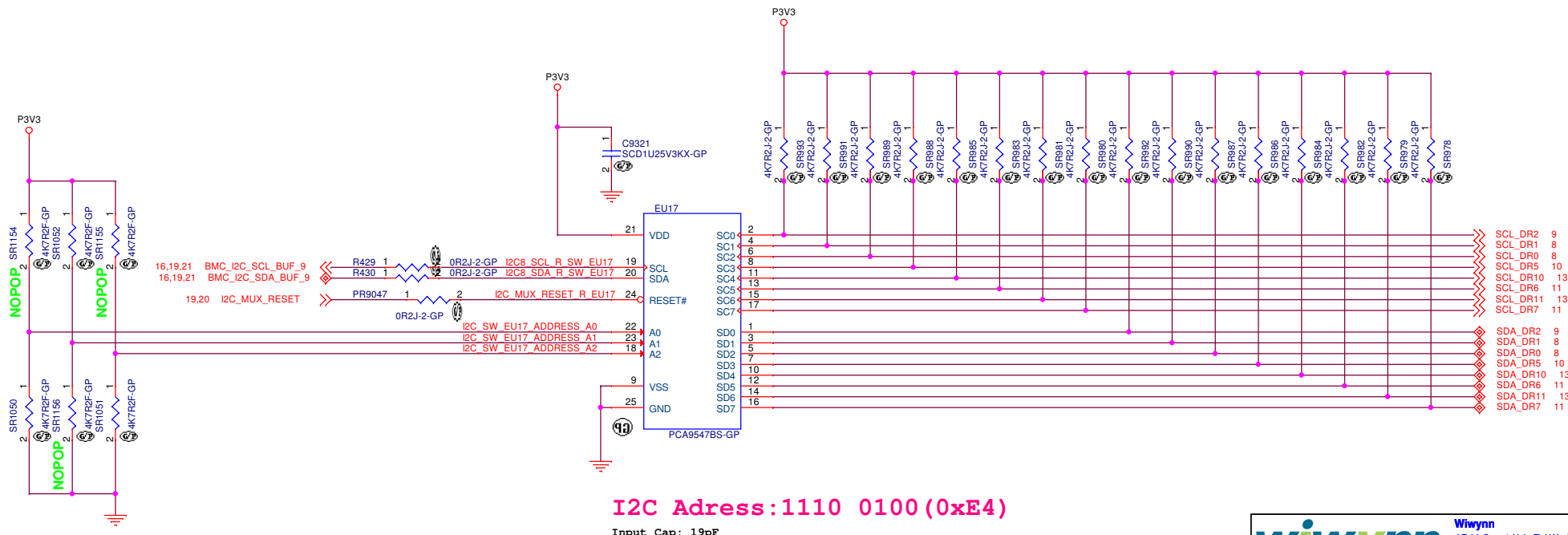
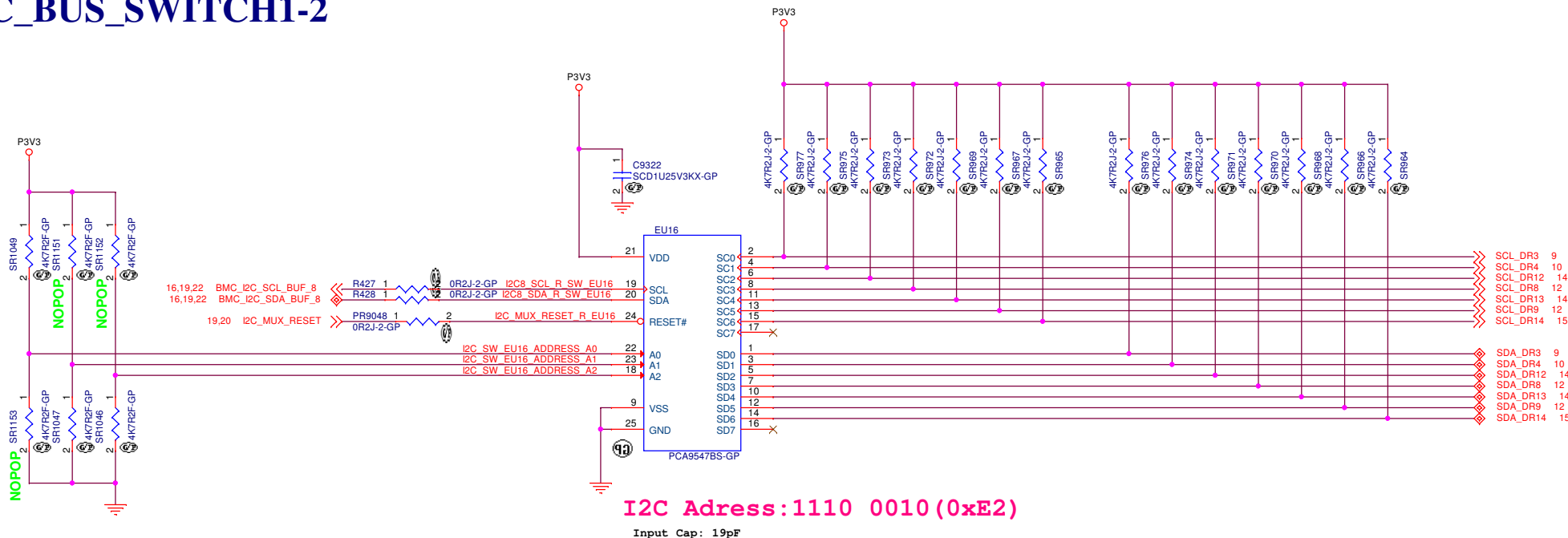


STRADDLE CONN





I2C_BUS_SWITCH1-2



I2C_CLK BUFFER3-4

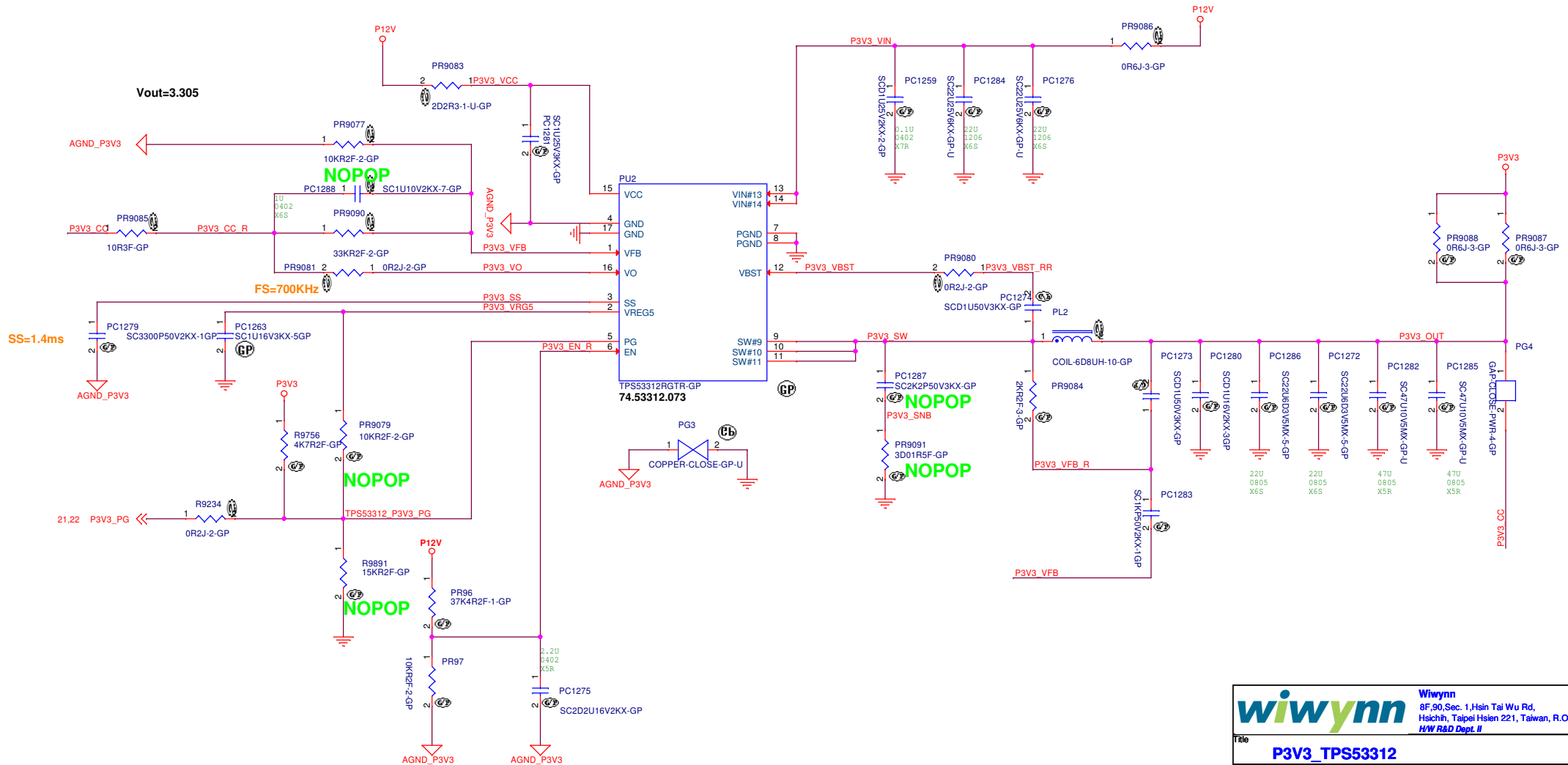
I2C Address: 0xDE

Input Cap: 5pF

I2C Address: 0xCE

Input Cap: 5pF

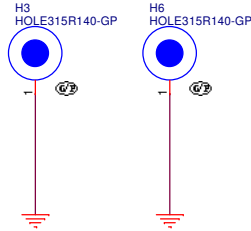
Power rail :P3V3
Controller : TPS53312 (Fswitching=700KHz)
TDC :0.88584A
MAX :0.88584A
OCP=4.1A



SCREW & T-SLOT

SCREW HOLES ARE PTH

SCREW HOLE



T-SLOT HOLE

