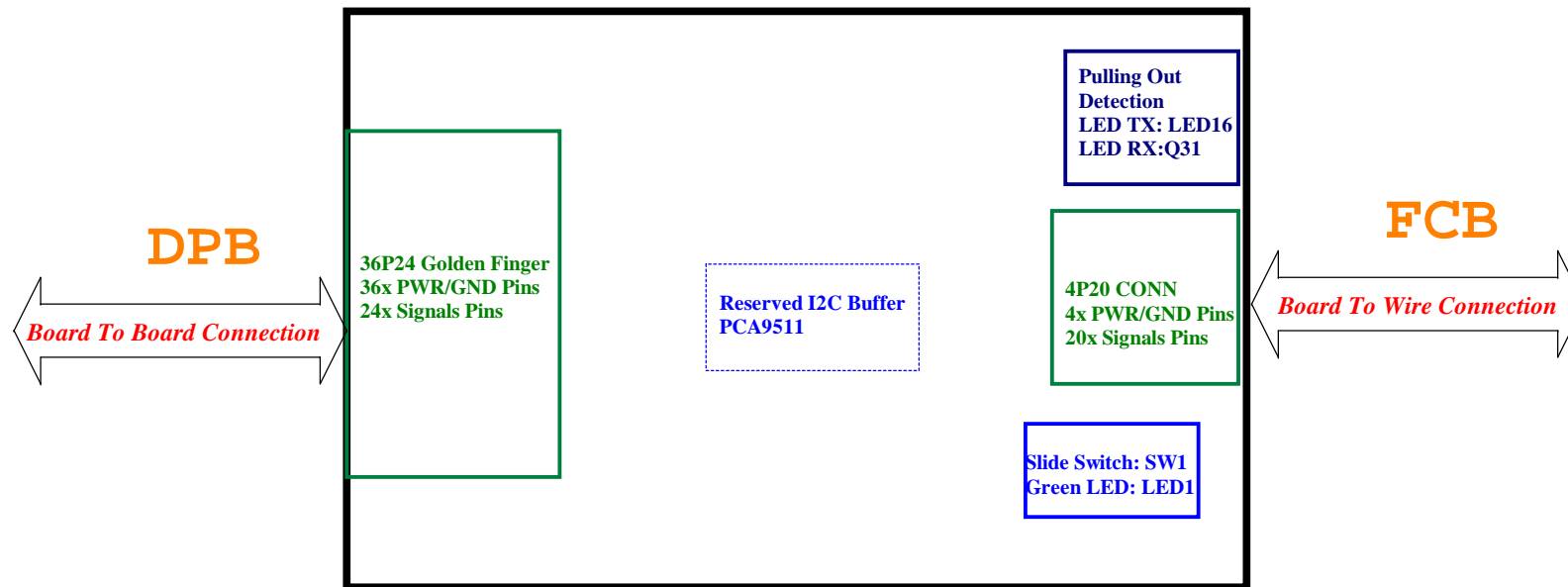


POWER TRANSITION BOARD



Revision Histroy:

DVT V04:2012/07/21 DVT Gerber Release

PVT V03:2012/10/16 PVT Gerber Release

PIN Definition

Golden Finger To DPB

Pin	Pin name	Type
S1	GND	GND
S2	3.3V	PWR
S3	3.3V	PWR
S4	PEER_TRAY_PRESENT	H/L
S5	PWM_EXP_B	PWM
S6	SELF_TRAY_PRESENT	H/L
S7	PWM_EXP_A	PWM
S8	GND	GND
S9	I2C_C_SDA	PWM
S10	GND	GND
S11	I2C_C_SCL	PWM
S12	GND	GND
S13	GND	GND
S14	SELF_1A&2A_HB	H/L
S15	SELF_1B&2B_HB	H/L
S16	TRAY_ID	H/L
S17	FCB_HW_REV	H/L
S18	SD_LATCH_RELEASE	H/L
S19	GND	GND
S20	PEER_1A&2A_HB	H/L
S21	PEER_1B&2B_HB	H/L
S22	GND	GND
S23	GND	GND
S24	5VA	PWR

4P20S To FCB

Pin	Pin name	Type
A1	I2C_C_SDA	PWM
A2	GND	GND
A3	I2C_C_SCL	PWM
A4	GND	GND
A5	PWM_1B/2B	PWM
B1	GND	GND
B2	PWM_1A/2A	PWM
B3	GND	GND
B4	SELF_1B&2B_HB	H/L
B5	GND	GND
C1	TPS2490_EN1	H/L
C2	TPS2490_EN2	H/L
C3	SELF_1A&2A_HB	H/L
C4	PEER_1A&2A_HB	H/L
C5	PEER_1B&2B_HB	H/L
D1	PEER_TRAY_PRESENT	H/L
D2	TRAY_ID	H/L
D3	FCB_HW_REV	H/L
D4	SD_LATCH_RELEASE	H/L
D5	SELF_TRAY_PRESENT	H/L

DY=Dummy parts
=not populated


SCD1U10V2KX-5GP
D1U = 0.1uF (2D2U means 2.2uF)
10Voltage (6D3V means 6.3V)
2 = size 0402, K tolerance
K=tolerance
[Wistron C code as below:]
G=2%
J=5%
K=10%
M=20%
X=temp characteristics
[Wistron C Series/Temp]
N=NPO
X=X7R/X5R
Y=Y5V
-5=different symbol/customer
GP= Green Part (RoHS)

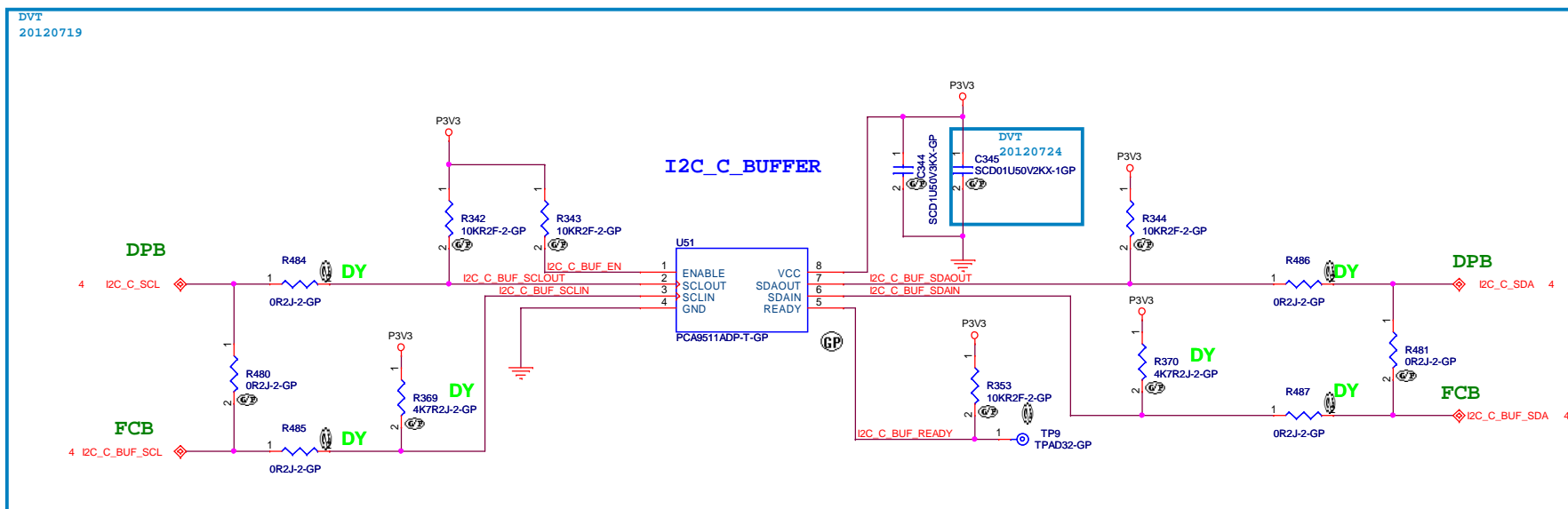
562R2F- GP
562 = 562 ohm, (2K2R means 2.2K ohm)
2 = size 0402
F = 1% tolerance
GP= Green Part (RoHS)
Wistron RC size code as below:
1 = 0201
2= 0402
3= 0603
5= 0805
6= 1206
Wistron R tolerance code as below:
D=0.5%
F= 1%
J= 5%

24 Pins PWR/GND

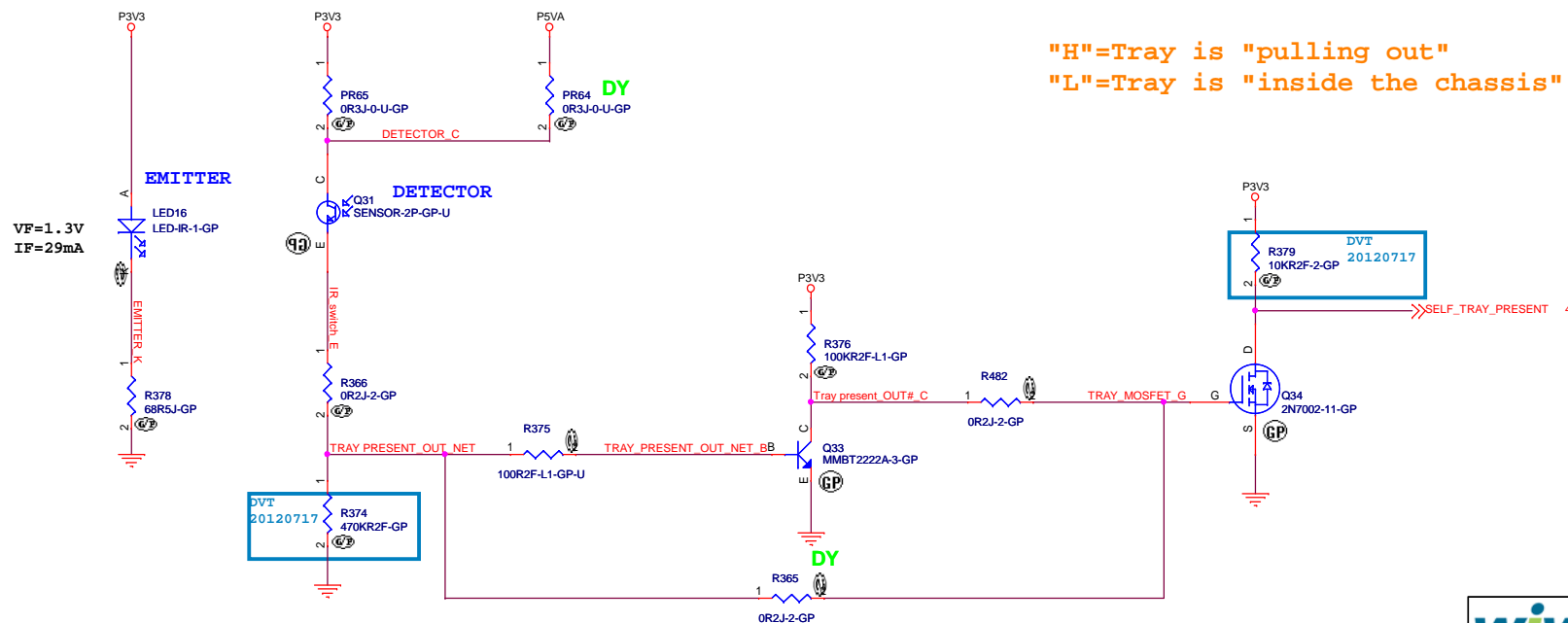
Pin	Pin name	Type	Pin	Pin name	Type
P1	12.5V	PWR	P9	GND	GND
P2	12.5V	PWR	P10	GND	GND
P3	12.5V	PWR	P11	GND	GND
P4	12.5V	PWR	P12	GND	GND
P5	12.5V	PWR	P13	GND	GND
P6	12.5V	PWR	P14	GND	GND
P7	12.5V	PWR	P15	GND	GND
P8	12.5V	PWR	P16	GND	GND
P19	GND	GND	P17	GND	GND
P20	GND	GND	P18	GND	GND
P21	GND	GND	P29	12.5V	PWR
P22	GND	GND	P30	12.5V	PWR
P23	GND	GND	P31	12.5V	PWR
P24	GND	GND	P32	12.5V	PWR
P25	GND	GND	P33	12.5V	PWR
P26	GND	GND	P34	12.5V	PWR
P27	GND	GND	P35	12.5V	PWR
P28	GND	GND	P36	12.5V	PWR

	PCB VENDOR	DVT BUILD	DVT BUILD
PCBA_DIP	GCE	55.64W04.DA1G	
	Hannstar	55.64W04.DA2G	
PCBA_SMT	GCE	55.64W04.SA1G	
	Hannstar	55.64W04.SA2G	
PCB P/N	GCE	48.64W13.0SA	
	Hannstar	48.64W15.0SA	
PCB VER		12523-SA	12523-1

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PULLING OUT DETECTION



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V02: (Andy #6068)

2012/07/11

1. Change C348 and C349 from 10U/16V to 10U/25V to have more voltage derating, sheet 4.
2. I2C_C buffer circuitry fine tune for offset voltage improvement, sheet 3.
 - a.) remove reserved I2C buffer U55 PCA9507 and relative components, C365, C366, C364, C356 and R479.
 - b.) depopulate I2C buffer U51 PCA9511 and relative components R369, R370, R343, C345 and C344.

D V03: (Andy #6068)

2012/07/17

1. Pulling out detection circuitry fine tune, sheet 3.
 - a.) Change dummy part R374 from 8.06K to 470K to enlarge the voltage of VB.
 - b.) Change R379 from 100K to 10K.

V04: (Andy #6068)

2012/07/19

1. Populate I2C_C buffer PCA9511 and relative components.
 - a.) Swap the IN/OUT direction, IN connects to FCB side, OUT connects to DPB.
 - b.) The PU resistors on DPB are R342 and R344, change the value of resistance from 4.7K to 10K.
 - c.) The PU resistors on FCB are R370 and R369, change the value of resistance from 10K to 4.7K, default is "Dummy".
 - d.) Add extra 0 ohm R484 and R487, default is "dummy", bypass the path of buffer.
 - e.) Correct the connection of "READY" pin, in PCA9525 , it is for direction selection, in PCA9511, it is ready pin. It needs to tie 3.3V.
2. Reserve the location of damping R and bypass C, sheet 4.
 - a.) Reserve 220pF cap on location C359 and C360.
 - b.) Add 0 ohm R on location R388 and R389.

2012/07/20

1. Connect SW1 Pin 3 to GND via 0 ohm R390, it is used to make TPS2490 EN pins discharge quickly.

2012/07/24

1. There are two 0.1uF caps C344 and C345 on I2C_C buffer PCA9511 VCC, Change C345 from 0.1uF to 0.01uF, sheet 3

PVT V01: (Andy #6068)

B2012/10/08

- 1.Remove slide switch function from PVT to avoid human switch it to wrong direction, sheet 04.
 - a.) De-populate SW1 and R390.
 - b.) Add 0402 package 0 ohm on location R391.

PVT V02: (Andy #6068)


2012/10/09

1. Add screw hole H5 for plastic CONN protector fix.

PVT V03: (Andy #6068)

2012/10/12

1. Remove screw hole H5, since it is NPTH not PTH, it is no need to have orcad symbol for NPTH, sheet 04.
2. Reserve 47uF/16V ceramic caps, C361, C362, C363, C364 for inrush current improvement, sheet 04.

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