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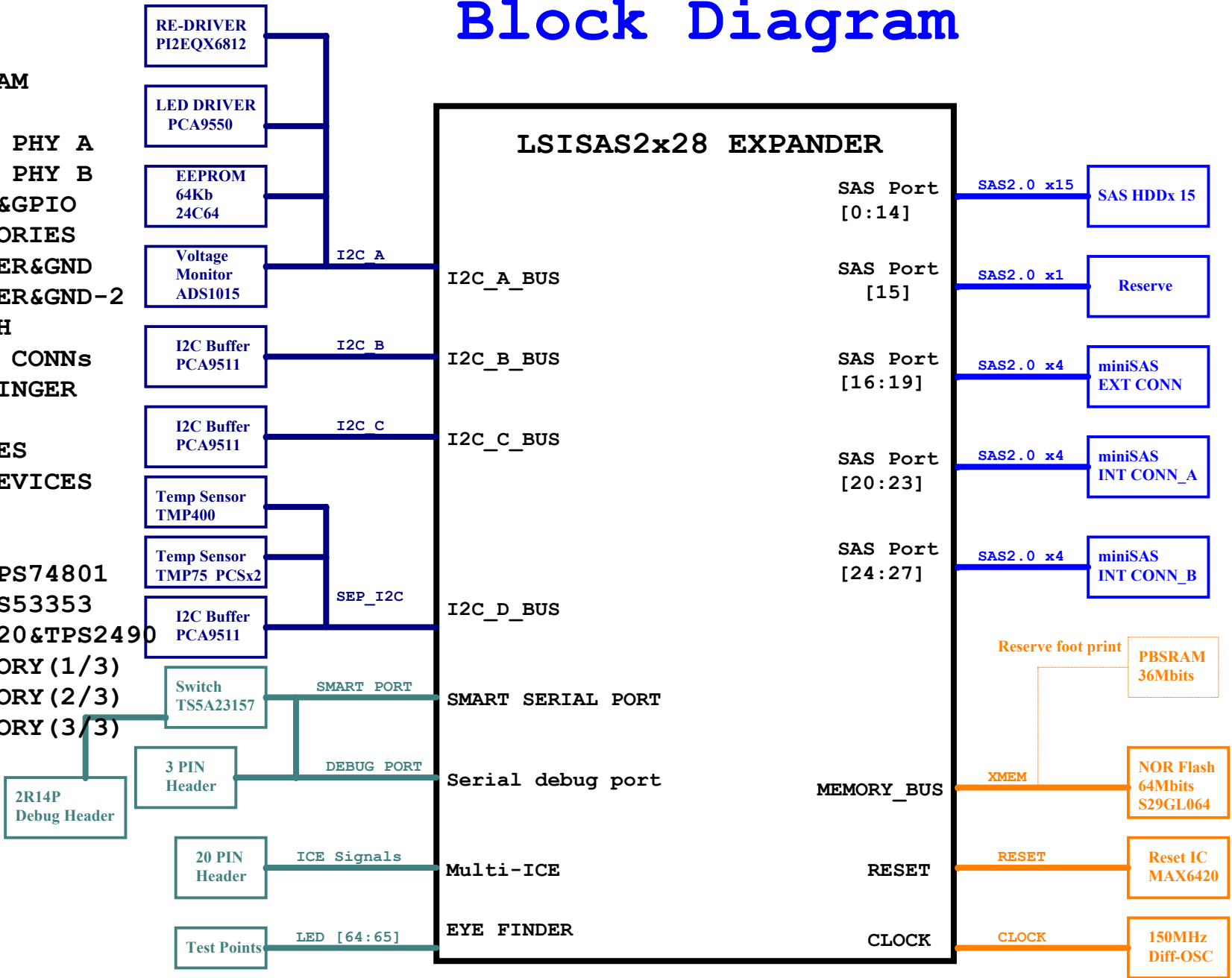
Sheet 18: VCORE 1V\_TPS53353

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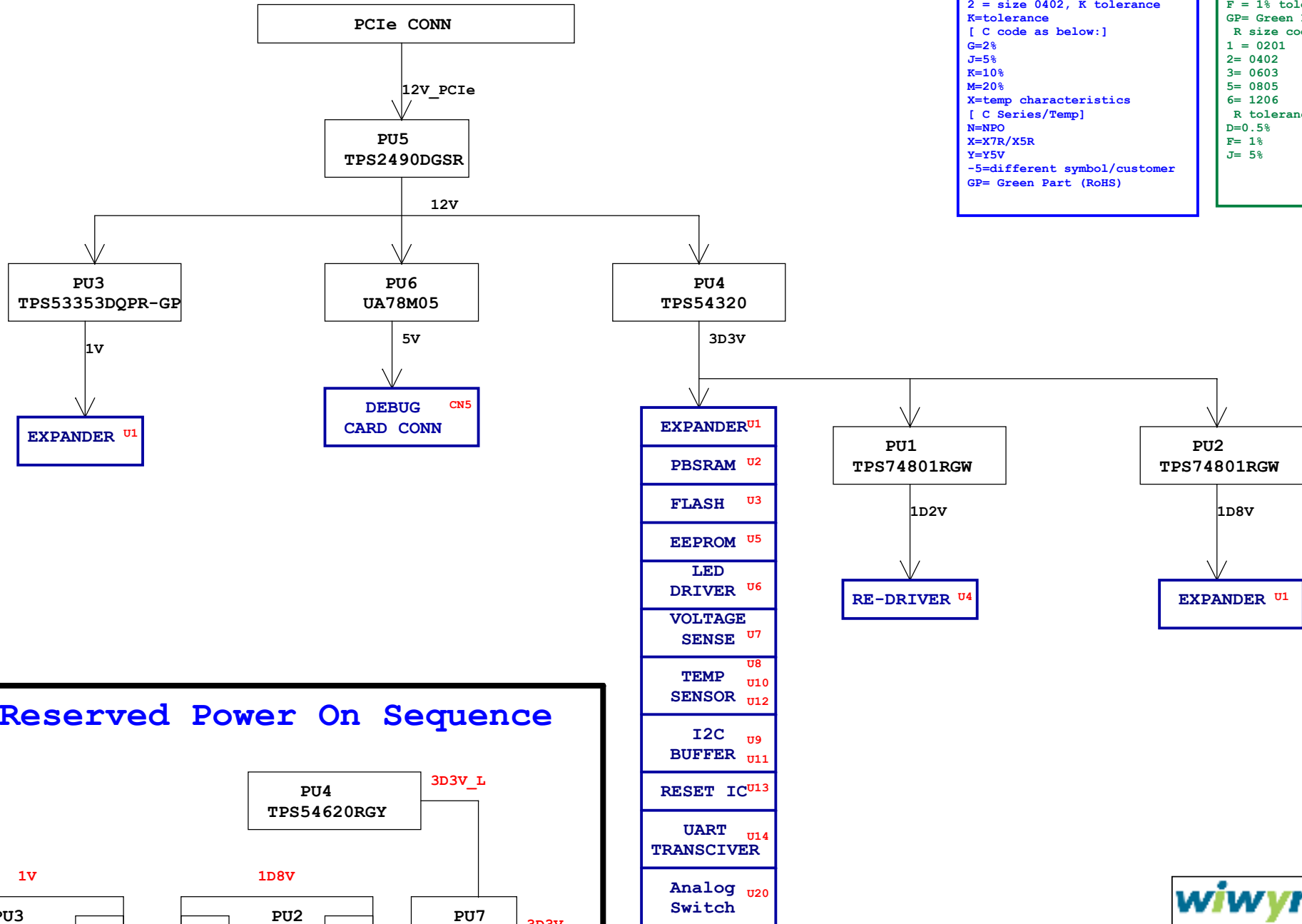
Sheet 20: CHANGE HISTORY(1/3)

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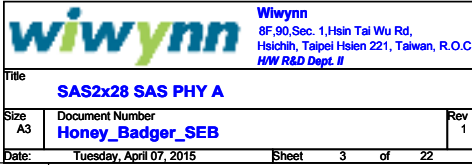
# POWER MAP



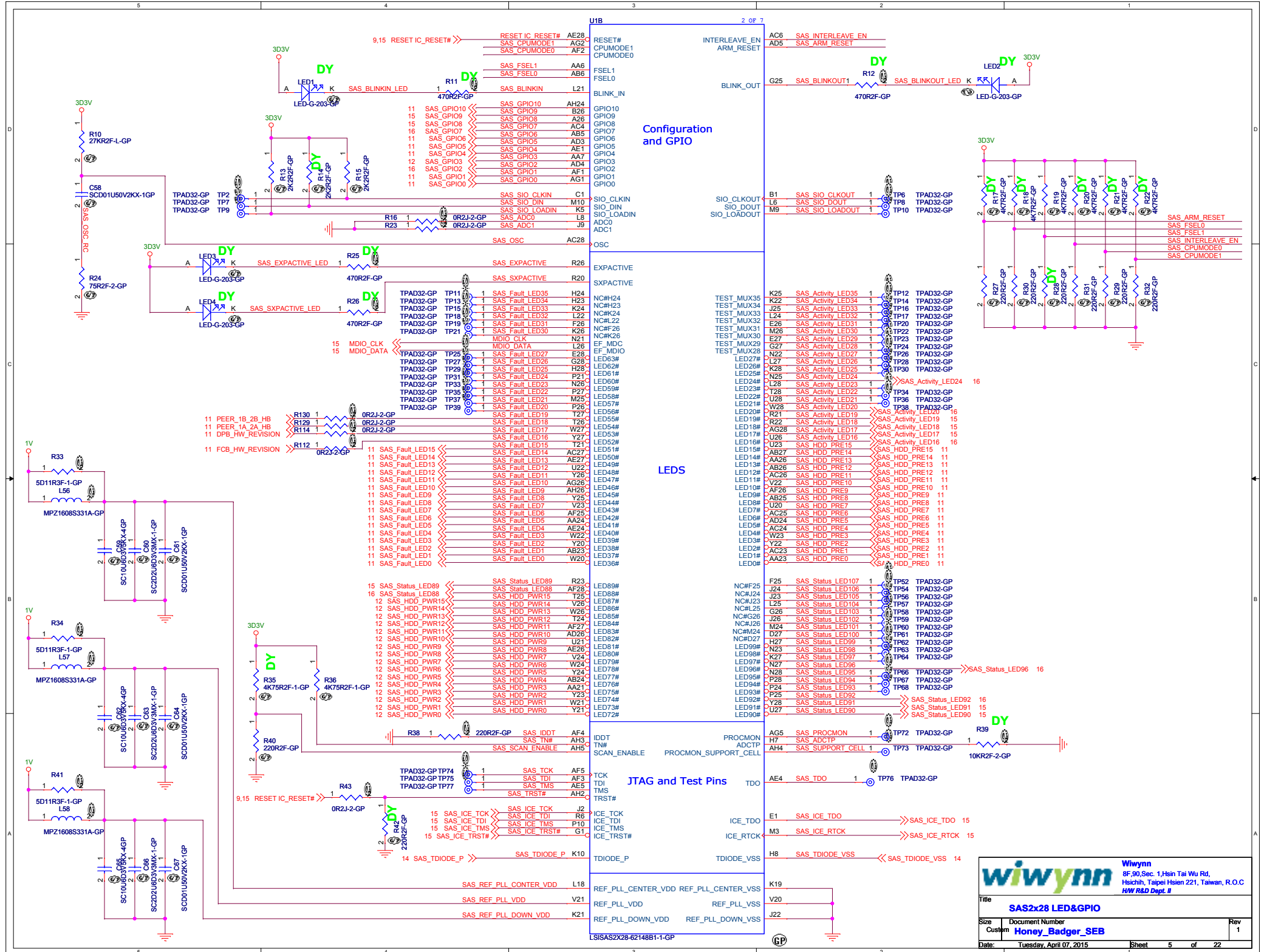
DY=Dummy parts  
=not populated

SCD1U10V2KX-5GP  
D1U = 0.1uF (2D2U means 2.2uF)  
10Voltage (6D3V means 6.3V)  
2 = size 0402, K tolerance  
K=tolerance  
[ C code as below:]  
G=2%  
J=5%  
K=10%  
M=20%  
X=temp characteristics  
[ C Series/Temp]  
N=NPO  
X=X7R/X5R  
Y=Y5V  
-5=different symbol/customer  
GP= Green Part (RoHS)

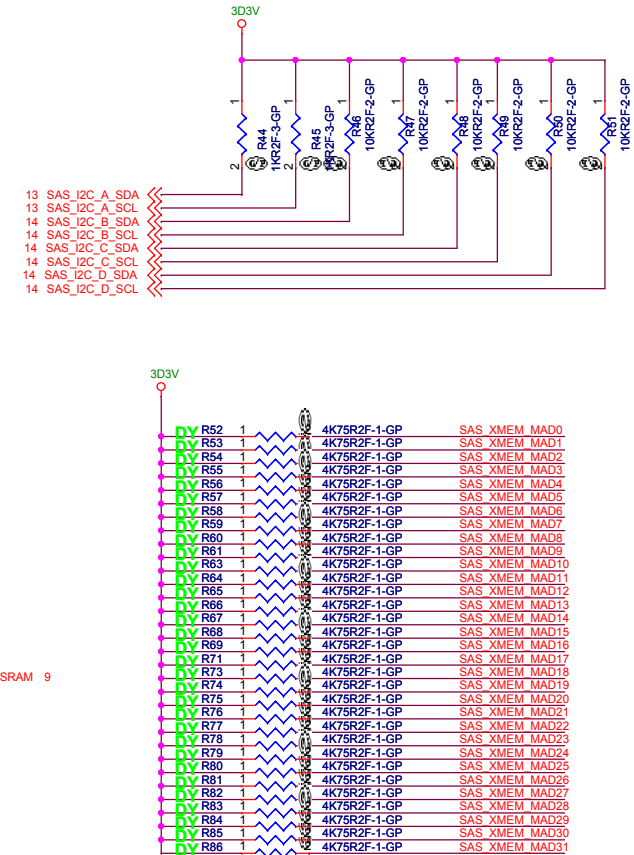
562R2F- GP  
562 = 562 ohm, (2K2R means 2.2K ohm)  
2 = size 0402  
F = 1% tolerance  
GP= Green Part (RoHS)  
R size code as below:  
1 = 0201  
2= 0402  
3= 0603  
5= 0805  
6= 1206  
R tolerance code as below:  
D=0.5%  
F= 1%  
J= 5%



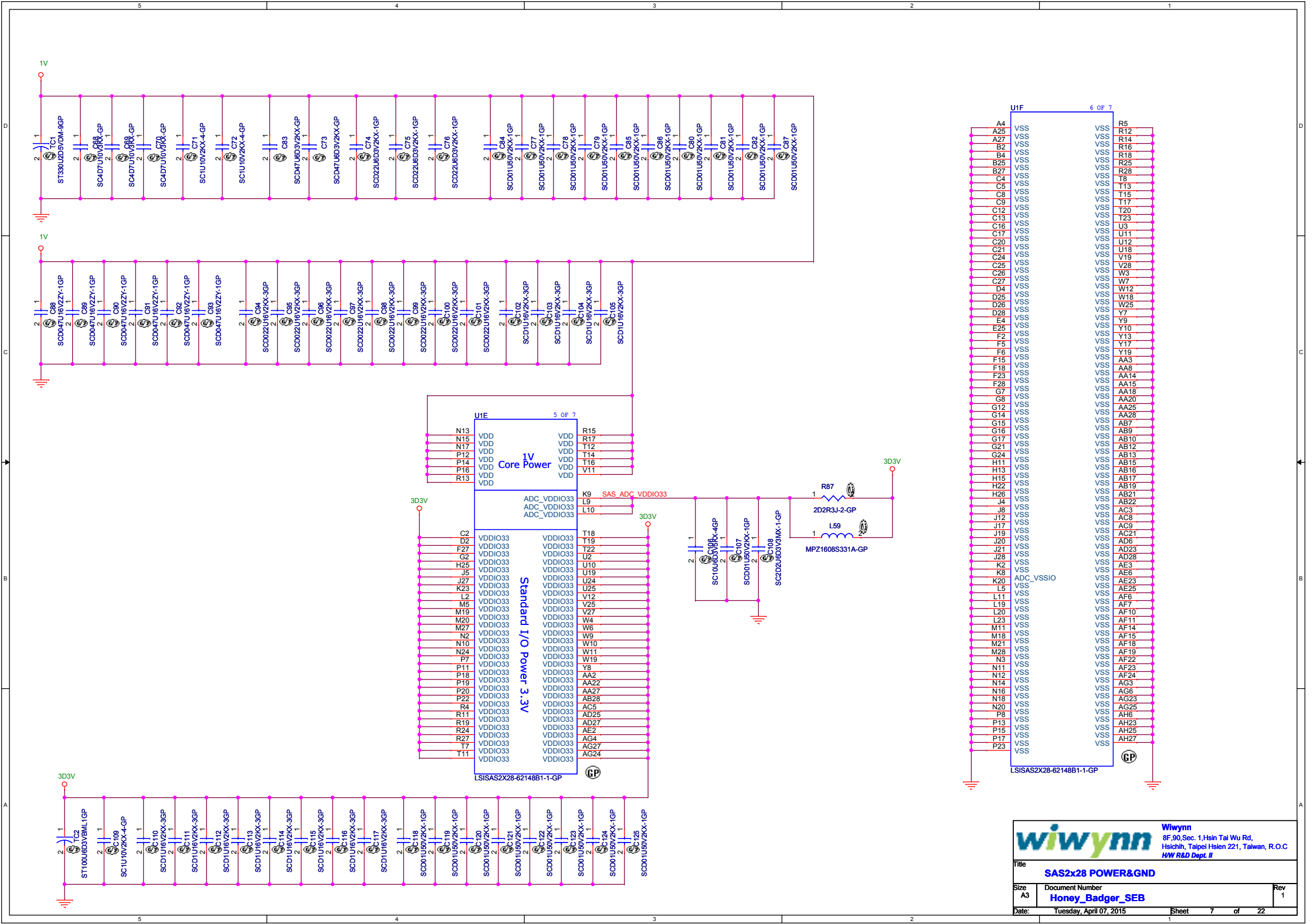


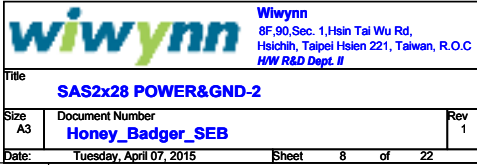


## I2C PULL UP





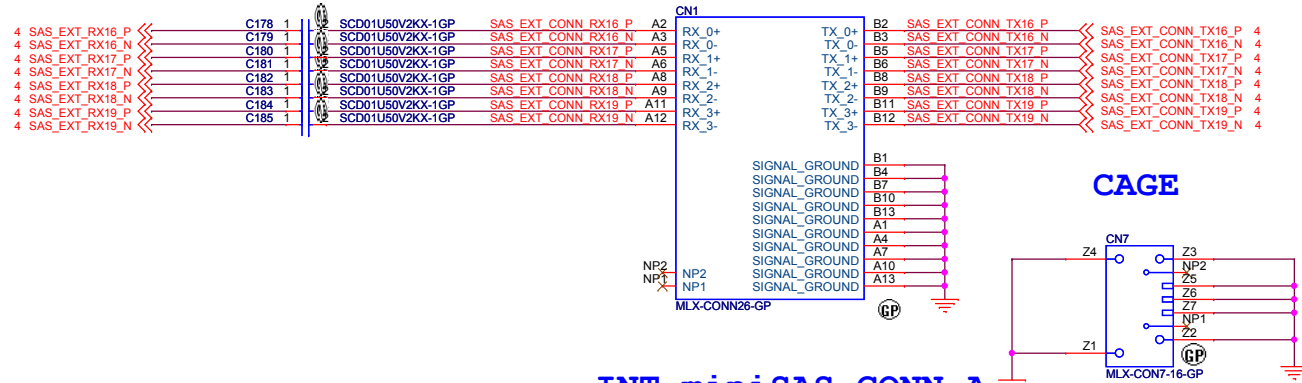




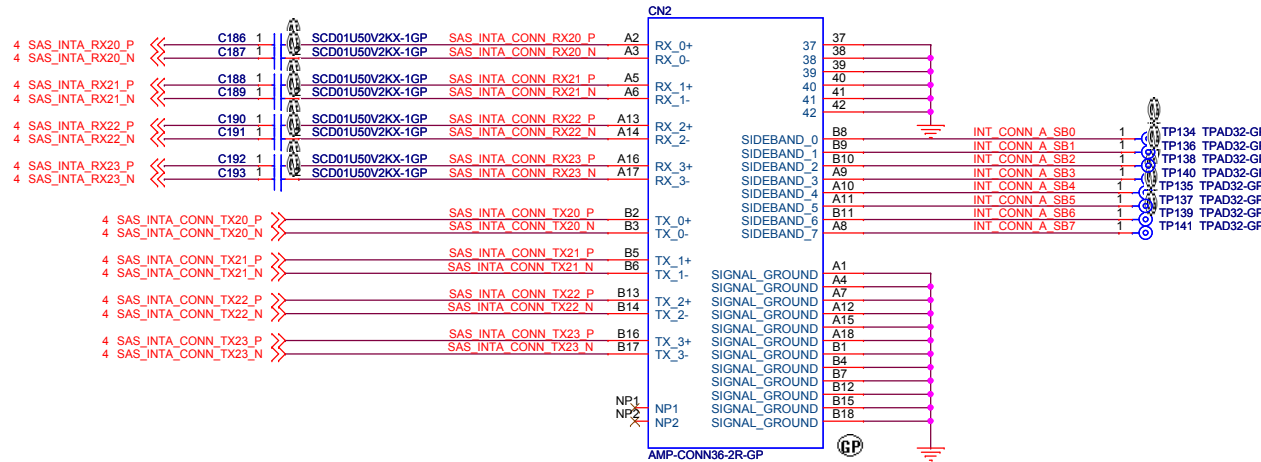




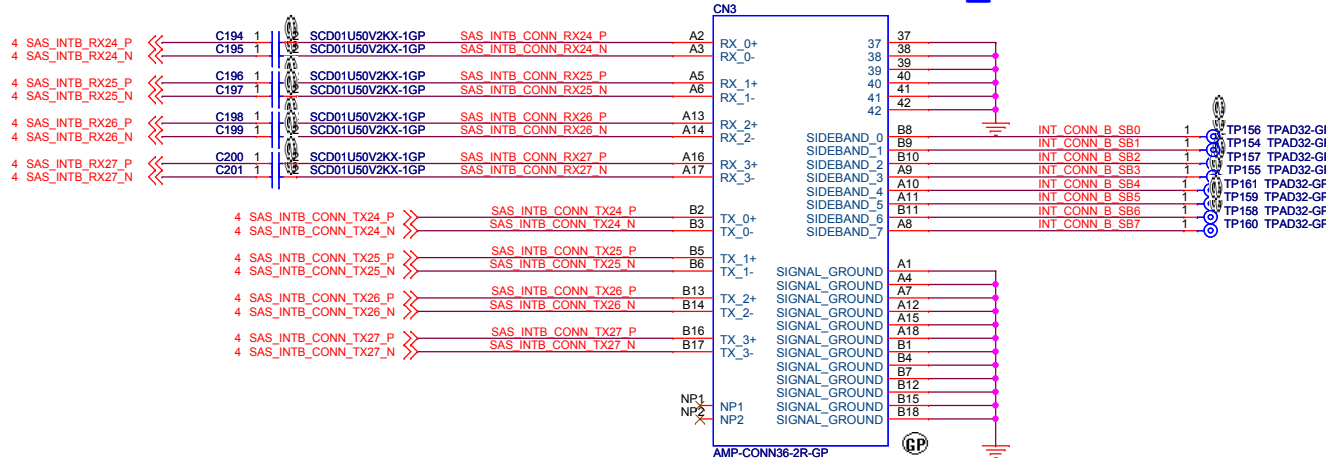
## EXT miniSAS CONN



## INT miniSAS CONN\_A

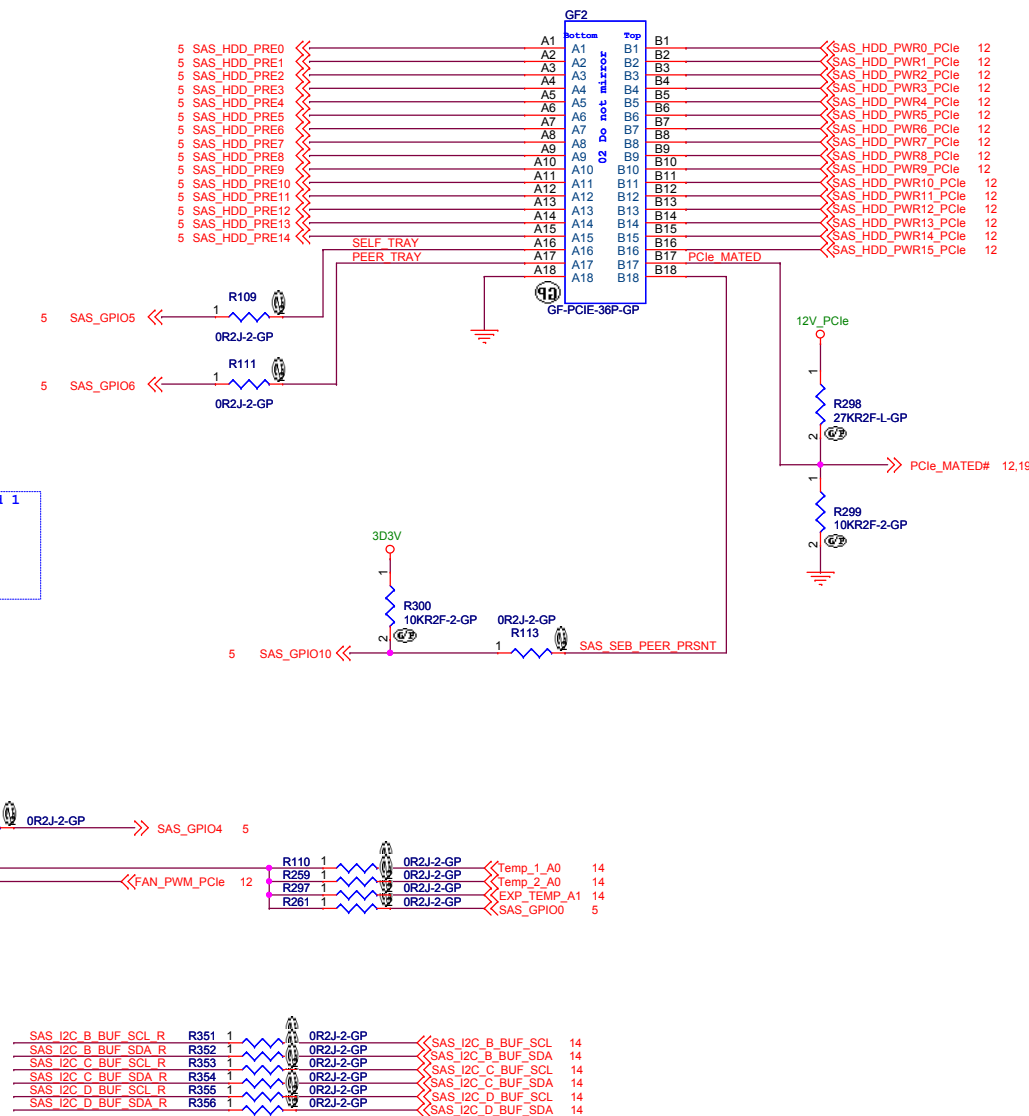
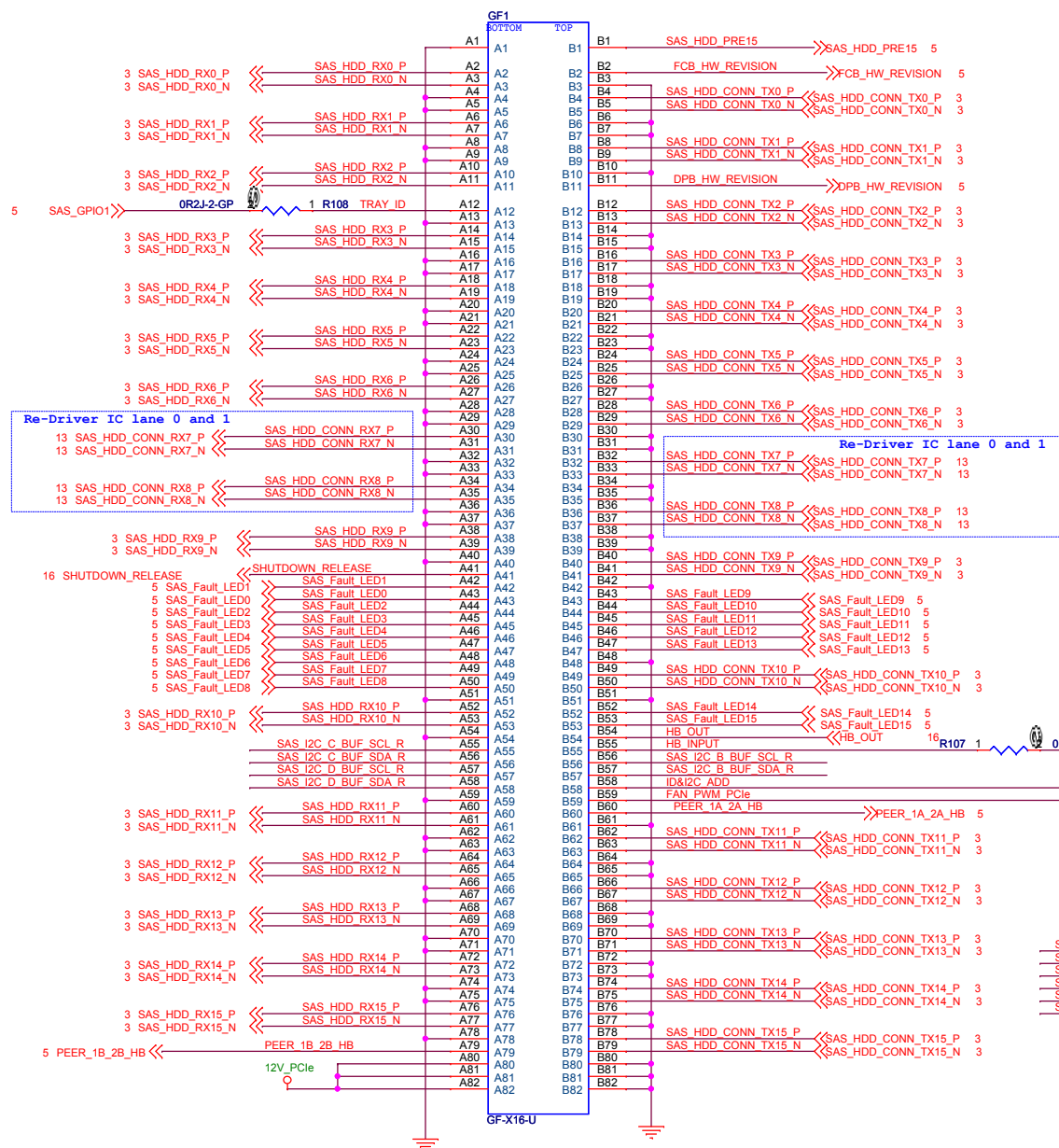


## INT miniSAS CONN\_B

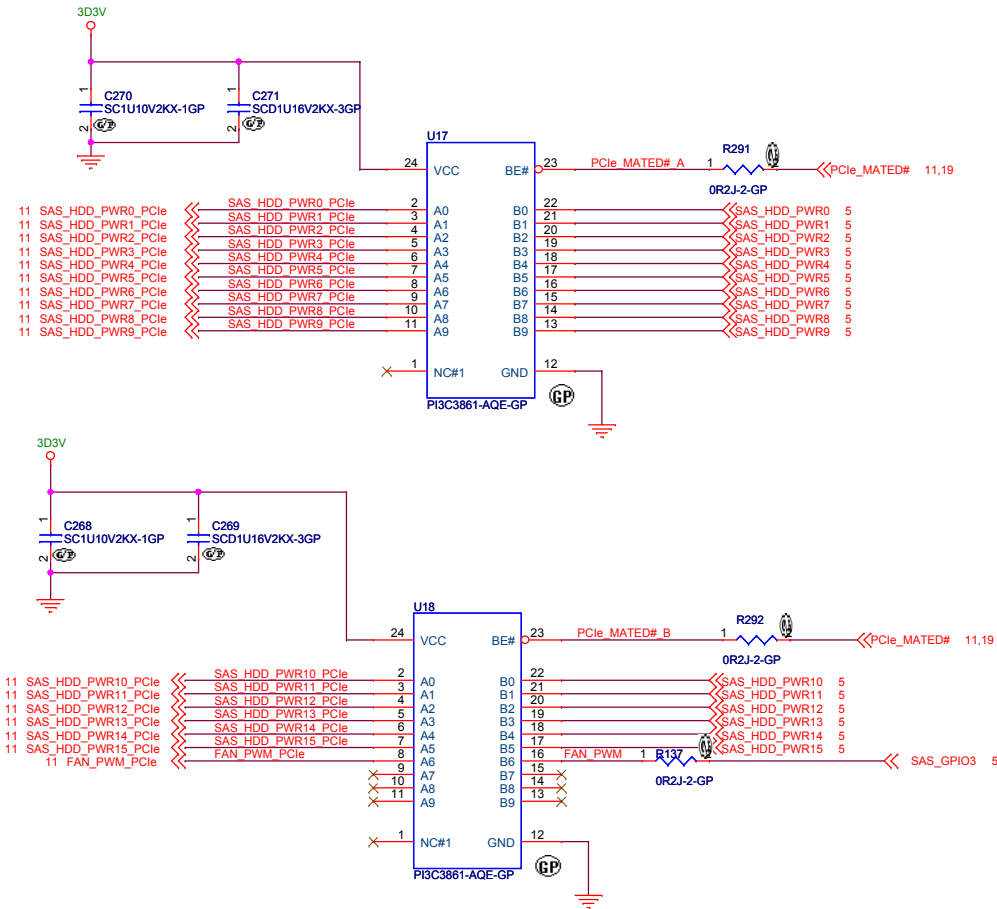


# x16 PCIe 164 PINs GOLDEN FINGER STRADDLE TYPE

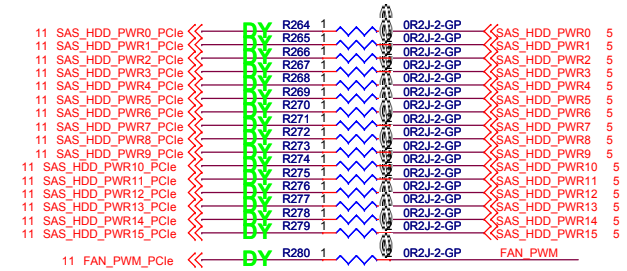
# x1 PCIe 36 PINs GOLDEN FINGER STRADDLE TYPE

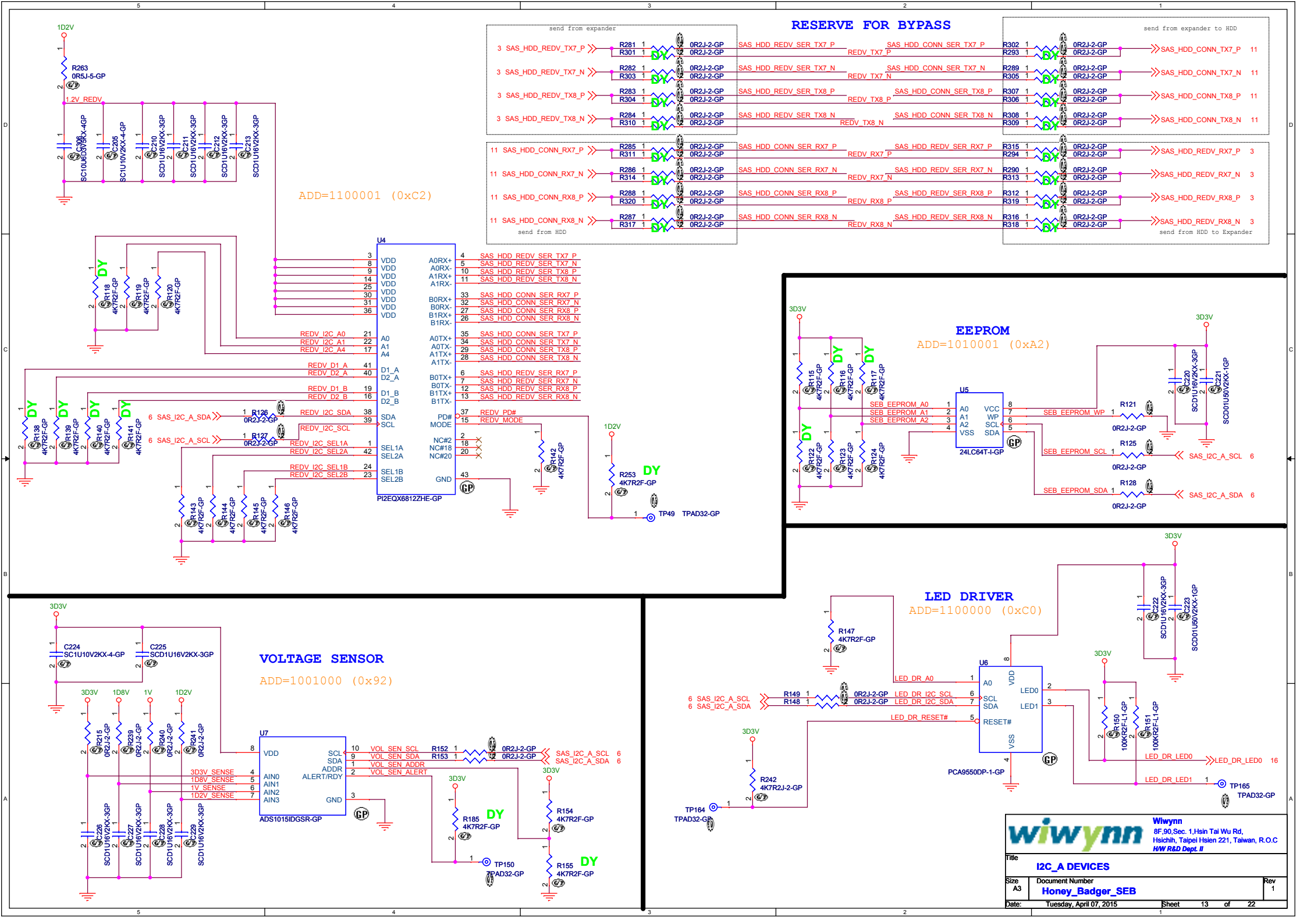


# BUS SWITCH



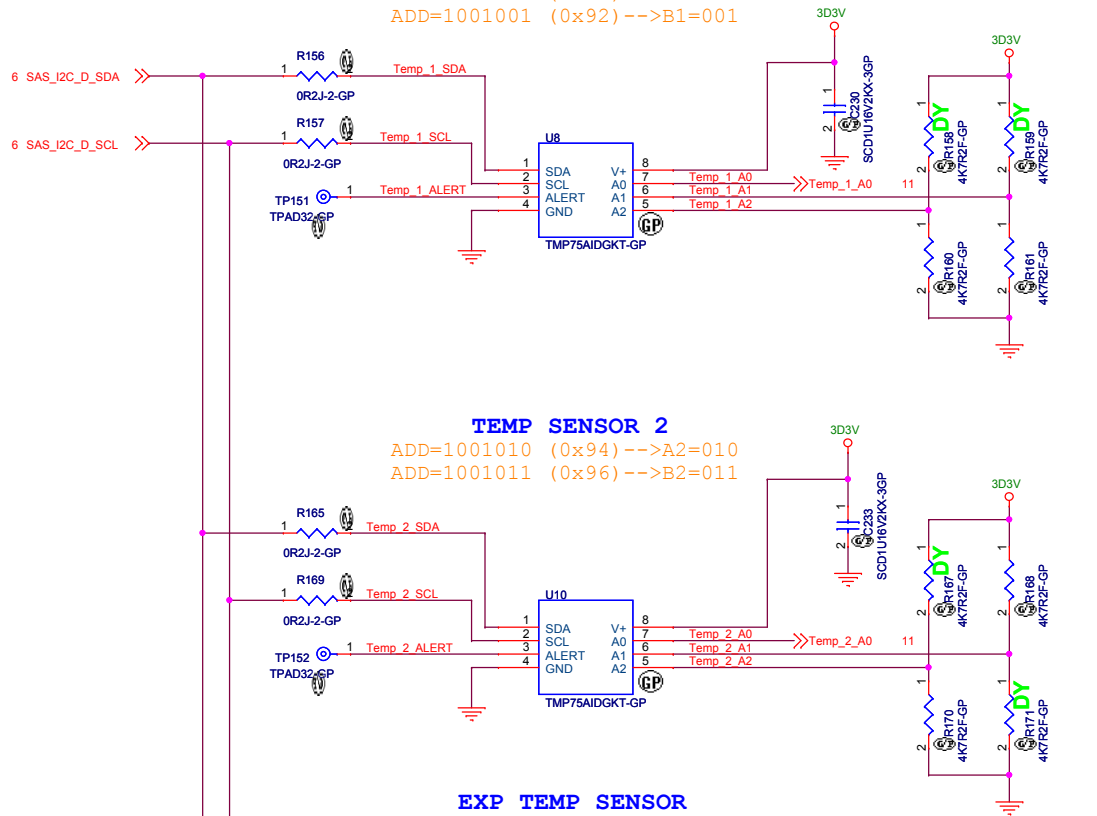
# RESERVE FOR BYPASS





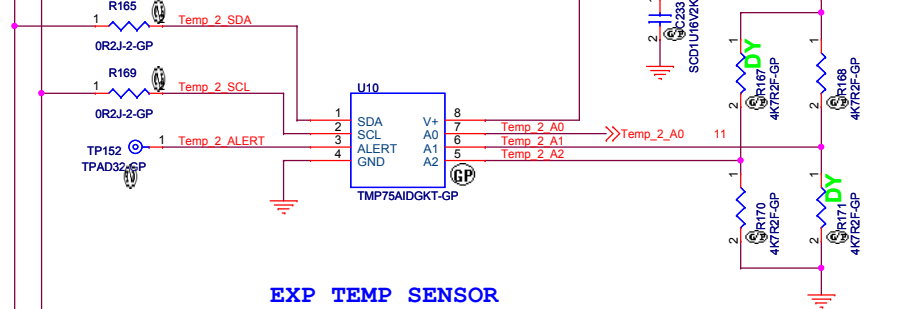
## TEMP SENSOR 1

ADD=1001000 (0x90) --> A1=000  
ADD=1001001 (0x92) --> B1=001



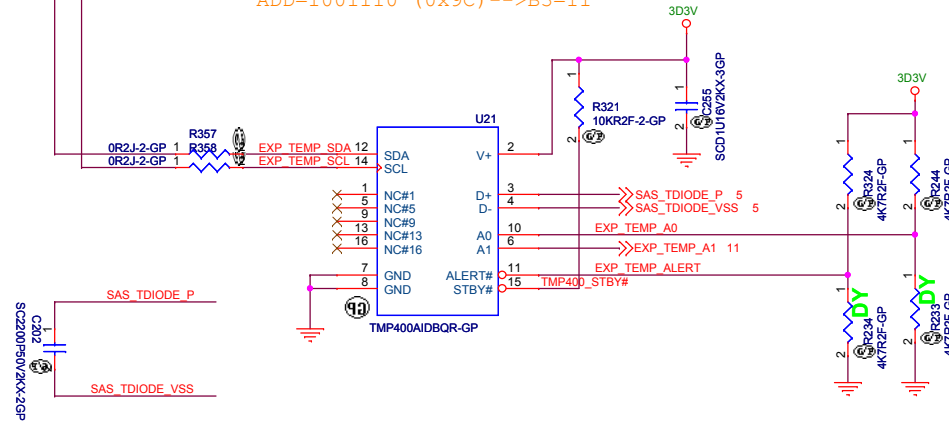
## TEMP SENSOR 2

ADD=1001010 (0x94) --> A2=010  
ADD=1001011 (0x96) --> B2=011

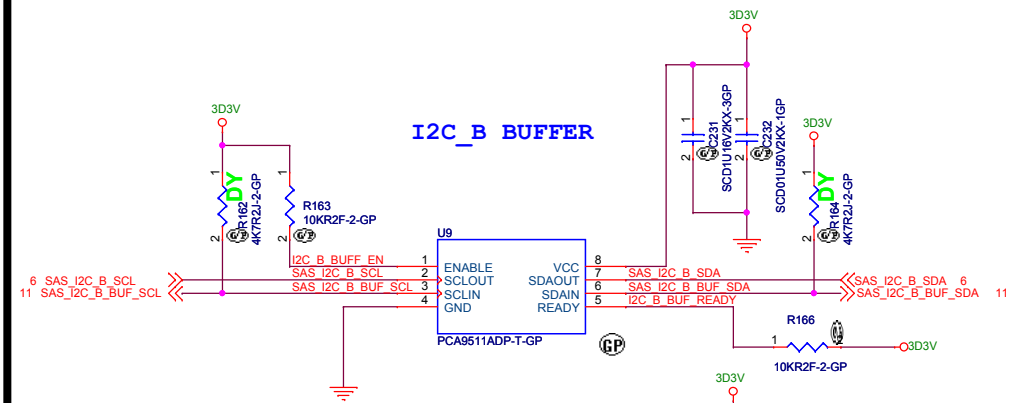


## EXP TEMP SENSOR

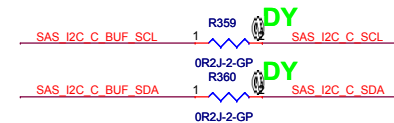
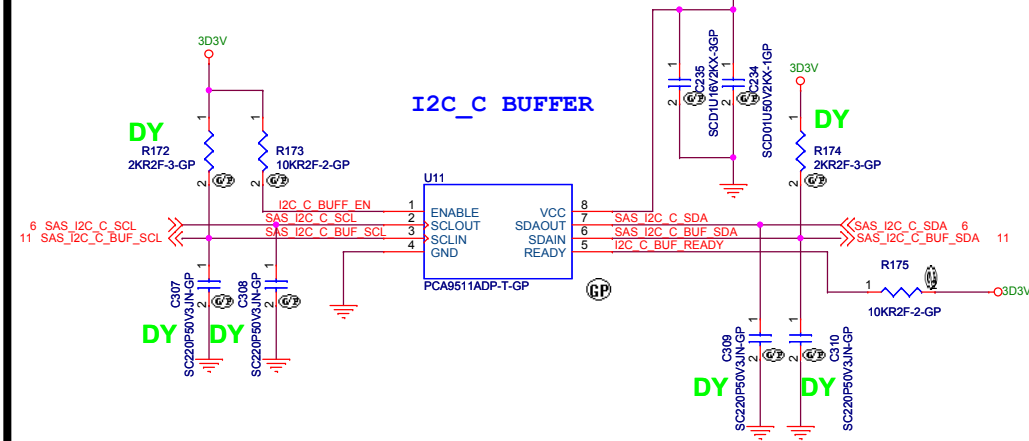
ADD=0011100 (0x98) --> A3=01  
ADD=1001110 (0x9C) --> B3=11



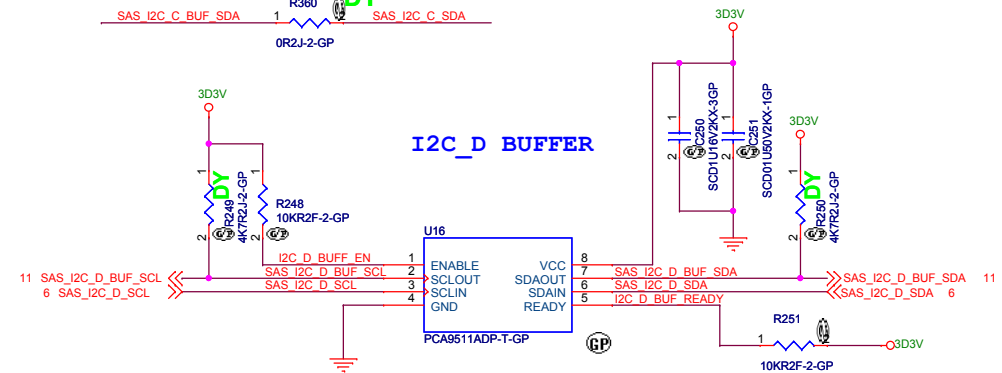
## I2C\_B BUFFER



## I2C C BUFFER

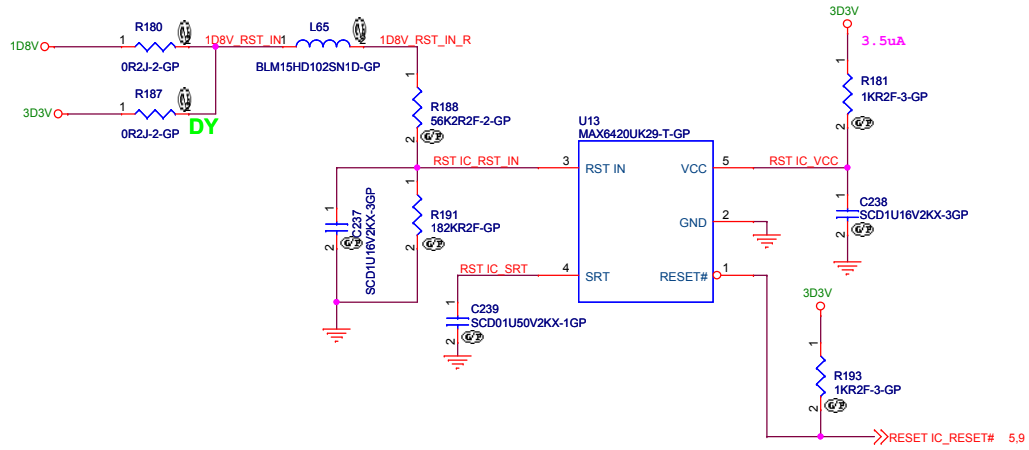


## I2C D BUFFER

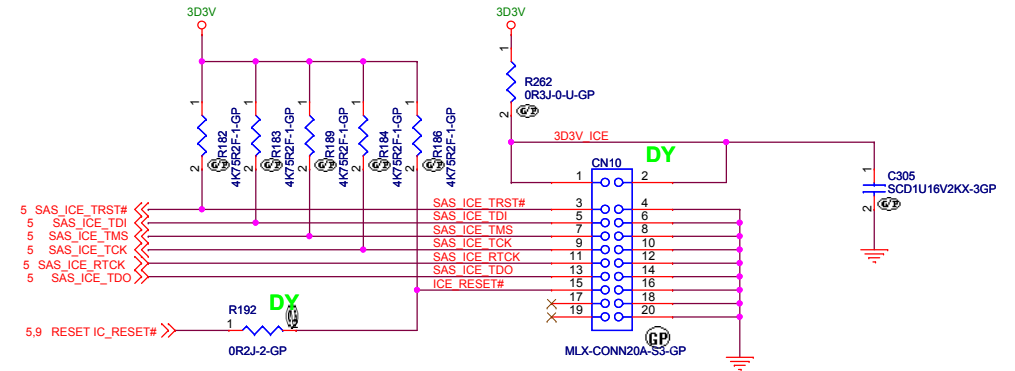




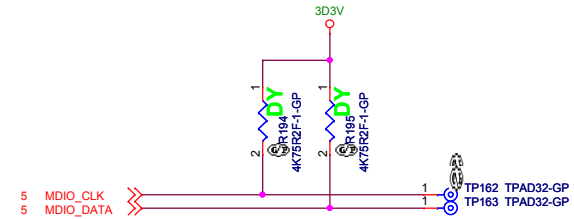
## SYSTEM RESET



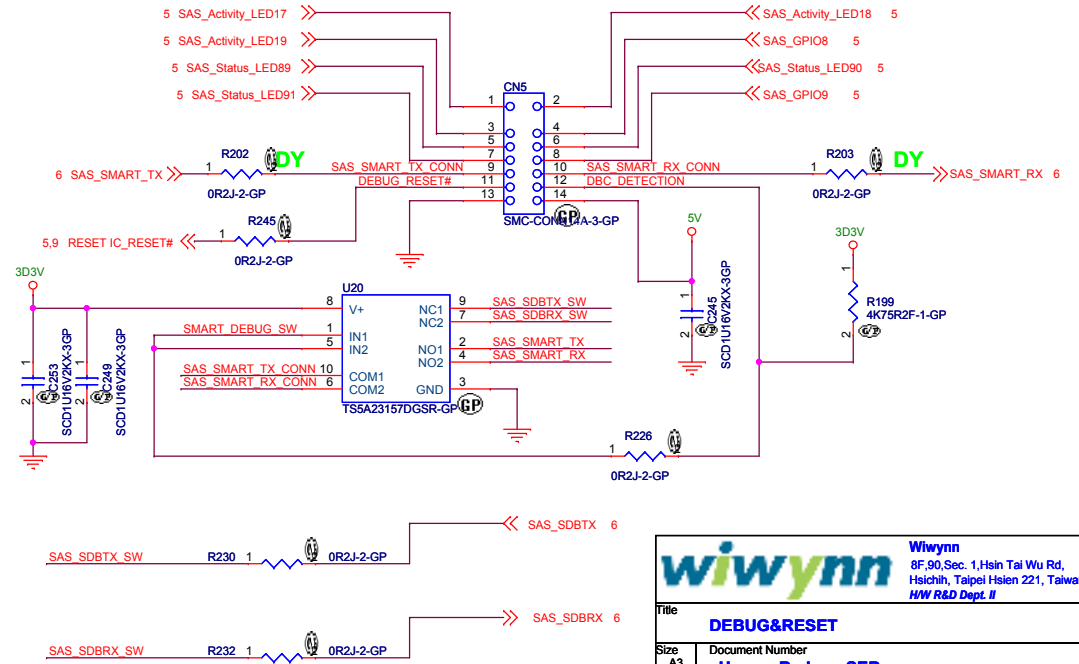
## ICE CONN



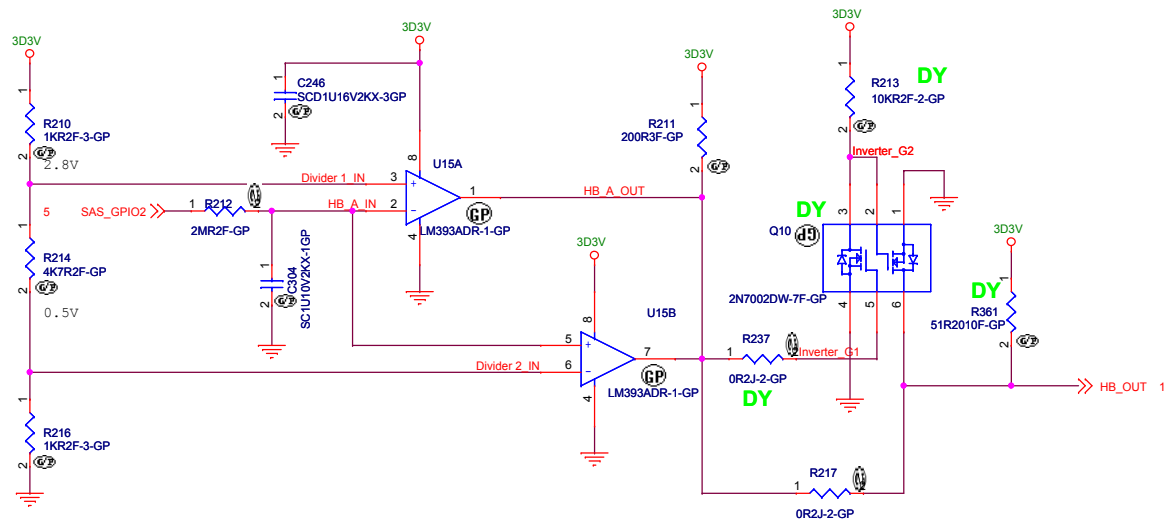
## MDIO EYE FINDER



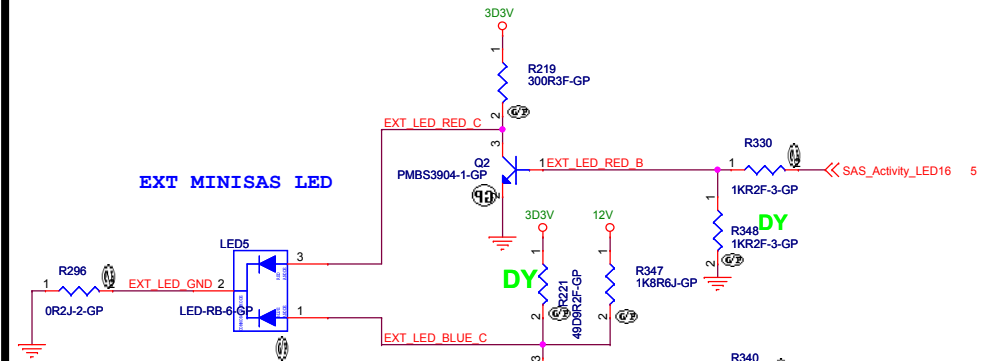
## DEBUG CARD HEADER



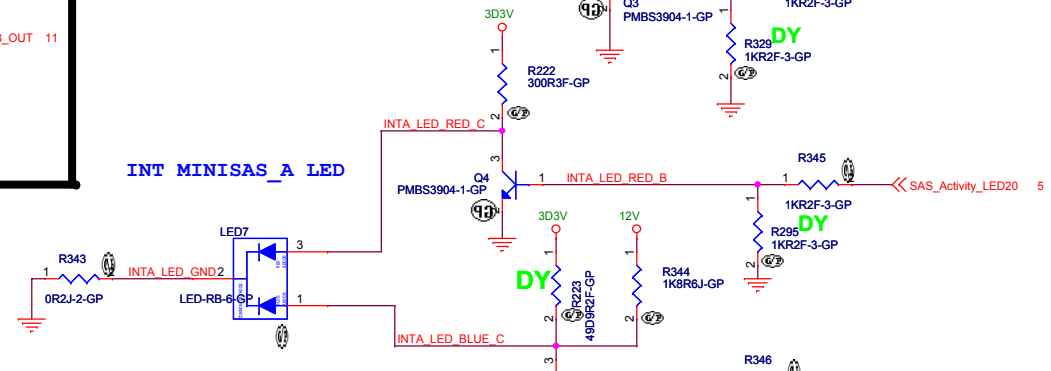
# HEARTBEAT CIRCUITRY



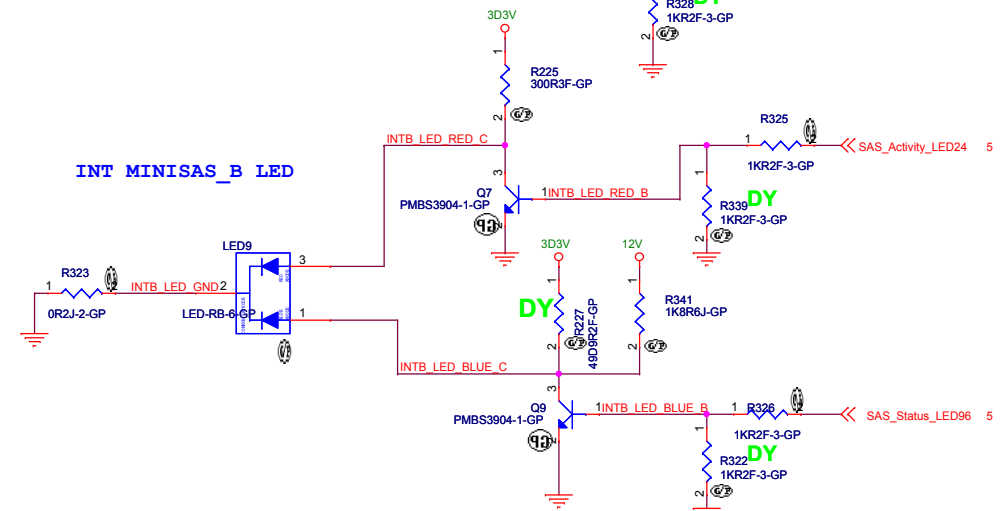
## EXT MINISAS LED



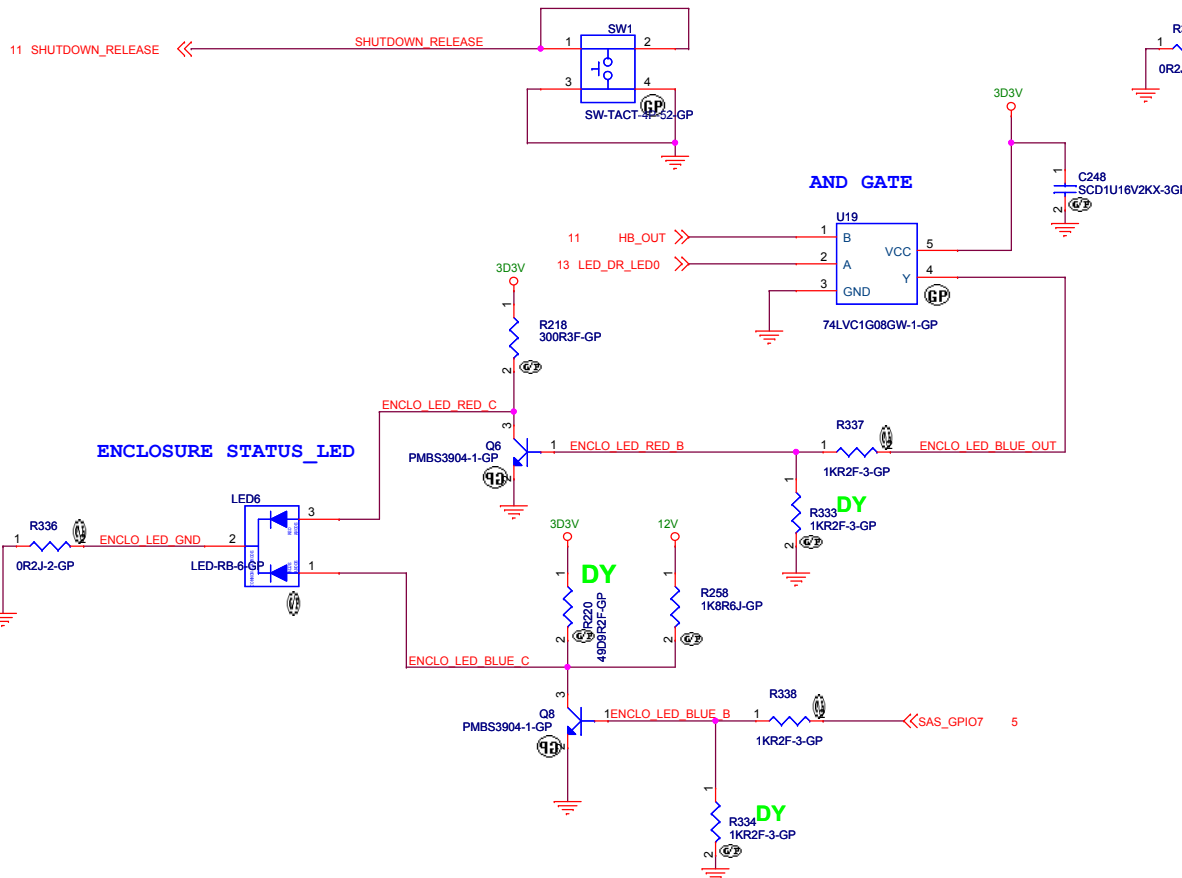
## INT MINISAS\_A LED



## INT MINISAS\_B LED



## ENCLOSURE STATUS\_LED



1D2V LDO TPS74801

The schematic diagram illustrates the 1D2V LDO TPS74801 circuit. The input is 3D3V, connected to the IN#5 pin of the TPS74801RGW-GP. The input is filtered by capacitors PC1 and PC2. The output is connected to the OUT#1 pin and provides a 1D2V output, filtered by capacitors PC3 and PC4. The LDO is configured with a feedback network (PR3, PR6) and a bias network (PR9, PC6). The output is also connected to a 3D3V output through a resistor PR8. The circuit is powered by a 5V\_VREG supply connected to the EN pin.

**1D8V LDO TPS74801**

The schematic shows a 1D8V LDO circuit using the TPS74801. The input is 3D3V, and the output is 1D8V. The circuit includes a 1D8V\_LDO\_PG output, a 1D8V\_FB feedback, and a 1D8V\_SS sense. The LDO is configured with IN#8, IN#7, IN#6, IN#5, EN, SS, BIAS, PG, FB, OUT#18, OUT#19, OUT#20, and OUT#1. The output is labeled 1D8V\_LDO\_PG.

**Component List:**

- PR24: 0R2J-2-GP
- PR58: 0R1206-PAD-1-GP
- PR10: 10KR2F-2-GP
- PR11: 3K57R2F-GP
- PR13: 2K87R2F-1-GP
- PR14: 0R2J-2-GP
- PR15: 20KR2F-L-GP
- PR59: 0R1206-PAD-1-GP
- PC7: SC10U16V6KX-2GP
- PC8: SC10U16V6KX-2GP
- PC9: SC10U6D3V5KX-4GP
- PC10: SC10U6D3V5KX-4GP
- PC11: SCD1U16V2KX-3GP
- PC46: SC470P50V2KX-3GP
- D1: Diode

**Wiwynn**  
8F 30 Sec. 1, Hsin Tai Wu Rd,  
Hsichih, Taipei Hsein 221, Taiwan, R.O.C  
HW R&D Dept. II

**Title:** 1D2V&1D8V\_TPS74801

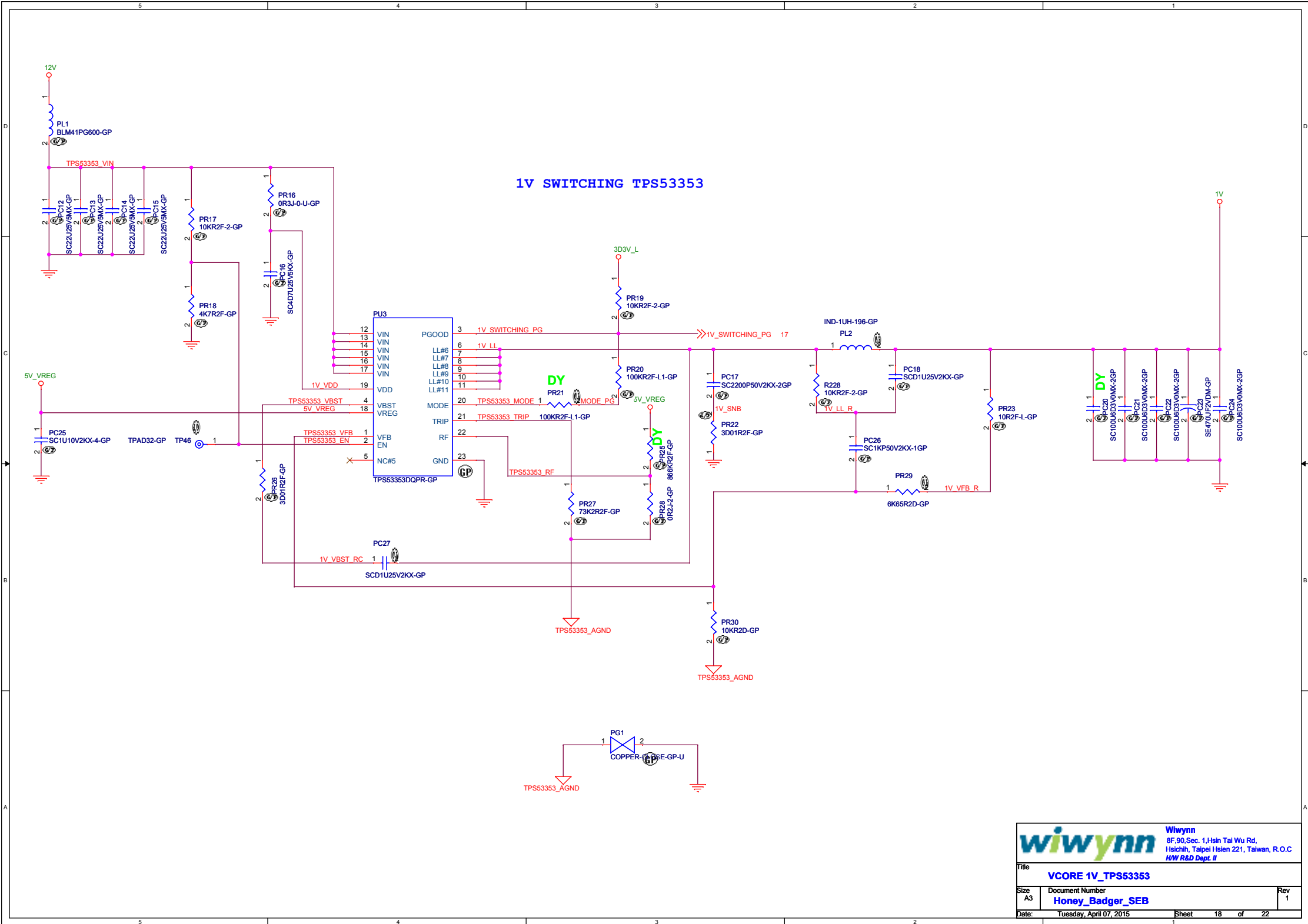
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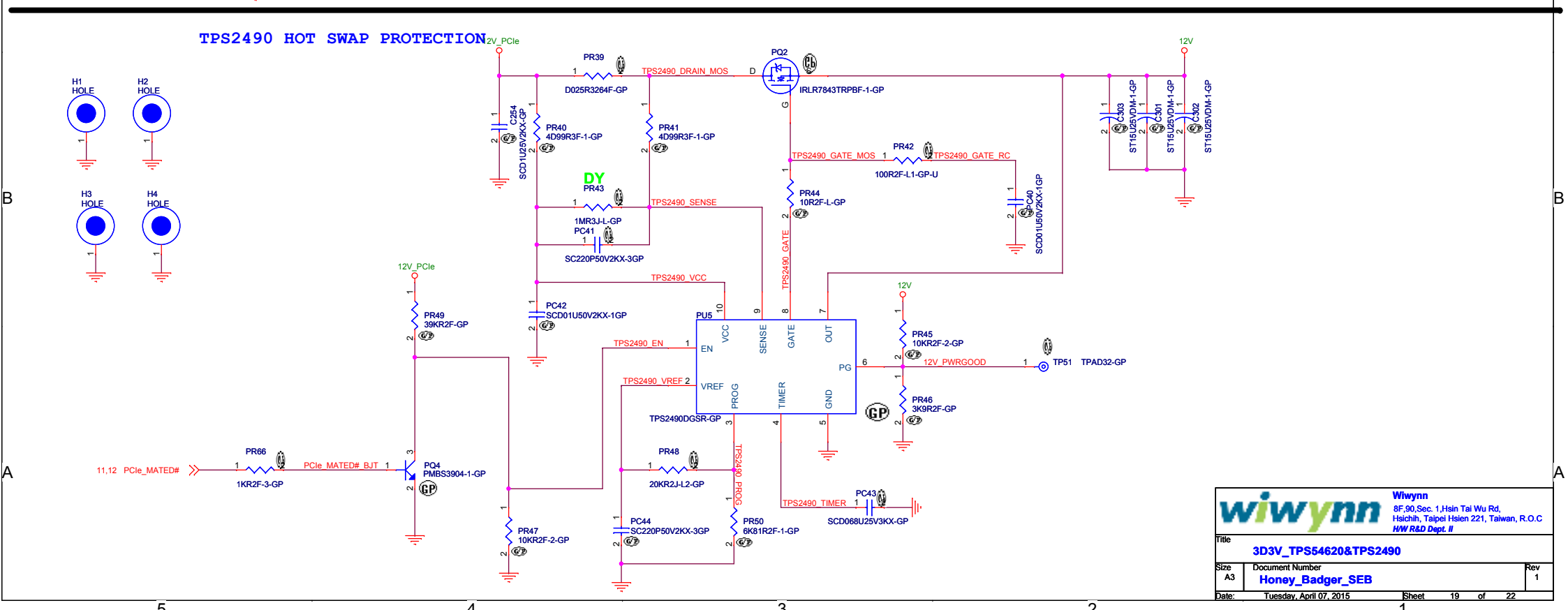
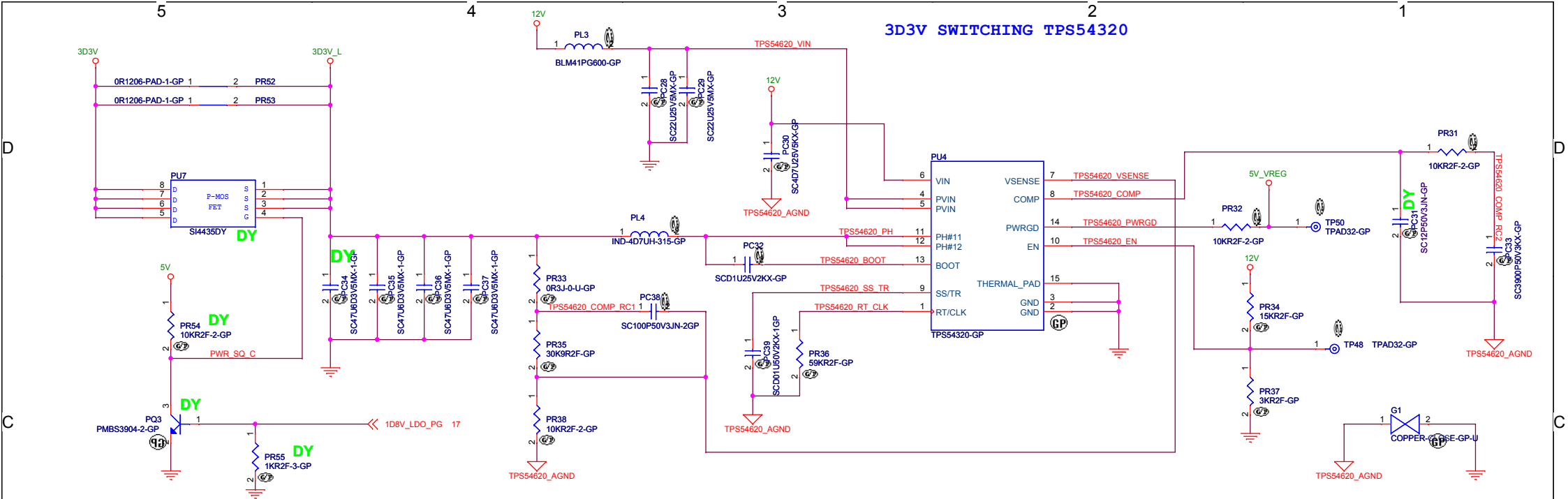
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**Rev:** 1





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