Broadcom J2/Ramon White Boxes components in Distributed Disaggregated Chassis Routing System
Large WAN SP-Class Routers Use Cases
Traditional Modular Chassis Design for Large WAN SP-Class Routers

**Mechanical and Physical Challenge**
- Port Density
- Cabling Density
- Scale Out Growth
- Power Density
- Cooling Density

**Other Considerations:**
- System Timing Distribution and Synchronization Via Electrical PCB traces
- RP to LC Control and Management path via internal Ethernet Switched path or can be via the internal Switch Fabric or other methods → Depend on technology and implementation.
Disaggregated Distributed Chassis Approach for Large WAN SP-Class Routers

Physical Pizza Box View

Functional Block View

Relax Mechanical and Physical Challenge

➢ Realize each Functional Sub-block of the traditional system into a Physical White box to serve that function.

➢ NOS needs to deal with distributed nature of DDC.
Broadcom J2 and Ramon White Box Components For DDC

- **WB1**: 2 RU [40x100G QSFP28] I/O Ports + [13 QSFP-DD 400G] for Fabric Ports – Need to support 4x10G Breakout mode.

- **WB2**: 2 RU [10x400G QSFP-DD] I/O Ports + [13 QSFP-DD 400G] Cell Fabric Connections

- **WB3**: 2 RU [48x400G QSFP-DD] Cell Fabric Connections

- **WB4**: 1 RU [24x400G QSFP-DD] Cell Fabric Connections.
Cell Fabric Connections (CFC):
Distance LIMIT = 100 meters

DAC Option
- 400G DAC Limit (1-3m)

Optical options
- 400G AOC
- 400G Transceiver + Fibers

DDC J2/Ramon WB Elements: [WB1, WB2, WB3, WB4]

WB1 (40x100G QSFP28)
WB2: (10x400G QSFP-DD)
WB3: (48x400G QSFP-DD)
WB4: (24x400G QSFP-DD)

Chassis Beacon/ID = 2 Digit 7 Segment LED

CFC LED Colors { Green, Amber/Red, Blue} Blue for Fabric Port Beaconing

Flush Mount
Sliding Adjustment
Post Depth [26-30 inches]

Craft 2RU
EIA-19"

Craft 1RU
EIA-19"

Target Max Depth = 1762mm = 30.0'
Major Requirements

- The White Boxes in a DDC Routing system are designed to meet TP76200/TP76450 Level 3 requirements operating in a Central Office/Telco environment. Must pass homologation requirements for use in Most of the World (MOW).
  
  - Physical Dimension: 1RU or 2RU, EIA-19”, Max-Depth = 30 in.
  
  - Front Flush Mount, Rear Mount Sliding adjustable For Post Depth from [26-30in]
  
  - Front to Back Air flow – (Port to Power Direction)
  
  - Hot-swappable Fans and PSU modules. 1+1 Redundant/Loadsharing AC or DC PSU – High Efficiency - 80Plus Gold or better.
  
  - Circuitry to support 1588V2 and SyncE, T-TC, T-OC, T-BC support within each WB. ** (DDC Synch). Target Class B.
  
  - OnBoard BMC with dual flash for recoverable remote field upgrade of BMC. **BMC is an optional factory installable option.**
  
  - Intel x86 CPU with TPM (No Coin Cell Battery) and dual flash for recoverable remote field upgrade. Factory Option for different CPU models. Broadwell DE {Model1 8 cores, Model2 6 cores, Model3 4 cores}
  
  - Broadcom [J2 + HBM Deep buffer] + [OP2 for high route Scale] designed to support line rate forwarding on all ports at published minimum packet size.
  
  - Broadcom Ramon for Fabric Whitebox.
NOTE: Cabinet Illustration is to provide a physical mounting context for the application of these WB in AT&T which do not necessarily apply to other customers of service providers.

➢ The key requirement is that the rear mounting needs to be adjustable.
High-Level Block Diagram: WB1 [40x100G] I/O + [13x400G] CFC

Silk Screen Numbering Convention:
Within a grouping
1. \(0,1,2,\ldots,i\)
2. Top to bottom
3. Left to right

I/O Ports in Group 0 support 4x10G break out

I/O Ports Numbering Silk Screen

<table>
<thead>
<tr>
<th>Group 0 Port Numbers</th>
<th>0</th>
<th>2</th>
<th>4</th>
<th>6</th>
<th>8</th>
<th>10</th>
<th>12</th>
<th>14</th>
<th>16</th>
<th>18</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1</td>
<td>3</td>
<td>5</td>
<td>7</td>
<td>9</td>
<td>11</td>
<td>13</td>
<td>15</td>
<td>17</td>
<td>19</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fabric Ports Numbering</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
</tr>
<tr>
<td>1</td>
</tr>
</tbody>
</table>

Example of Associated Port Blocked when a Port is operating as 4x10G

<table>
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<tr>
<th>Group 0 Port Numbers</th>
<th>0</th>
<th>2</th>
<th>4</th>
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</tbody>
</table>

Craft Console OOB/SFP+ Section

20 x 100G QSFP28 I/O Group 0
20 x 100G QSFP28 I/O Group 1

13x400G QSFP-DD Fabric Ports Group 0
High-Level Block Diagram: WB2 [10x400G] I/O + [13x400G] CFC

Silk Screen Numbering Convention:
Within a grouping
1. \(0,1,2,\ldots,i\)
2. Top to bottom
3. Left to right

I/O Ports Numbering Silk Screen

Fabric Ports Numbering

Craft Console OOB/SFP+ Section

10 x 400G QSFP-DD I/O Group 0

13x400G QSFP-DD Fabric Ports Group 0

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High-Level Block Diagram: WB3 [48x400G] CFC

Silk Screen Numbering Convention:
1. \((0,1,2,\ldots,i)\)
2. Top to bottom
3. Left to right

Fabric Ports Numbering Silkscreen

- **0 2 4 6 8 10 12 14 16 18 20 22**
- **1 3 5 7 9 11 13 15 17 19 21 23**
- **24 26 28 30 32 34 36 38 40 42 44 46**
- **25 27 29 31 33 35 37 39 41 43 45 47**

- Retimers used as Necessary for SI
Silk Screen Numbering Convention:
1. \((0,1,2,\ldots,i)\)
2. Top to bottom
3. Left to right

Fabric Ports Numbering Silk Screen

<table>
<thead>
<tr>
<th>0</th>
<th>2</th>
<th>4</th>
<th>6</th>
<th>8</th>
<th>10</th>
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<td>19</td>
<td>21</td>
<td>23</td>
</tr>
</tbody>
</table>

Retimers used as Necessary for SI
Craft/Console Ports

<table>
<thead>
<tr>
<th>Craft Type</th>
<th>QTY</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>Micro USB Serial</td>
<td>1</td>
<td>Console</td>
</tr>
<tr>
<td>RJ-45 USB Serial</td>
<td>1</td>
<td>Console</td>
</tr>
<tr>
<td>USB Port</td>
<td>1</td>
<td>USB data access</td>
</tr>
<tr>
<td>RJ-45 10M/100M/1G</td>
<td>1</td>
<td>Ethernet OOB Powered on Standby Power rail</td>
</tr>
<tr>
<td>10G SFP+ ports</td>
<td>2</td>
<td>For Internal Management Communication for the Modules in a large DDC-RS system</td>
</tr>
</tbody>
</table>

**Console:**

Only one Serial input can be active for the Console. Micro USB will have higher priority than RJ45 when something is connected to Micro USB such as a terminal from a technician. Otherwise, the default is RJ45 which would be connecting to a terminal server by default. The Serial console needs to support default selectable between the BMC or the X86 CPU. (customer-provisioned choice).

**OOB Management:**

The RJ-45 OOB Ethernet management port needs to be operational even when the system is in the shutdown mode. As such it needs to be designed using the standby power rail. It also needs to provide simultaneous connectivity to the X86 CPU and the BMC. The Intel I-210 NIC is specified to use for the RJ-45 OOB Ethernet Management. Design should allow to shutdown Ethernet OOB access to the BMC as needed.

**USB port:**

USB port can be used to provide access to external USB drive for initial system setup or system rebuild. However, for security reasons, once the system is up and running under the control of the NOS, the hardware/firmware of the box needs to provide a mechanism to turn off access to this external USB port. This lock needs to persist even after a cold boot, warm boot, other reset mechanism. To unlock this would require authentication to access the utility to set the registers to unlock it.

**2x10G SFP+:**

These two 10G SFP+ ports provide connections to the X86 Host CPU. This will be used for communication with the Route-Engine Controller of the DDC-RS when this is one component of the larger DDC-RS.
Notes on Cell Fabric Connections (CFC)

- **1 Cell Fabric Connection = 400G QSFP-DD Optics + Fiber (100m Max) + 400G QSFP-DD Optics**

- **1 Cell Fabric Connection = 400G QSFP-DD DAC (1-3m Max)**

- **1 Cell Fabric Connection = 400G QSFP-DD AOC (100m Max)**

Traffic over Fabric Cell Connections are **Fixed sized Cells**

As opposed to the I/O Port traffic which are Ethernet variable length **Packetized format**.

An ingress Ethernet Packet from one I/O port on WBx needs to egress an I/O port on WBy, it is broken up into a number of cells and sprayed over multiple Fabric interfaces toward the Egress WBx where it is reassembled before egressing an I/O port on WBx.

**Note:**

100m Max is a Broadcom Fabric Latency Constraint
Sample Illustrations for a 16T Cluster Configuration:

One “DDC Router”

LEGEND:
- I/O - I/O Ports
- CFC – Cell Fabric Connections
- DDC - Disaggregated Distributed Chassis

Notes on WB1: Can support 10G I/O needs for I/O Ports for MOW.
Supports up to 80x10G. Give up 2x100G ports to get 4x10G ports.
Sample Illustration for a Larger Cluster Configuration:

Different Sized “DDC Router”

Illustrating ALL 13 CFC from each deployed WB Connected to Fabric WB

WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC

WB3- 48x400G QSFP-DD CFC

WP: 0
eds-SW2
48x1/10G SFP + 6x100G QSFP

2x400G QSFP-DD CFC

WB: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC

WB3- 48x400G QSFP-DD CFC

WB3- 48x400G QSFP-DD CFC

WP: 0
eds-SW1
48x1/10G SFP + 6x100G QSFP

2x400G QSFP-DD CFC

WB: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC

Illustrating ALL 13 CFC from each deployed WB Connected to Fabric WB

WP: 0
eds-SW1
48x1/10G SFP + 6x100G QSFP

2x400G QSFP-DD CFC

WB: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB2: 10x400G QSFP-DD I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC
WB1: 40x100G QSFP I/O + 13x400G QSFP-DD CFC

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• Q&A:

• Next session: Timing & Synchronization across DDC
• Q&A:

• Future Discussion:
  Timing & Synchronization across DDC
• Q&A:

• Next session: Timing & Synchronization across DDC